

USER MANUAL

SP-6265

15" Fanless Panel PC
Powered by Intel® Core™
i7/i5/i3 / Pentium® / Celeron®
CPU Processor

SP-6265 M1

SP-6265

15” High Performance Panel PC

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DISCLAIMER

This user’s manual is meant to assist users in installing and setting up the system. The information contained in this document is subject to change without any notice.

CE NOTICE

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

FCC NOTICE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

You are cautioned that any change or modifications to the equipment not expressly approved by the party responsible for compliance could void your authority to operate such equipment.



CAUTION: Danger of explosion may occur when the battery is incorrectly replaced. Replace the battery only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.



WARNING: Some internal parts of the system may have high electrical voltage. And therefore we strongly recommend that qualified engineers can open and disassemble the system. **RESTRICTED ACCESS LOCATION:** access can only be gained by **SERVICE PERSONS** or by **USERS** who have been instructed about the reasons for the restrictions applied to the location and about any precautions that shall be taken; and access is through the use of a **TOOL** or lock and key, or other means of security, and is controlled by the authority responsible for the location.

Contents

Revision History	vi
1 Introduction	1-1
1.1 About This Manual	1-2
2 Getting Started	2-1
2.1 Package List.....	2-2
2.2 System Overview	2-3
2.3 System Specifications	2-6
2.4 Safety Precautions	2-8
3 System Configuration.....	3-1
3.1 External I/O Ports Diagram	3-2
3.2 SP-6265 Bottom I/O Ports Diagram.....	3-2
3.3 Jumper & Connector Quick Reference Table.....	3-3
3.4 Component Locations Of System Main Board.....	3-5
3.4.1 Top View and Jumper Settings of SB-8400	3-5
3.4.2 Bottom View of SB-8400.....	3-7
3.5 How To Set Jumpers	3-8
3.6 Function Buttons and I/O Ports.....	3-10
3.6.1 Power Button	3-10
3.6.2 DC IN Port (For SP-6265Rx-x0x)	3-10
3.6.3 DC IN 3 Pins Terminal Block (For SP-6265Rx-x1x)	3-11
3.7 Setting Connectors and Jumpers.....	3-12
3.7.1 COM Port.....	3-12
3.7.2 COM1 Pin9 and COM2 PIN18 Definition Selection Guide.....	3-14
3.7.3 LAN1, LAN2 Ports.....	3-15
3.7.4 4 x 1 USB 3.0 Connectors	3-17

3.7.5	Digital Input/Output Connector (For SP-6265Rx-x1x) (optional)	3-18
3.7.6	Digital Input/Output Connector (For SP-6265Rx-x0x) (optional)	3-19
3.7.7	DVI-I Port	3-20
3.7.8	HD Audio Connector	3-21
3.7.10	Mini PCI Express Slots	3-22
3.7.11	Serial ATA (SATA) 3.0 Connectors	3-24
3.7.12	SATA Power Connectors.....	3-25
3.7.13	Power Input Connector	3-26
3.7.14	DC Power Input Connector.....	3-26
3.7.15	LVDS Connector	3-27
3.7.16	LVDS Inverter Connector	3-27
3.7.17	PCI Express Slot (x4)	3-28
3.7.18	Control Signal Connector.....	3-29
3.7.19	I2C Wafer (optional).....	3-30
3.7.20	Power Button Connectors	3-30
3.7.21	Panel Power LED Connector.....	3-31
3.7.22	Panel HDD LED Connector	3-31
3.7.23	Low Pin Count (LPC) Connector	3-32
3.7.24	CompactFlash Card Connector	3-33
3.7.25	SIM Card Slot.....	3-34
3.7.26	Hardware Power Failure Selection	3-34
3.7.27	CFast Voltage Control Selection.....	3-35
3.7.28	LVDS Resolution Selection	3-36
3.7.29	LVDS Voltage Selection	3-39
3.7.30	Flash Descriptor Override Selection	3-39
3.7.31	VCCIO Voltage Selection.....	3-40
3.7.32	BIOS Recovery Mode Selection	3-40

3.7.33	Clear CMOS Data Selection	3-41
3.8	TOUCH CONTROL BOARD SR-6145 CONNECTOR QUICK REFERENCE TABLE	3-42
3.9	TOUCH CONTROL BOARD SR-6145 COMPONENT LOCATIONS	3-42
3.9.1	Touch Control Board SR-6145 Top View	3-42
3.10	SETTING TOUCH CONTROL BOARD SR-6145 CONNECTORS AND JUMPERS	3-43
3.10.1	Control Signal Connector.....	3-43
3.10.2	LVDS Panel Signal Connector (Connected To Motherboard) 3-44	
3.10.3	LVDS Panel Signal Connector (Connected To LCD Panel) 3-45	
3.10.4	15" Panel LED Backlight Control Connector	3-46
3.10.5	Touch Panel Connector	3-46
3.10.6	LED1 Connector	3-47
3.10.7	Touch Panel Up Signal Setting	3-48
3.10.8	Touch Panel Low Signal Setting	3-49
4	Software Utilities	4-1
4.1	Introduction.....	4-2
4.2	Installing Intel® Chipset Software Installation Utility.....	4-4
4.2.1	Introduction	4-4
4.2.2	Intel® Chipset Software Installation Utility.....	4-4
4.3	Intel® Management Engine Components Installer Installation ...	4-5
4.4	Intel® USB 3.0 eXtensible Host Controller Utility	4-7
4.5	Installing Intel RST Driver Utility (Only for Q170, Optional)	4-8
4.6	Intel® RapidStorage Technology Option ROM	4-8

4.7	Installing Graphics Driver Utility	4-10
4.8	Installing LAN Driver Utility.....	4-11
4.9	Installing Sound Driver Utility	4-12
4.10	Installing Touch Driver Utility	4-12
4.11	Microsoft Hotfix kb3211320 and kb3213986 Driver installation	4-13
5	BIOS SETUP	5-1
5.1	Introduction.....	5-2
5.2	Accessing Setup Utility.....	5-3
5.3	Main.....	5-6
5.4	Advanced	5-8
5.4.1	Advanced – CPU Configuration.....	5-9
5.4.2	Advanced – PCH-FW Configuration	5-11
5.4.3	Advanced – Trusted Computing	5-12
5.4.4	Advanced – ACPI Settings.....	5-13
5.4.6	Advanced – SATA Configuration.....	5-14
5.4.7	Advanced – Hardware Monitor	5-15
5.4.8	Advanced – F81866 Watchdog Configuration	5-16
5.4.9	Advanced – F81866 Super IO Configuration.....	5-17
5.4.10	Advanced – Network Stack Configuration	5-23
5.4.11	Advanced – USB Configuration	5-25
5.5	Chipset.....	5-26
5.5.1	Chipset – System Agent (SA) Configuration.....	5-27
5.5.2	Chipset – PCH-IO Configuration.....	5-32
5.6	Security	5-38

5.7	Boot	5-40
5.7.1	Boot – Network Device BBS Priorities	5-42
5.7.2	Boot – Hard Drive BBS Priorities	5-43
5.7.3	Boot – CSM Configuration	5-44
5.8	Save & Exit.....	5-46
Appendix A System Diagrams		A-1
	SP-6265 Equipped Module PC Exploded Diagram	A-2
	SP-6265 LCD Display Exploded Diagram.....	A-3
	SP-6265 Module PC Exploded Diagram	A-5
	SP-6265 1st Memory Slot & CPU Installation Exploded Diagram	A-7
	SP-6265 2nd Memory Slot and SSD Installation Exploded Diagram.....	A-8
	SP-6265 Panel Mount Exploded Diagrams.....	A-9
Appendix B Technical Summary		B-1
	System Block Diagram	B-2
	Interrupt Map	B-3
	I/O Map	B-20
	Memory Map.....	B-23
	Configuring WatchDog Timer	B-25
	Flash BIOS Update.....	B-27

Revision History

The revision history of SP-6265 User Manual is described below:

Version No.	Revision History	Page No.	Date
M1	Initial Release	-	2018/01/22

1

Introduction

This chapter provides the introduction for the SP-6265 system as well as the framework of the user manual.

The following topic is included:

- About This Manual

1.1 About This Manual

Thank you for purchasing our SP-6265 system. The SP-6265 is an updated system designed to be comparable with the highest performance of IBM AT personal computers. The SP-6265 provides faster processing speed, greater expandability and can handle more tasks than before. This manual is designed to assist you how to install and set up the whole system. It contains 5 chapters and 2 appendixes. Users can configure the system according to their own needs. This user manual is intended for service personnel with strong hardware background. It is not intended for general users.

The following section describes the structure of this user manual.

Chapter 1 Introduction

This chapter introduces the framework of this user manual.

Chapter 2 Getting Started

This chapter describes the package contents and system specifications, and illustrates the physical appearances for the SP-6265 system. Read the safety reminders carefully on how to take care of your system properly.

Chapter 3 System Configuration

This chapter describes the locations and functions of the system motherboard components. You will learn how to properly configure the connectors and system configuration jumpers on the motherboard and configure the system to meet your own needs.

Chapter 4 Software Utilities

This chapter contains helpful information for proper installations of the Intel Chipset Software Installation Utility, Intel Management Engine Components Installer Driver Utility, Intel USB 3.0 Extensible Host Controller Driver Utility, Graphics Driver Utility, LAN Driver Utility, Sound Driver Utility, KMDF Driver Utility (Windows 7 Only), Intel RST Driver Utility (optional, only for Q170 SKU), Touch Driver Utility and Hotfix Driver Utility.

Chapter 5 BIOS Setup

This chapter provides BIOS setup information.

Appendix A System Assembly Diagrams

This appendix provides the exploded diagrams and part numbers of the SP-6265.

Appendix B Technical Summary

This appendix provides the information about the system block diagram, allocation maps for system resources, Watchdog Timer Configuration and Flash BIOS Update.

2 Getting Started

This chapter provides the information for the SP-6265 system. It describes how to set up the system quickly and outlines the system specifications.

The following topics are included:

- Package List
- System Overview
- System Specification
- Safety Precautions

Experienced users can go to Chapter 3 System Configuration on page 3-1 for a quick start.

2.1 Package List

If you discover any of the items listed below are damaged or lost, please contact your local distributor immediately.

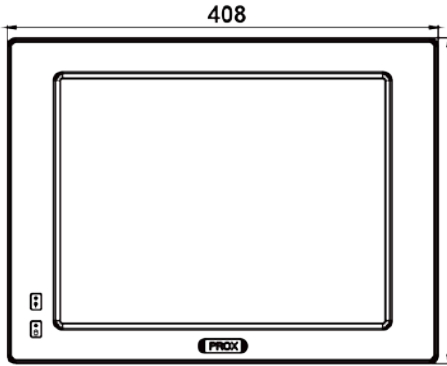
Item	Q'ty
SP-6265	1
Manual / Driver DVD	1
Quick Guide	1

2.2 System Overview

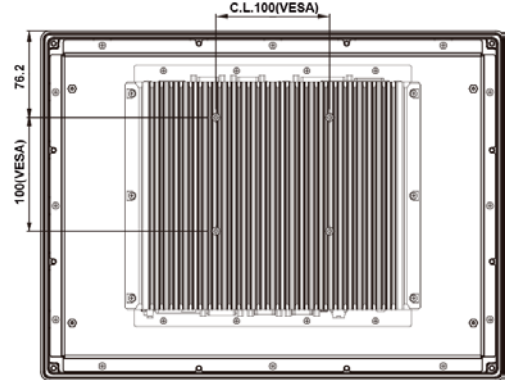
Unit: mm

SP-6265

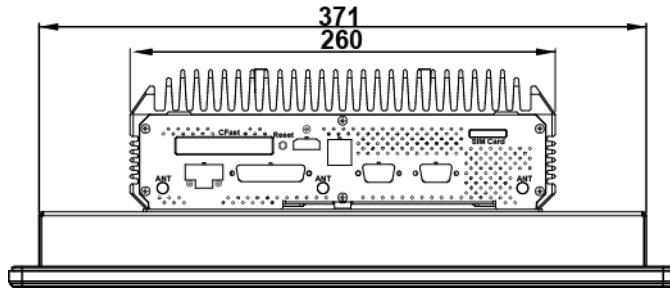
Front View



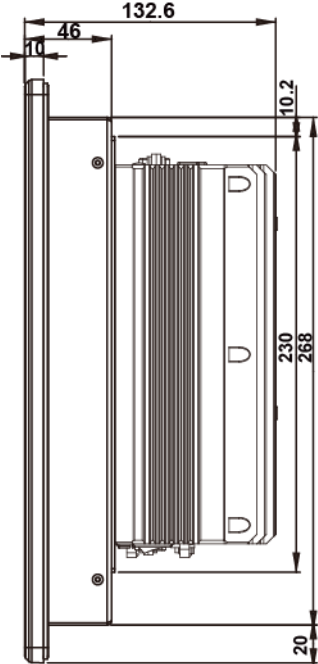
Rear View



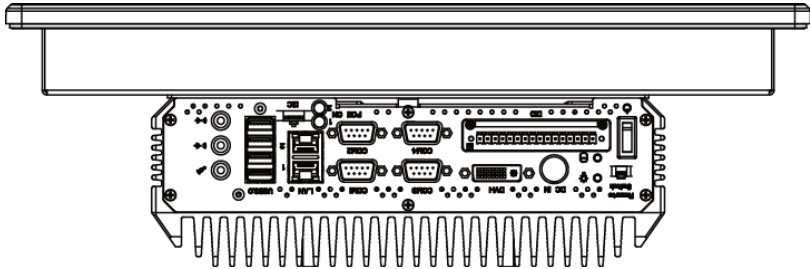
Top View



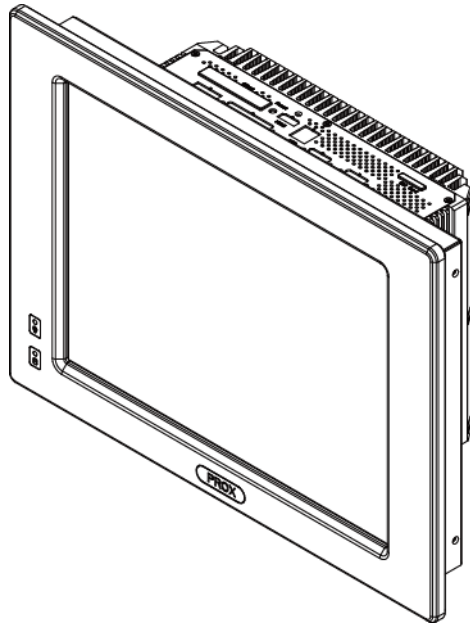
Side View



Bottom View



Quarter View



2.3 System Specifications

System	
CPU Support	<ul style="list-style-type: none"> ➤ Intel Core™ i7-6700TE, Quad Core, 2.4GHz, 8M cache ➤ Intel Core™ i5-6500TE, Quad Core, 2.3GHz, 6M cache ➤ Intel Core™ i3-6100TE, Quad Core, 2.7GHz, 4M cache ➤ Intel Pentium® G4400TE, Dual Core, 2.4GHz, 3M cache ➤ Intel Celeron® G3900TE, Dual Core, 2.3GHz, 2M cache
Chipset	➤ Intel® Q170/ H110
Memory Support	➤ 2 x DDR4 2133MHz SO-DIMM up to 32 GB
Watchdog	➤ 1 ~ 255 seconds Watchdog timer selectable
Drive Bay	➤ 2 x 2.5" SATAIII HDD/SSD (support RAID 0/1 in Q170 SKU only)
Audio	➤ 1 x Line-in / 1 x Line-out / 1 x Mic-in
Expansion Slot	<ul style="list-style-type: none"> ➤ 1 x Full-sized mini-PCIe (mini PCIe, USB and SIM signals) ➤ 1 x Half-sized mini-PCIe (mini PCIe and USB signals) ➤ 1 x SIM card slot ➤ 1 x CFAST slot
System Weight	➤ 10.8 kg
Operating System	<ul style="list-style-type: none"> ➤ Windows 7 (64-bit) ➤ Windows 8.1 (64-bit) ➤ Windows 10 IoT LTSC 2016 ➤ Linux Ubuntu 14.04 (64-bit)
Dimension (W x H x D)	➤ 408 x 308 x 132.6mm
Power Input	➤ DC in 9~36V
Certificate	➤ FCC / CE
I/O Ports (Top side)	
SIM Card	➤ 1 x SIM card slot
CFAST Slot	➤ 1 x CFAST card slot
Reset	➤ 1 x Reset pin
Antenna	➤ 3 x antenna holes
I/O Ports (Bottom side)	
Display	➤ 1 x DVI-I (DVI-D+VGA)
USB	➤ 4 x USB 3.0
Serial Port	<ul style="list-style-type: none"> ➤ COM3/4 for RS232 ➤ COM1/2 for RS232/422/485 (selectable by BIOS) ➤ COM1/2 for +5V/+12V/RI selectable by jumper
LAN	➤ 2 x GbE LAN, Wake-On-LAN, PXE

	<ul style="list-style-type: none"> ➤ LAN 1: Intel® PHY I219 LM (10/100/1000 Mbps) ➤ LAN 2: Intel® LAN I210 AT (10/100/1000 Mbps)
Digital I/O Port	➤ 8 in / 8 out, DSUB-25 type or 16-pin terminal block (optional)
I2C	➤ 1x I2C port (optional)
Audio	➤ 1 x Line-in / 1 x Line-out / 1 x Mic-in
Power Input	➤ DC in 9~36V (DIN type 4-pin connector or 3-pin terminal block)
LED	➤ 1 x Power LED, 1 x HDD LED, 2 x PoE active LED (optional)
Power On/Off	➤ 1 x power button, 1 x remote switch
Display	
LCD	➤ 15" TFT LCD(LED) Resolution XGA 1024 x768
Touch Screen	➤ 5-wire resistive touch screen (USB interface)
Environment	
Operating Temperature (with airflow)	<ul style="list-style-type: none"> ➤ HDD: 0°C ~ 35°C (32°F ~ 95°F) ➤ SSD: 0°C ~ 45°C (32°F ~ 113°F) (with PoE 0°C ~ 40°C (32°F ~104°F))
Storage Temperature	➤ -20°C ~ 60°C (-4°F ~ 140°F)
Humidity	➤ 10%~ 90%

2.4 Safety Precautions

Before operating this system, read the following information carefully to protect your systems from damages, and extend the life cycle of the system.

1. Check the Line Voltage
 - The operating voltage for the power supply should be within the range of 100V to 240V AC; otherwise, the system may be damaged.
2. Environmental Conditions
 - Place your SP-6265 on a sturdy, level surface. Be sure to allow enough space around the system to have easy access needs.
 - Avoid installing your SP-6265 system in extremely hot or cold places.
 - Avoid direct sunlight exposure for a long period of time (for example, in a closed car in summer time. Also avoid the system from any heating device.). Or do not use SP-6265 when it has been left outdoors in a cold winter day.
 - Avoid moving the system rapidly from a hot place to a cold place, and vice versa, because condensation may occur inside the system.
 - Protect your SP-6265 from strong vibrations which may cause hard disk failure.
 - Do not place the system too close to any radio-active device. Radio-active device may cause signal interference.
 - Always shut down the operating system before turning off the power.
3. Handling
 - Avoid placing heavy objects on the top of the system.
 - Do not turn the system upside down. This may cause the hard drive to malfunction.
 - Do not allow any objects to fall into this device.
 - If water or other liquid spills into the device, unplug the power cord immediately.
4. Good Care
 - When the outside case gets stained, remove the stains using neutral washing agent with a dry cloth.
 - Never use strong agents such as benzene and thinner to clean the surface of the case.
 - If heavy stains are present, moisten a cloth with diluted neutral washing agent or alcohol and then wipe thoroughly with a dry cloth.
 - If dust is accumulated on the case surface, remove it by using a special vacuum cleaner for computers.

3

System Configuration

This chapter contains helpful information about the jumper & connector settings, and component locations for the main board.

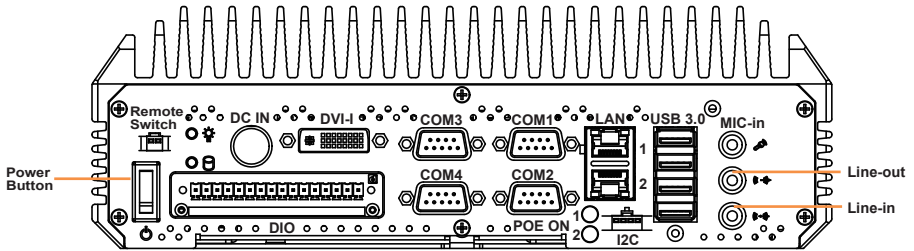
The following topics are included:

- SP-6265 Bottom I/O Ports Diagrams
- Connector & Jumper Quick Reference Table
- System Main Board Component Locations
- How to Set Jumpers
- Setting Main Board Connectors and Jumpers
- Touch Control Board Component Locations
- Setting Touch Control Board Connectors and Jumpers

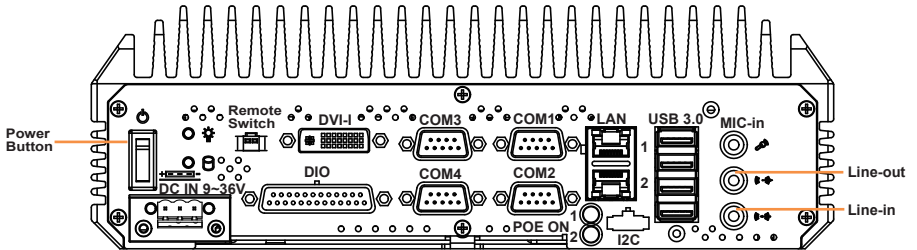
3.1 External I/O Ports Diagram

3.2 SP-6265 Bottom I/O Ports Diagram

For SP-6265Rx-x0x



For SP-6265Rx-x1x



3.3 Jumper & Connector Quick Reference Table

CONNECTOR Description	NAME
Clear CMOS Data Selection	CMOS1
LVDS Panel Selection	JP1, JP2
LVDS Power Selection	JP3
BIOS Recovery Mode Selection	JP4
Flash Descriptor Override Selection	JP7
CFast Voltage Control Selection	JP9
COM1 Pin9 RI/5V/12V Selection	JP12
COM2 Pin18 RI/5V/12V Selection	JP15
Hardware Power Failure Selection	JP16
VCCIO Voltage Selection	JP17

CONNECTOR Description	NAME
Mainboard Front Side Connectors	
COM Port	COM1, COM2, COM3, COM4
LAN1, LAN2 Ports	LAN1, LAN2
4 x 1 USB 3.0 Connectors	USB1
(Optional) Digital Input/ Output Connector	DIO1
DVI-I Connector	DVI1
HD Audio Connector	AUDIO1
Full-Sized Mini PCI Express Slot	M_PCIE1
Half-Sized Mini PCI Express Slot	M_PCIE2
SATA 3.0 Connectors	SATA1, SATA2
SATA Power Connectors	SATA_PWR1, SATA_PWR2
Power Input Connector	PWR_IN1
DC Power Input Connector	ATX_PWR1
LVDS Connector	LVDS1
LVDS Inverter Connector	INV1
PCIe Slot x4	PCI_E1
Control Signal Connector	SGN_BRD1
I2C Wafer (optional)	I2C1
Power Button Connector	PWR_BTN1
Remote Switch Connector	PWR_BTN2

CONNECTOR Description	NAME
Panel Power LED Connector	PWRLED1
Panel HDD LED Connector	HDDLED1
Low Pin Count (LPC) Connector	JLPC1
CFast Card Slot	CFAST1
DDR4 SO-DIMM memory socket	SO-DIMM1
Mainboard Rear Side Connector	
SIM Card Slot	SIM1
DDR4 SO-DIMM memory socket	SO-DIMM2

3.4 Component Locations Of System Main Board

3.4.1 Top View and Jumper Settings of SB-8400

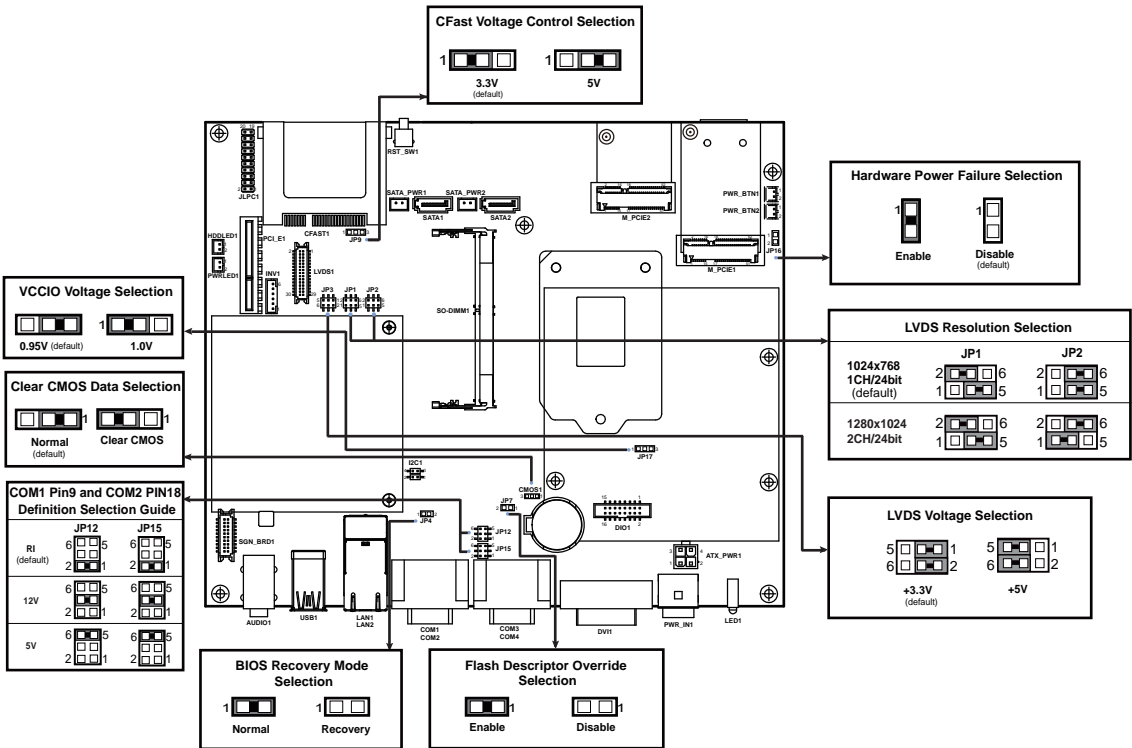





Figure 3-1. Main Board Component Location (Top View)

	<p>WARNING: Always disconnect the power cord when you are working with connectors and jumpers on the main board. Make sure both the system and peripheral devices are turned OFF as sudden surge of power could damage sensitive components. Make sure SP-6265 is properly grounded.</p>
	<p>CAUTION: Observe precautions while handling electrostatic sensitive components. Make sure to ground yourself to prevent static charge while you are working on the connectors and jumpers. Use a grounding wrist strap and place all electronic components in any static-shielded devices.</p>
	<p>CAUTION: Always touch the motherboard components by the edges. Never touch components such as a processor by its pins. Take special cares while you are holding electronic circuit boards by the edges only. Do not touch the mainboard components.</p>

3.4.2 Bottom View of SB-8400

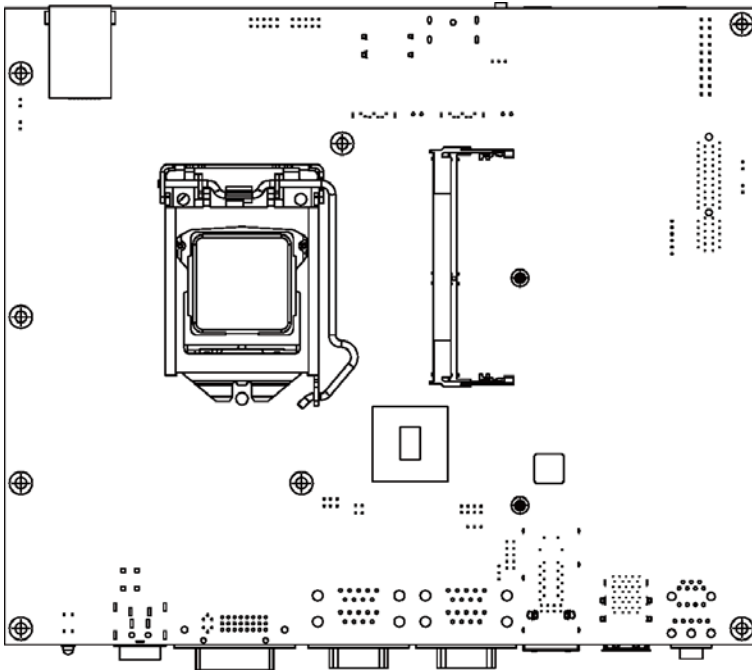


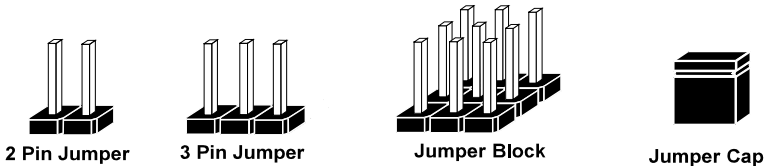
Figure 3-2. Main Board Component Location (Rear View)

3.5 How To Set Jumpers

You can configure your board by setting the jumpers. A jumper consists of two or three metal pins with a plastic base mounted on the card. By using a small plastic "cap", also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can configure your hardware settings by "opening" or "closing" jumpers.

Jumpers can be combined into sets that are called jumper blocks. When jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows what this looks like.

Jumpers & Caps

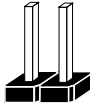


If a jumper has three pins, for example, labeled 1, 2 and 3. You can connect pins 1 and 2 to create one setting and shorting. You can also select to connect pins 2 and 3 to create another setting. The format of the jumper picture will be illustrated throughout this manual. The figure below shows different types of jumpers and jumper settings.

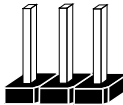
Jumper diagrams



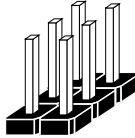
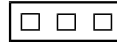
Jumper Cap looks like this



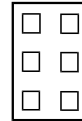
2 pin Jumper looks like this



3 pin Jumper looks like this



Jumper Block looks like this



Jumper settings

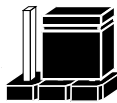


2 pin Jumper closed(enabled)
looks like this



1

1



3 pin Jumper
2-3 pin closed(enabled)
looks like this

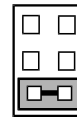


1

1



Jumper Block
1-2 pin closed(enabled)
looks like this



1 2

1 2

3.6 Function Buttons and I/O Ports

3.6.1 Power Button

Press the Power button located on the left side of Bottom I/O panel.

ACTION	ASSIGNMENT
Press	0V
Release	PWRBTN



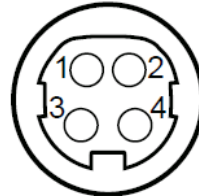
Power Button

3.6.2 DC IN Port (For SP-6265Rx-x0x)

Port Name: DC IN

Description: DC Power-In Port

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	2	GND
3	+12V	4	+12V



DC IN

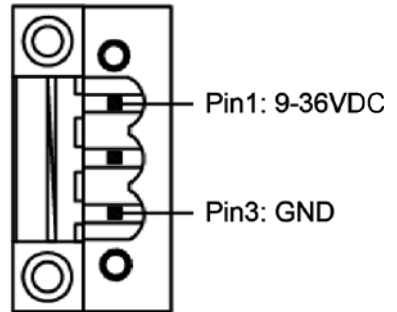
Note: The 4-pin DC IN port (DIN) is not supported in SP-6265Rx-x1x.

3.6.3 DC IN 3 Pins Terminal Block (For SP-6265Rx-x1x)

Port Name: DC IN

Description: DC IN 3 pins terminal block

PIN	ASSIGNMENT
1	VCC: 9 ~ 36 VDC
2	-
3	GND



DC IN

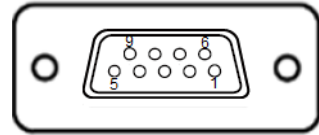
Note: The 3-pin DC IN terminal block is not supported in SP-6265Rx-x0x.

3.7 Setting Connectors and Jumpers

3.7.1 COM Port

COM1(RS232/RS422/RS485) Connector Pin Assignment:

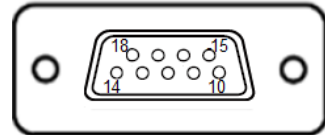
PIN	ASSIGNMENT		
	RS232 <i>(Default Setting)</i>	RS422	RS485
1	DCD#	TX-	RS-485-
2	RX	TX+	RS-485+
3	TX	RX+	X
4	DTR#	RX-	X
5	GND	GND	GND
6	DSR#	X	X
7	RTS#	X	X
8	CTS#	X	X
9	RI#	X	X



COM1

COM2(RS232/RS422/RS485) Connector Pin Assignment:

PIN	ASSIGNMENT		
	RS232 <i>(Default Setting)</i>	RS422	RS485
10	DCD#	TX-	RS-485-
11	RX	TX+	RS-485+
12	TX	RX+	X
13	DTR#	RX-	X
14	GND	GND	GND
15	DSR#	X	X
16	RTS#	X	X
17	CTS#	X	X
18	RI#	X	X



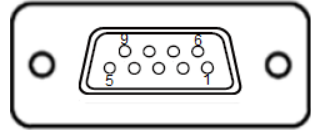
COM2

Notes:

1. COM1 Pin 9 and COM2 pin 18 are selectable for RI, +5V or +12V by jumper setting. Default setting is RI, please see “COM1 Pin9 and COM2 PIN18 Definition Selection Guide” for selection details
2. COM1,COM2 is selectable as RS232, RS422, RS485 by BIOS.
3. COM1,COM2 default setting is RS232. Please see **Chapter 5 “Advanced – F81866 Super IO Configuration – Serial Port 1/2 Configuration”** for selection details.

COM3(RS232) Connector Pin Assignment:

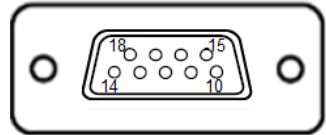
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DCD#	6	DSR#
2	RX	7	RTS#
3	TX	8	CTS#
4	DTR#	9	RI#
5	GND	-	-



COM3

COM4(RS232) Connector Pin Assignment:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
10	DCD#	15	DSR#
11	RX	16	RTS#
12	TX	17	CTS#
13	DTR#	18	RI#
14	GND	-	-

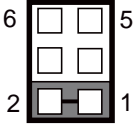
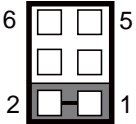
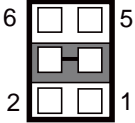
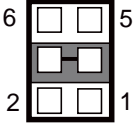
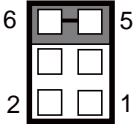
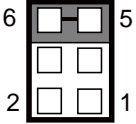


COM4

3.7.2 COM1 Pin9 and COM2 PIN18 Definition Selection Guide

Jumper Location: JP12, JP15

Description: COM1 pin9 (JP12) and COM2 pin18 (JP15) RI/5V/12V Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION	
RI	1-2 <i>(Default Setting)</i>	 <p>JP12</p>	 <p>JP15</p>
+12V	3-4	 <p>JP12</p>	 <p>JP15</p>
+5V	5-6	 <p>JP12</p>	 <p>JP15</p>

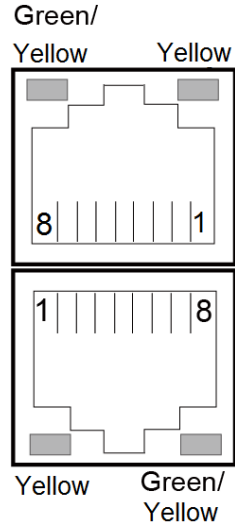
3.7.3 LAN1, LAN2 Ports

Port Name: LAN1, LAN2

Description: LAN1, LAN2 Port, LAN RJ-45 Port (Bottom I/O)

- LAN1: Intel® PHY I219 LM (10/100/1000Mbps)
- LAN2: Intel® LAN I210 AT (10/100/1000Mbps)

PIN	ASSIGNMENT
A1	MDI_1D0+
A2	MDI_1D0-
A3	MDI_1D1+
A4	MDI_1D1-
A5	MDI_1D2+
A6	MDI_1D2-
A7	MDI_1D3+
A8	MDI_1D3-
B1	MDI_2D0+
B2	MDI_2D0-
B3	MDI_2D1+
B4	MDI_2D1-
B5	MDI_2D2+
B6	MDI_2D2-
B7	MDI_2D3+
B8	MDI_2D3-



**LAN1/
LAN2**

LAN1 / LAN2 Status

There are LAN LED indicators on the bottom side of the system. By observing their status, you can know the status of the Ethernet connection.

LAN LED Indicator**Left Side LED**

Green Color On	10/100Mbps LAN Speed Indicator
Yellow Color On	Giga LAN Speed Indicator
Off	No LAN Switch/HUB connected

Right Side LED

Yellow Color Blinking	LAN Message Active
Off	No LAN Message Active

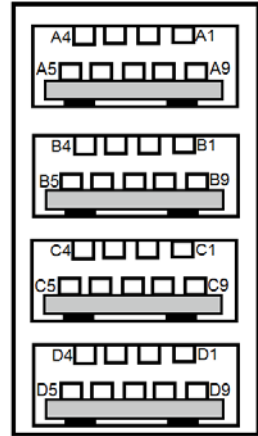
3.7.4 4 x 1 USB 3.0 Connectors

Port Name: USB1

Description: USB 3.0 Ports x 4

USB 3.0 signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	+5V	A9	USB3_TX1+
A2	USB_D1-	A8	USB3_TX1-
A3	USB_D1+	A7	GND
A4	GND	A6	USB3_RX1+
-	-	A5	USB3_RX1-
B1	VCC5	B9	USB3_TX2+
B2	USB_D2-	B8	USB3_TX2-
B3	USB_D2+	B7	GND
B4	GND	B6	USB3_RX2+
-	-	B5	USB3_RX2-
C1	+5V	C9	USB3_TX3+
C2	USB_D3-	C8	USB3_TX3-
C3	USB_D3+	C7	GND
C4	GND	C6	USB3_RX3+
-	-	C5	USB3_RX3-
D1	VCC5	D9	USB3_TX4+
D2	USB_D4-	D8	USB3_TX4-
D3	USB_D4+	D7	GND
D4	GND	D6	USB3_RX4+
-	-	D5	USB3_RX4-



USB1

3.7.5 Digital Input/Output Connector (For SP-6265Rx-x1x) (optional)

Connector Location: DIO1

Description: M/B DIO Port Connector 16 pins

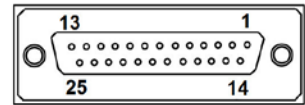
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DIN1	2	DOUT1
3	DIN2	4	DOUT2
5	DIN3	6	DOUT3
7	DIN4	8	DOUT4
9	DIN5	10	DOUT5
11	DIN6	12	DOUT6
13	DIN7	14	DOUT7
15	DIN8	16	DOUT8



DIO1

System Digital I/O Port DSUB-25 pins (bottom I/O)

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DIN1	14	DOUT1
2	DIN2	15	DOUT2
3	DIN3	16	DOUT3
4	DIN4	17	DOUT4
5	DIN5	18	DOUT5
6	DIN6	19	DOUT6
7	DIN7	20	DOUT7
8	DIN8	21	DOUT8
9	-	22	-
10	-	23	-
11	-	24	-
12	-	25	-
13	-	-	-



DIO Port

Note: The 25-pin D-Sub DIO connector is not supported in SP-6265Rx-x0x.

3.7.6 Digital Input/Output Connector (For SP-6265Rx-x0x) (optional)

Connector Location: DIO1

Description: M/B DIO Port Connector 16 pins

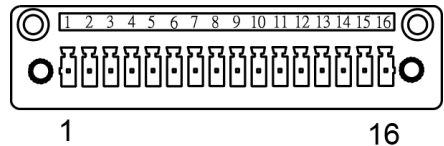
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	DIN1	2	DOUT1
3	DIN2	4	DOUT2
5	DIN3	6	DOUT3
7	DIN4	8	DOUT4
9	DIN5	10	DOUT5
11	DIN6	12	DOUT6
13	DIN7	14	DOUT7
15	DIN8	16	DOUT8



DIO1

System 16 pins terminal block DIO Port

PIN	ASSIGNMENT
1	DIN1
2	DIN2
3	DIN3
4	DIN4
5	DIN5
6	DIN6
7	DIN7
8	DIN8
9	DOUT1
10	DOUT2
11	DOUT3
12	DOUT4
13	DOUT5
14	DOUT6
15	DOUT7
16	DOUT8



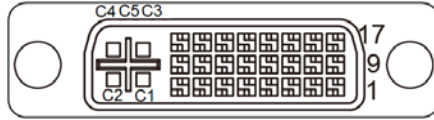
DIO Port

Note: The 16-pin terminal block DIO port is not supported in SP-6265Rx-x1x.

3.7.7 DVI-I Port

Connector Location: DVI1

Description: DVI (Digital Visual Interface) Integrated Connector



DVI1

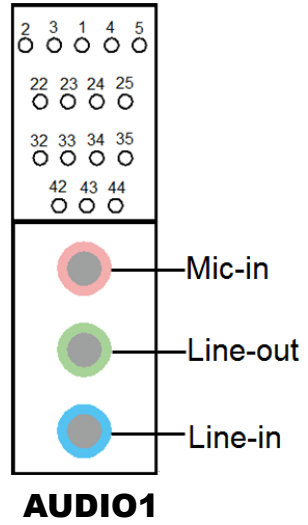
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	TMDS_D2-	13	NC
2	TMDS_D2+	14	+5V
3	GND	15	GND
4	NC	16	TMDS_HPD
5	NC	17	TMDS_D0-
6	TMDS_CLK	18	TMDS_D0+
7	TMDS_DATA	19	GND
8	VSYNC	20	NC
9	TMDS_D1-	21	NC
10	TMDS_D1+	22	GND
11	GND	23	TMDS_D3+
12	NC	24	TMDS_D3-
C1	RED	C2	GREEN
C3	BLUE	C4	HSYNC
C5	GND	C6	-

3.7.8 HD Audio Connector

Connector Location: AUDIO1

Description: HD Audio Connector for Line_in/Line_out/Mic_in.

PIN	ASSIGNMENT
2	MIC_IN_L
3	GND
1	GND
4	GND
5	MIC_IN_R
22	LINE_OUT_L
23	GND
24	GND
25	LINE_OUT_R
32	LINE_IN_L
33	GND
34	GND
35	LINE_IN_R
42	NC
43	NC
44	NC



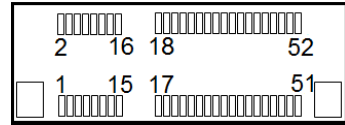
3.7.10 Mini PCI Express Slots

Connector Location: M_PCIE1 and M_PCIE2

Description: Mini-PCI Express Slots

Full-Sized Mini-PCI Express Slot 1 (M_PCIE1) signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	WAKE#	2	+3.3V_SB
3	NC	4	GND
5	NC	6	+1.5V
7	CLKREQ#	8	SIM_PWR
9	GND	10	SIM_DAT
11	REFCLK+	12	SIM_CLK
13	REFCLK-	14	SIM_RST
15	GND	16	SIM_VPP
17	SIM_SW1	18	GND
19	SIM_SW2	20	NC
21	GND	22	PERST#
23	PE_RX7+	24	+3.3V_SB
25	PE_RX7-	26	GND
27	GND	28	+1.5V
29	GND	30	SMB_CLK
31	PE_TX7-	32	SMB_DATA
33	PE_TX7+	34	GND
35	GND	36	USB_D9-
37	GND	38	USB_D9+
39	+3.3V_SB	40	GND
41	+3.3V_SB	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	+1.5V
49	NC	50	GND
51	NC	52	+3.3V_SB



**M_PCIE1/
M_PCIE2**

Half-Sized Mini-PCI Express Slot 2 (M_PCIE2) signals:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	WAKE#	2	+3.3V_SB
3	NC	4	GND
5	NC	6	+1.5V
7	CLKREQ#	8	NC
9	GND	10	NC
11	REFCLK+	12	NC
13	REFCLK-	14	NC
15	GND	16	NC
17	NC	18	GND
19	NC	20	NC
21	GND	22	PERST#
23	PE_RX8-	24	+3.3V_SB
25	PE_RX8+	26	GND
27	GND	28	+1.5V
29	GND	30	SMB_CLK
31	PE_TX8-	32	SMB_DATA
33	PE_TX8+	34	GND
35	GND	36	USB_D10-
37	GND	38	USB_D10+
39	+3.3V_SB	40	GND
41	+3.3V_SB	42	NC
43	GND	44	NC
45	NC	46	NC
47	NC	48	+1.5V
49	NC	50	GND
51	NC	52	+3.3V_SB

Mini PCI Express is the successor of the Mini PCI card and provides an increased data throughput. The cards have a detached network interface and are equipped with one lane. They are used in particular in embedded designs or compact box PCs.

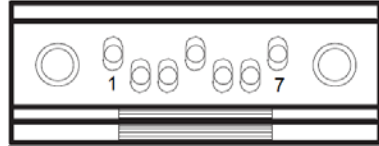
3.7.11 Serial ATA (SATA) 3.0 Connectors

Connector Location: SATA1 and SATA2

Description: Serial ATA 3.0 Connectors

Serial ATA 3.0 Connector 1 (SATA1) signals:

PIN	ASSIGNMENT
1	GND
2	SATA_TX+
3	SATA_TX-
4	GND
5	SATA_RX-
6	SATA_RX+
7	GND



**SATA1/
SATA2**

Serial ATA 3.0 Connector 2 (SATA2) signals:

PIN	ASSIGNMENT
1	GND
2	SATA_TX1+
3	SATA_TX1-
4	GND
5	SATA_RX1-
6	SATA_RX1+
7	GND

3.7.12 SATA Power Connectors

Connector Location: SATA_PWR1 and SATA_PWR2

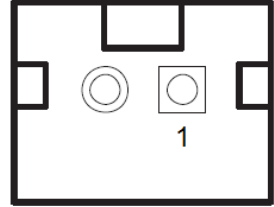
Description: Serial ATA Power Connectors

Serial ATA Power Connector 1 (SATA_PWR1) signals:

PIN	ASSIGNMENT
1	+5V
2	GND

Serial ATA Power Connector 2 (SATA_PWR2) signals:

PIN	ASSIGNMENT
1	+5V
2	GND



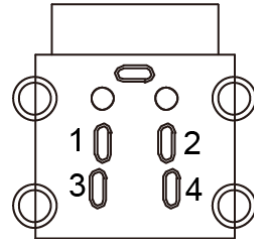
**SATA_PWR1/
SATA_PWR2**

3.7.13 Power Input Connector

Connector Location: PWR_IN1

Description: Power Input Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	2	GND
3	+12V	4	+12V



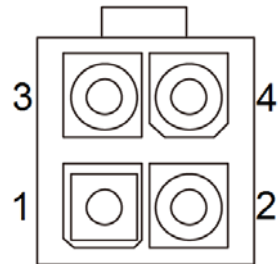
PWR_IN1

3.7.14 DC Power Input Connector

Connector Location: ATX_PWR1

Description: DC Power Input Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	3	+12V
2	GND	4	+12V



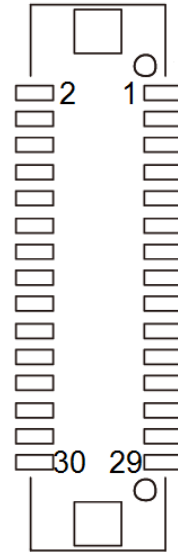
ATX_PWR1

3.7.15 LVDS Connector

Connector Location: LVDS1

Description: LVDS Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
2	GND	1	LVDS_VCC
4	LVDS1_CLK+	3	LVDS1_CLK-
6	LVDS1_D2-	5	GND
8	GND	7	LVDS1_D2+
10	LVDS1_D1+	9	LVDS1_D1-
12	LVDS1_D3-	11	LVDS1_D3+
14	LVDS1_D0-	13	LVDS1_D0+
16	LVDS0_CLK+	15	GND
18	GND	17	LVDS0_CLK-
20	LVDS0_D2-	19	LVDS0_D2+
22	LVDS0_D1+	21	GND
24	GND	23	LVDS0_D1-
26	LVDS0_D0-	25	LVDS0_D0+
28	LVDS0_D3-	27	LVDS0_D3+
30	LVDS_VCC	29	LVDS_VCC



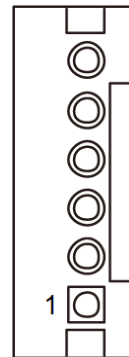
LVDS1

3.7.16 LVDS Inverter Connector

Connector Location: INV1

Description: LVDS Inverter Connector

PIN	ASSIGNMENT
1	+12V
2	+12V
3	GND
4	BRCTR
5	GND
6	BKLTEN



INV1

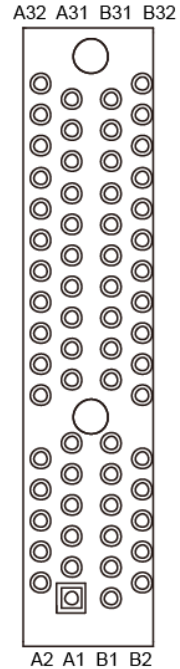
3.7.17 PCI Express Slot (x4)

Connector Location: PCI_E1

Description: PCI Express Slot x4 (Not supported in H110 chip)

PCI_E1 is only supported in Q170 SKU.

PIN	ASSIGNMENT	PIN	ASSIGNMENT
B1	+12V	A1	PRSNT#1
B2	+12V	A2	+12V
B3	+12V	A3	+12V
B4	GND	A4	GND
B5	SMB_CLK	A5	NC
B6	SMB_DATA	A6	NC
B7	GND	A7	NC
B8	+3.3V	A8	NC
B9	NC	A9	+3.3V
B10	+3.3V_AXU	A10	+3.3V
B11	WAKE#	A11	PERST#
B12	RSVD	A12	GND
B13	GND	A13	REFCLK+
B14	HSOP0	A14	REFCLK-
B15	HSO0	A15	GND
B16	GND	A16	HSIP0
B17	PRSNT#2	A17	HSIN0
B18	GND	A18	GND
B19	HSOP1	A19	RSVD
B20	HSO1	A20	GND
B21	GND	A21	HSIP1
B22	GND	A22	HSIN1
B23	HSOP2	A23	GND
B24	HSO2	A24	GND
B25	GND	A25	HSIP2
B26	GND	A26	HSIN2
B27	HSOP3	A27	GND
B28	HSO3	A28	GND
B29	GND	A29	HSIP3
B30	RSVD	A30	HSIN3
B31	PRSNT#2	A31	GND
B32	GND	A32	RSVD



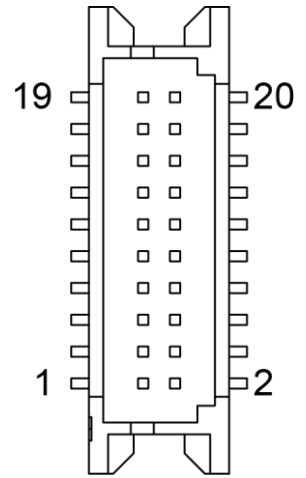
PCI_E1(x4)

3.7.18 Control Signal Connector

Connector Location: SGN_BRD1

Description: Control Signal Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	VCC5A	2	VCC5A
3	USB_N	4	VCC12
5	USB_P	6	VCC12
7	GND	8	VCC12
9	LVDS_BKLTEN	10	VCC12
11	USB_N	12	VCC12
13	USB_P	14	HDD_LED
15	GND	16	PWR_LED
17	GND	18	BRCTR
19	VCC5	20	USB_OC



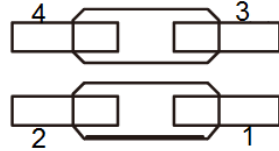
SGN_BRD1

3.7.19 I2C Wafer (optional)

Connector Location: I2C1

Description: I2C Wafer

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	I2C_SDA	2	I2C_SCL
3	I2C_SDA	4	I2C_SCL



I2C1

3.7.20 Power Button Connectors

Connector Location: PWR_BTN1, PWR_BTN2

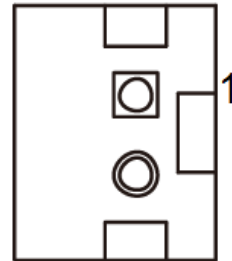
Description: Power Button and Remote Switch Connectors

PWR_BTN1 : Power Button Connector

PIN	ASSIGNMENT
1	PWR_BTN
2	GND

PWR_BTN2 : Remote Switch Connector

PIN	ASSIGNMENT
1	PWR_BTN
2	GND



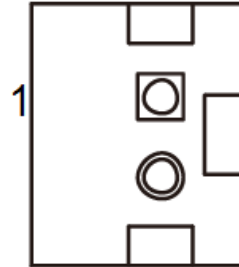
**PWR_BTN1/
PWR_BTN2**

3.7.21 Panel Power LED Connector

Connector Location: PWRLED1

Description: Power LED Connector

PIN	ASSIGNMENT
1	PWRLED+
2	GND



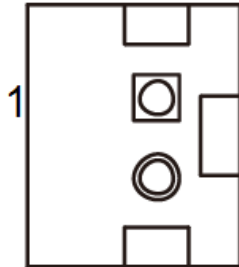
PWRLED1

3.7.22 Panel HDD LED Connector

Connector Location: HDDLED1

Description: Hard Disk Drive LED Connector

PIN	ASSIGNMENT
1	HDDLED+
2	HDDLED-



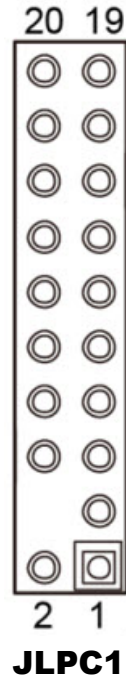
HDDLED1

3.7.23 Low Pin Count (LPC) Connector

Connector Location: JLPC1

Description: Low Pin Count (LPC) Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	CLK	2	GND
3	LFRAME#	4	NC
5	RST#	6	+5V
7	LAD3	8	LAD2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	SMBCLK	14	SMBDAT
15	+3.3V_SB	16	SERIRQ
17	GND	18	CLKRUN#
19	LPCPD#	20	LDREQ#

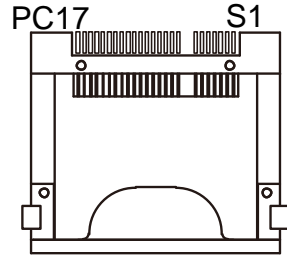


3.7.24 CompactFlash Card Connector

Connector Location: CFAST1

Description: CompactFlash Card Connector

PIN	ASSIGNMENT
S1	GND
S2	SATA_TXP0
S3	SATA_TXN0
S4	GND
S5	SATA_RXN0
S6	SATA_RXP0
S7	GND
PC1	NC
PC2	GND
PC3	NC
PC4	NC
PC5	NC
PC6	NC
PC7	GND
PC8	NC
PC9	NC
PC10	NC
PC11	NC
PC12	NC
PC13	PWR
PC14	PWR
PC15	GND
PC16	GND
PC17	NC



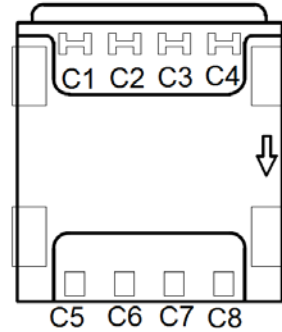
CFAST1

3.7.25 SIM Card Slot

Connector Location: SIM1 (rear side of mainboard)

Description: SIM (Subscriber Identity Module) Card Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
C5	GND	C1	VSIM
C6	VPP	C2	RST
C7	DATA	C3	CLK
C8	RSV	C4	RSV



SIM1

3.7.26 Hardware Power Failure Selection

Jumper Location: JP16



Description: Hardware Power Failure Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Enable	Close	<p>JP16</p>
Disable	<i>Open (Default Setting)</i>	<p>JP16</p>

3.7.27 CFast Voltage Control Selection

Jumper Location: JP9

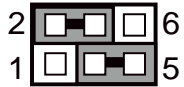
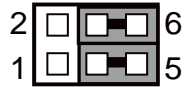
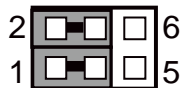
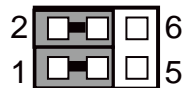
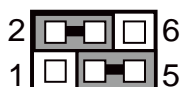
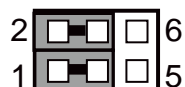

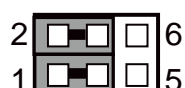




Description: CompactFlash (CFast) Voltage Control Selection



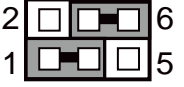
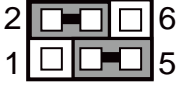
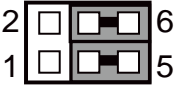
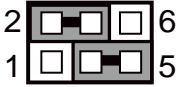
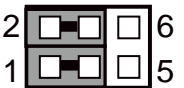
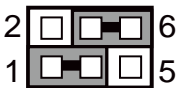
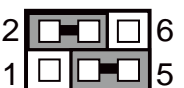
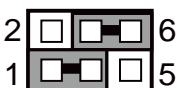


SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
+3.3V	<i>1-2</i> <i>(Default Setting)</i>	1  JP9
+5V	2-3	1  JP9

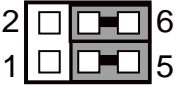
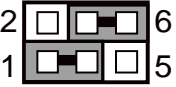
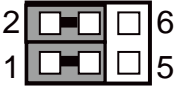
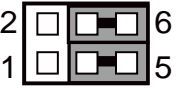
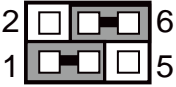
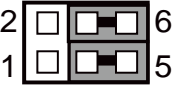
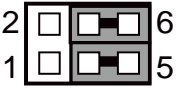
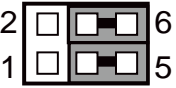
3.7.28 LVDS Resolution Selection

Jumper Name: JP1, JP2

Description: LVDS Resolution Selection

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION	
1024x768 1CH/24bit <i>(Default Setting)</i>	JP1(2-4) JP1(3-5) JP2(3-5) JP2(4-6)	 JP1	 JP2
1920x1200 2CH/24bit	JP1(1-3) JP1(2-4) JP2(1-3) JP2(2-4)	 JP1	 JP2
1920x1080 2CH/24bit	JP1(2-4) JP1(3-5) JP2(1-3) JP2(2-4)	 JP1	 JP2
1600x1200 2CH/24bit	JP1(1-3) JP1(4-6) JP2(1-3) JP2(2-4)	 JP1	 JP2
1680x1050 2CH/24bit	JP1(3-5) JP1(4-6) JP2(1-3) JP2(2-4)	 JP1	 JP2
1600x900 2CH/24bit	JP1(1-3) JP1(2-4) JP2(2-4) JP2(3-5)	 JP1	 JP2

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION	
1400x1050 2CH/24bit	JP1(2-4) JP1(3-5) JP2(2-4) JP2(3-5)	 JP1	 JP2
1440x900 2CH/24bit	JP1(1-3) JP1(4-6) JP2(2-4) JP2(3-5)	 JP1	 JP2
1366x768 1CH/24bit	JP1(3-5) JP1(4-6) JP2(2-4) JP2(3-5)	 JP1	 JP2
1366x768 1CH/18bit	JP1(1-3) JP1(2-4) JP2(1-3) JP2(4-6)	 JP1	 JP2
1280x1024 2CH/24bit	JP1(2-4) JP1(3-5) JP2(1-3) JP2(4-6)	 JP1	 JP2
1280x960 1CH/24bit	JP1(1-3) JP1(4-6) JP2(1-3) JP2(4-6)	 JP1	 JP2


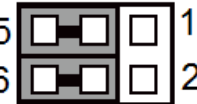
SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION	
1280x800 1CH/18bit	JP1(3-5) JP1(4-6) JP2(1-3) JP2(4-6)	 <p>JP1</p>	 <p>JP2</p>
1280x768 1CH/18bit	JP1(1-3) JP1(2-4) JP2(3-5) JP2(4-6)	 <p>JP1</p>	 <p>JP2</p>
1024x768 1CH/18bit	JP1(1-3) JP1(4-6) JP2(3-5) JP2(4-6)	 <p>JP1</p>	 <p>JP2</p>
800x600 1CH/18bit	JP1(3-5) JP1(4-6) JP2(3-5) JP2(4-6)	 <p>JP1</p>	 <p>JP2</p>

Note: Manufacturing Default: 1024X768 ICH 24bit.

3.7.29 LVDS Voltage Selection

Jumper Name: JP3

Description: Voltage selection for selecting LVDS1 (LVDS_VCC)



SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
+3.3V	1-3, 2-4 <i>(Default Setting)</i>	 <p style="text-align: center;">JP3</p>
+5V	3-5, 4-6	 <p style="text-align: center;">JP3</p>

Note: Manufacturing Default: +3.3V.

3.7.30 Flash Descriptor Override Selection

Jumper Location: JP7



Description: Flash Descriptor Override Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Enable	Close	 <p style="text-align: center;">JP7</p>
Disable	<i>Open</i> <i>(Default Setting)</i>	 <p style="text-align: center;">JP7</p>

3.7.31 VCCIO Voltage Selection

Jumper Location: JP17



Description: VCCIO Voltage Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
0.95V	2-3 <i>(Default Setting)</i>	1  JP17
1.0V	1-2	1  JP17

3.7.32 BIOS Recovery Mode Selection

Jumper Location: JP4

Description: BIOS Recovery Mode Selection



SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Normal	Close <i>(Default Setting)</i>	1  JP4
Recovery	Open	1  JP4

3.7.33 Clear CMOS Data Selection

Jumper Location: CMOS1

Description: Clear CMOS Data Selection

- Step 1. Remove the main power of the PC.
- Step 2. Close **CMOS1** (pins 1-2) for 6 seconds by a cap.
- Step 3. Remove the cap which is just used on **CMOS1** (1-2), so that **CMOS1** returns to "OPEN".
- Step 4. Power on the PC and the PC will then auto-reboot for once in order to set SoC's register.
- Step 5. Done!

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Normal	1-2 <i>(Default Setting)</i>	<div style="text-align: center;"> <p>3 1</p>  <p>CMOS1</p> </div>
Clear CMOS	2-3	<div style="text-align: center;"> <p>3 1</p>  <p>CMOS1</p> </div>

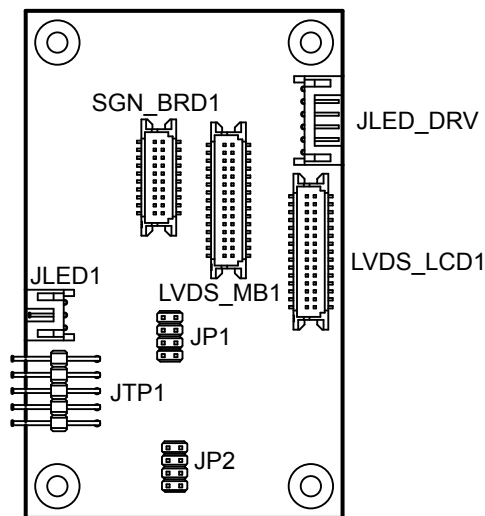
3.8 TOUCH CONTROL BOARD SR-6145 CONNECTOR QUICK REFERENCE TABLE

JUMPER Description	NAME
Touch Panel Up Signal Setting	JP1
Touch Panel Low Signal Setting	JP2

CONNECTOR Description	NAME
Control Signal Connector	SGN_BRD1
LVDS Panel Signal Connector (Connected To Motherboard)	LVDS_MB1
LVDS Panel Signal Connector (Connected To LCD Panel)	LVDS_LCD1
15" Panel LED Backlight Control Connector	JLED_DRV
Touch Panel Connector	JTP1
LED1 Connector	JLED1

3.9 TOUCH CONTROL BOARD SR-6145 COMPONENT LOCATIONS

3.9.1 Touch Control Board SR-6145 Top View



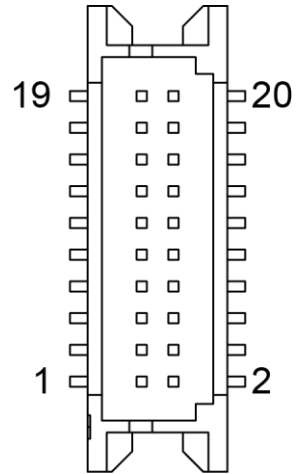
3.10 SETTING TOUCH CONTROL BOARD SR-6145 CONNECTORS AND JUMPERS

3.10.1 Control Signal Connector

Connector Location: SGN_BRD1

Description: Control Signal Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	5VSB	2	5VSB
3	USB D+	4	+12V
5	USB D-	6	+12V
7	GND	8	+12V
9	Backlight Enable	10	+12V
11	NC	12	+12V
13	NC	14	HD_LED
15	GND	16	POWER_LED
17	GND	18	LCD_PWM
19	VCC	20	USB_OC



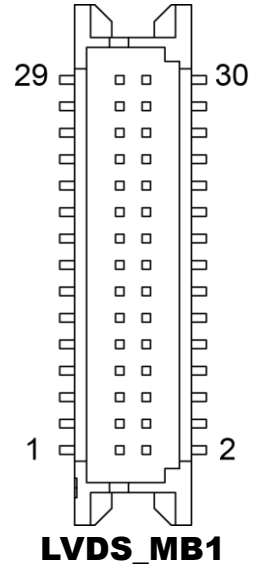
SGN_BRD1

3.10.2 LVDS Panel Signal Connector (Connected To Motherboard)

Connector Location: LVDS_MB1

Description: LVDS Panel Signal Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	LVDS_VCC	2	GND
3	LVDS1_CLK-(Even)	4	LVDS1_CLK+(Even)
5	GND	6	LVDS1_D2-(Even)
7	LVDS1_D2+(Even)	8	GND
9	LVDS1_D1-(Even)	10	LVDS1_D1+(Even)
11	LVDS1_D3+(Even)	12	LVDS1_D3-(Even)
13	LVDS1_D0+(Even)	14	LVDS1_D0-(Even)
15	GND	16	LVDS0_CLK+(Odd)
17	LVDS0_CLK-(Odd)	18	GND
19	LVDS0_D2+(Odd)	20	LVDS0_D2-(Odd)
21	GND	22	LVDS0_D1+(Odd)
23	LVDS0_D1-(Odd)	24	GND
25	LVDS0_D0+(Odd)	26	LVDS0_D0-(Odd)
27	LVDS0_D3+(Odd)	28	LVDS0_D3-(Odd)
29	LVDS_VCC	30	LVDS_VCC

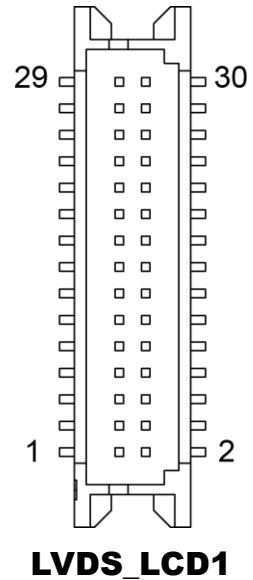


3.10.3 LVDS Panel Signal Connector (Connected To LCD Panel)

Connector Location: LVDS_LCD1

Description: LVDS Panel Signal Connector

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	LVDS_VCC	2	GND
3	LVDS1_CLK-(Even)	4	LVDS1_CLK+(Even)
5	GND	6	LVDS1_D2-(Even)
7	LVDS1_D2+(Even)	8	GND
9	LVDS1_D1-(Even)	10	LVDS1_D1+(Even)
11	LVDS1_D3+(Even)	12	LVDS1_D3-(Even)
13	LVDS1_D0+(Even)	14	LVDS1_D0-(Even)
15	GND	16	LVDS0_CLK+(Odd)
17	LVDS0_CLK-(Odd)	18	GND
19	LVDS0_D2+(Odd)	20	LVDS0_D2-(Odd)
21	GND	22	LVDS0_D1+(Odd)
23	LVDS0_D1-(Odd)	24	GND
25	LVDS0_D0+(Odd)	26	LVDS0_D0-(Odd)
27	LVDS0_D3+(Odd)	28	LVDS0_D3-(Odd)
29	LVDS_VCC	30	LVDS_VCC

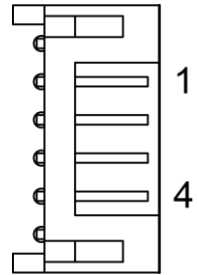


3.10.4 15" Panel LED Backlight Control Connector

Connector Location: JLED_DRV

Description: 15" Panel LED Backlight Control Connector

PIN	ASSIGNMENT
1	+12V
2	+12V
3	GND
4	LCD_PWM



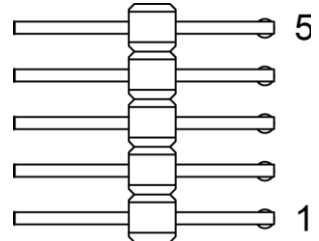
JLED_DRV

3.10.5 Touch Panel Connector

Connector Location: JTP1

Description: Touch Panel Connector

PIN	ASSIGNMENT
1	Low Right or Low Left
2	Low Left or Low Right
3	Sense signal
4	Up Right or Up Left
5	Up Left or Up Right



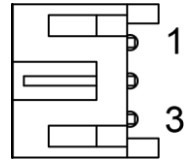
JTP1

3.10.6 LED1 Connector

Connector Location: JLED1

Description: LED1 Connector

<i>PIN</i>	<i>ASSIGNMENT</i>
1	VCC
2	POWER LED
3	HDD LED

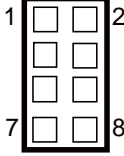
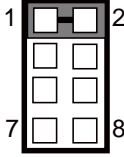
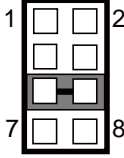
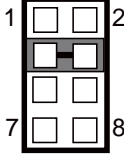
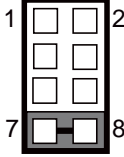


JLED1

3.10.7 Touch Panel Up Signal Setting

Jumper Name: JP1

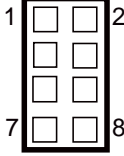
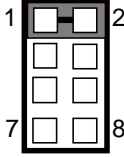
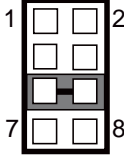
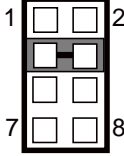
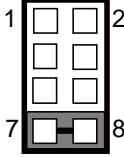
Description: Touch Panel Up Signal Setting

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
Normal	Open (Default Setting)	 <p style="text-align: center;">JP1</p>
Connect Up Left Signal	1-2	 <p style="text-align: center;">JP1</p>
Connect Up Right Signal	5-6	 <p style="text-align: center;">JP1</p>
Connect Up Right Signal	3-4	 <p style="text-align: center;">JP1</p>
Connect Up Left Signal	7-8	 <p style="text-align: center;">JP1</p>

3.10.8 Touch Panel Low Signal Setting

Jumper Name: JP2

Description: Touch Panel Low Signal Setting

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
Normal	Open <i>(Default Setting)</i>	 <p style="text-align: center;">JP2</p>
Connect Low Left Signal	1-2	 <p style="text-align: center;">JP2</p>
Connect Low Right Signal	5-6	 <p style="text-align: center;">JP2</p>
Connect Low Right Signal	3-4	 <p style="text-align: center;">JP2</p>
Connect Low Left Signal	7-8	 <p style="text-align: center;">JP2</p>

4 Software Utilities

This chapter provides the detailed information that guides users to install driver utilities for the system. The following topics are included:

- Installing Intel[®] Chipset Software Installation Utility
- Installing Intel[®] Management Engine Components Installation Utility
- Installing Graphics Driver Utility
- Installing LAN Driver Utility
- Installing Sound Driver Utility
- Installing KMDf Driver Utility (Windows 7 Only)
- Installing USB 3.0 eXtensible Host Controller Utility
- Installing Intel RST Driver Utility (Optional, For Q170 SKU Only)
- Installing Touch Driver Utility
- Installing Hotfix Driver Utility

4.1 Introduction

Enclosed with the SP-6265 Series package is our driver utilities contained in a DVD-ROM disk. Refer to the following table for driver locations:

Filename (Assume that DVD-ROM drive is D :)	Purpose	OS	
		Shell	Win7 (32/64bit)
D:\Driver\Flash BIOS	For BIOS Update Utility	✓	X
D:\Driver\Platform\Main Chip(Q170,H110)	Intel(R) Chipset Device Software Installation Utility	X	✓
D:\Driver\Platform\Sky Lake_Graphics(Q170,H110)	Intel(R) HD Graphics installer	X	✓
D:\Driver\Platform\LAN Chip(Q170,H110)	Intel(R) Network Connections Software	X	✓
D:\Driver\Platform\Sound Codec(Q170,H110)	Realtek High Definition Audio System Software	X	✓
D:\Driver\Platform\KMDF (Q170,H110)	Windows 7 update KMDF	X	✓
D:\Driver\Platform\ME(Q170,H110)	Intel(R) Management Engine Components installer	X	✓
D:\Driver\Platform\USB3 (Q170,H110)	Intel(R) USB 3.0 eXtensible Host Controller	X	✓
D:\Driver\Platform\RST(Q170)	Intel(R) Rapid Storage Technology Installer Software	X	✓
D:\Driver\Device\Touch	eGalaxTouch Driver	X	✓

X : Not supported

✓: Supported

Filename (Assume that DVD-ROM drive is D :)	Purpose	OS	
		Shell	Win8.1 (64bit)
D:\Driver\Flash BIOS	For BIOS update utility	✓	X
D:\Driver\Platform\Main Chip(Q170,H110)	Intel(R) Chipset Device Software Installation Utility	X	✓
D:\Driver\Platform\Sky Lake_Graphics (Q170,H110)	Intel(R) HD Graphics installer	X	✓
D:\Driver\Platform\LAN Chip(Q170,H110)	Intel(R) Network Connections Software	X	✓
D:\Driver\Platform\Sound Codec(Q170,H110)	Realtek High Definition Audio System Software	X	✓

Filename (Assume that DVD-ROM drive is D :)	Purpose	OS	
		Shell	Win8.1 (64bit)
D:\Driver\Platform\ME (Q170,H110)	Intel(R) Management Engine Components installer	X	✓
D:\Driver\Platform\RST (Q170)	Intel(R) Rapid Storage Technology Installer Software	X	✓
D:\Driver\Device\Touch	eGalaxTouch Driver	X	✓

X : Not supported

✓ : Supported

Filename (Assume that DVD-ROM drive is D :)	Purpose	OS	
		Shell	Win10 (64bit)
D:\Driver\Flash BIOS	For BIOS update utility	✓	X
D:\Driver\Platform\Main Chip(Q170,H110)	Intel(R) Chipset Device Software Installation Utility	X	✓
D:\Driver\Platform\Kaby Lake Graphics(Q170,H110)	Intel(R) HD Graphics installer	X	✓
D:\Driver\Platform\LAN Chip(Q170,H110)	Intel(R) Network Connections Software	X	✓
D:\Driver\Platform\Sound Codec(Q170,H110)	Realtek High Definition Audio System Software	X	✓
D:\Driver\Platform\ME (Q170,H110)	Intel(R) Management Engine Components installer	X	✓
D:\Driver\Platform\RST (Q170)	Intel(R) Rapid Storage Technology Installer Software	X	✓
D:\Driver\Platform\Hotfix (Q170,H110)	Microsoft Hotfix kb3211320 and kb3213986	X	✓
D:\Driver\Device\Touch	eGalaxTouch Driver	X	✓

X : Not supported

✓ : Supported

Note: Install the driver utilities immediately after the OS installation is completed.

4.2 Installing Intel® Chipset Software Installation Utility

4.2.1 Introduction

The Intel® Chipset Software Installation Utility installs the Windows *.INF files to the target system. These files outline to the operating system how to configure the Intel chipset components in order to ensure that the following functions work properly:

- Core PCI and ISAPNP Services
- PCIe Support
- SATA Storage Support
- USB Support
- Identification of Intel® Chipset Components in the Device Manager

4.2.2 Intel® Chipset Software Installation Utility

The utility pack is to be installed only for Windows® 7/8.1/10 series, and it should be installed immediately after the OS installation is finished. Please follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Enter the **Main Chip** folder where the Chipset driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 4** Click the chipset driver installation file for driver installation.
- 5** Follow the on-screen instructions to install the driver.
- 6** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

4.3 Intel® Management Engine Components Installer Installation

For Windows 7 only. Pre-install Microsoft's Kernel-Mode Driver Framework (KMDF) version 1.11 before you install the Intel® Management Engine Components Installer (ME) in order to avoid errors in Device Manager.

Installation Instructions for Kernel-Mode Driver Framework (KMDF)

To install the Kernel-Mode Driver Framework (KMDF), follow the steps below:

- 1 Insert the driver disk into a DVD-ROM device.
- 2 Select Windows 7 (32/64-bit) for your OS platform.
- 3 (For Windows 7 only) Click the **kmdf-1.11-Win-6.1-x86** file for Windows 32-bit driver installation.
- 4 (For Windows 7 only) Click the **kmdf-1.11-Win-6.1-x64** file for Windows 64-bit driver installation.

Installation Instructions for Intel® Management Engine Components Installer

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 3** Enter the **ME Installation Driver** folder where the driver is located.
- 4** Click **SetupME.exe** file for ME driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

4.4 Intel® USB 3.0 eXtensible Host Controller Utility

(For Windows 7 only) Intel® USB 3.0 eXtensible Host Controller Driver supports the following Intel® Chipsets/Processors:

- Intel® 8 Series/C220 series Chipset Family
- Intel® 4th Generation Core™ Processors
- Intel® C610 series Chipset Family
- Intel® 9 Series Chipset Family
- Intel® Pentium® Processor or Intel® Celeron® Processor N- & J-Series
- Intel® 5th generation Intel® Core™ Processors
- Intel® Core™ M Processor
- Intel® 6th generation Intel® Core™ processors
- Intel® 100 Series Chipset Family

To install the utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Select Windows 7 (32/64-bit) for your OS platform.
- 3** Enter the USB 3.0 folder where the driver is located.
- 4** Click **Setup.exe** file for USB 3.0 driver installation
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.
- 7** Run the application with administrative privileges.

4.5 Installing Intel RST Driver Utility (Only for Q170, Optional)

Installing RAID Driver Utility

The Intel® Rapid Storage Technology (Intel® RST) driver supports RAID 0, 1 in Q170 SKU for 2 x 2.5" SATAIII HDD/SSD. To install the RAID/RST driver utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Enter the **RST** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 4** Click **SetupRST.exe** driver installation file for driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

Note: The RAID driver utility is not supported for H110 SKU.

4.6 Intel® RapidStorage Technology Option ROM

The Intel® Rapid Storage Technology option ROM provides the following:

- Pre-operating system user interface for RAID volume management
- Ability to create, delete and reset RAID volumes
- RAID recovery

Accessing Intel® Rapid Storage Technology Option ROM User Interface

To enter the Intel® Rapid Storage Technology option ROM user interface, press **Ctrl-I** when prompted during the Power-On Self-Test (POST).

Option ROM prompt:

```

Intel(R) Rapid Storage Technology - Option ROM - 10.5.0.1034
Copyright(C) 2003-11 Intel Corporation. All Rights Reserved.

RAID Volumes:
None defined.

Physical Devices:
Port Device Model Serial # Size Type/Status(Vol ID)
2 WDC WD1600AAJS-7 WD-WMAP9D045721 149.0GB Non-RAID Disk
3 WDC WD1600AAJS-7 WD-WMAP9D046479 149.0GB Non-RAID Disk
Press <CTRL-I> to enter Configuration Utility...
    
```

In the user interface, the hard drive(s) and hard drive information listed for your system will differ from the example in the figure below:

Option ROM user interface:

```

Intel(R) Rapid Storage Technology - Option ROM - 10.5.0.1034
Copyright(C) 2003-11 Intel Corporation. All Rights Reserved.
[ MAIN MENU ]
1. Create RAID Volume
2. Delete RAID Volume
3. Reset Disks to Non-RAID
4. Recovery Volume Options
5. Acceleration Options
6. Exit
[ DISK/VOLUME INFORMATION ]
RAID Volumes:
None defined.

Physical Devices:
Port Device Model Serial # Size Type/Status(Vol ID)
0 WDC WD1600AAJS-7 WD-WMAP9D045633 149.0GB Non-RAID Disk
2 WDC WD1600AAJS-7 WD-WMAP9D045721 149.0GB Non-RAID Disk
3 WDC WD1600AAJS-7 WD-WMAP9D046479 149.0GB Non-RAID Disk

[+] Select [ESC]-Exit [ENTER]-Select Menu
    
```

4.7 Installing Graphics Driver Utility

The graphics interface embedded in SP-6265 can support a wide range of display types. You can have dual displays via DVI-I port (DVI-D+VGA) and make the system work simultaneously.

To install the Graphics driver utility, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Enter the **Graphics** folder where the driver is located.
- 3** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) OS platform for Skylake processor.
- 4** Select Windows 10 (64-bit) OS platform for Kaby Lake processor.
- 5** Click the graphics driver installation file for driver installation.
- 6** Follow the on-screen instructions to complete the installation.
- 7** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

4.8 Installing LAN Driver Utility

Enhanced with LAN function, SP-6265 supports various network adapters. To install the LAN Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 3** Click the LAN driver installation file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

For more details on the installation procedure, refer to the README.txt file that you can find on LAN Driver Utility.

4.9 Installing Sound Driver Utility

The sound function enhanced in this system is fully compatible with Windows® 7/8.1/10 series.

To install the Sound Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Select Windows 7 (32/64-bit) / Windows 8.1 (64-bit) / Windows 10 (64-bit) for your OS platform.
- 3** Click the Sound driver installation file for driver installation.
- 4** Follow the on-screen instructions to complete the installation.
- 5** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

4.10 Installing Touch Driver Utility

To install the eGalaxTouch Driver, follow the steps below:

- 1** Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2** Enter the **Device > Touch** folder where the driver is located.
- 3** Enter the eGalaxTouch driver installation folder.
- 4** Run **setup.exe** file for driver installation.
- 5** Follow the on-screen instructions to complete the installation.
- 6** Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

4.11 Microsoft Hotfix kb3211320 and kb3213986 Driver installation

To install the Hotfix driver utility, follow the steps below:

- 1 Connect the USB DVD-ROM device to SP-6265 and insert the driver disk.
- 2 Enter the **Hotfix** folder where the driver is located.
- 3 Click the **windows10.0-kb3211320-x64** and **windows10.0-kb3213986-x64** files for critical security update.
- 4 Follow the on-screen instructions to complete the installation.
- 5 Once the installation is completed, shut down the system and restart SP-6265 for the changes to take effect.

5

BIOS SETUP

This chapter guides users how to configure the basic system configurations via the BIOS Setup Utilities. The information of the system configuration is saved in battery-backed CMOS RAM and BIOS NVRAM so that the Setup information is retained when the system is powered off. The BIOS Setup Utilities consist of the following menu items:

- Main Menu
- Advanced Menu
- Chipset Menu
- Security Menu
- Boot Menu
- Save & Exit Menu

5.1 Introduction

The SP-6265 System uses an AMI (American Megatrends Incorporated) Aptio BIOS that is stored in the Serial Peripheral Interface Flash Memory (SPI Flash) and can be updated. The SPI Flash contains the built-in BIOS setup program, Power-On Self-Test (POST), PCI auto-configuration utility, LAN EEPROM information, and Plug and Play support.

Aptio is AMI's BIOS firmware based on the UEFI (Unified Extensible Firmware Interface) specifications and the Intel Platform Innovation Framework for EFI. The UEFI specification defines an interface between the operating system and platform firmware. The interface consists of data tables that contain platform-related information, boot service calls, and runtime service calls that are available to the operating system and its loader. These elements have combined to provide a standard environment for booting the operating system and running pre-boot applications.

The diagram below shows the Extensible Firmware Interface's location in the software stack.

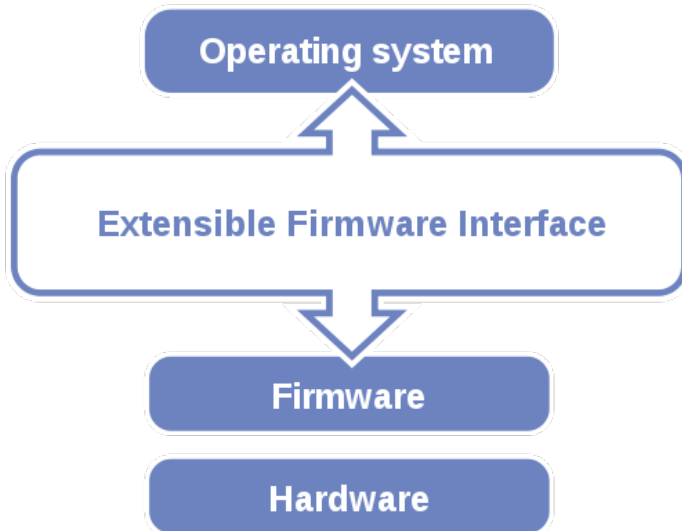


Figure 5-1. Extensible Firmware Interface Diagram

EFI BIOS provides an user interface that allows you to modify hardware configuration, e.g. change the system date and time, enable/disable a system component, determine bootable device priority, set up personal password, etc., which is convenient for engineers to perform modifications and customize the computer system and allows technicians to troubleshoot the occurred errors when the hardware is faulty.

The BIOS setup menu allows users to view and modify the BIOS settings for the computer. After the system is powered on, users can access the BIOS setup menu by pressing or <Esc> immediately while the POST message is running before the operating system is loading.

All the menu settings are described in details in this chapter.

5.2 Accessing Setup Utility

After the system is powered on, BIOS will enter the Power-On Self-Test (POST) routines and the POST message will be displayed:



Figure 5-2. POST Screen with AMI Logo

Press **** or **<Esc>** to access the Setup Utility program and the **Main** menu of the Aptio Setup Utility will appear on the screen as below:



BIOS Setup Menu Initialization Screen

You may move the cursor by <↑> and <↓> keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear on the right side of the screen.

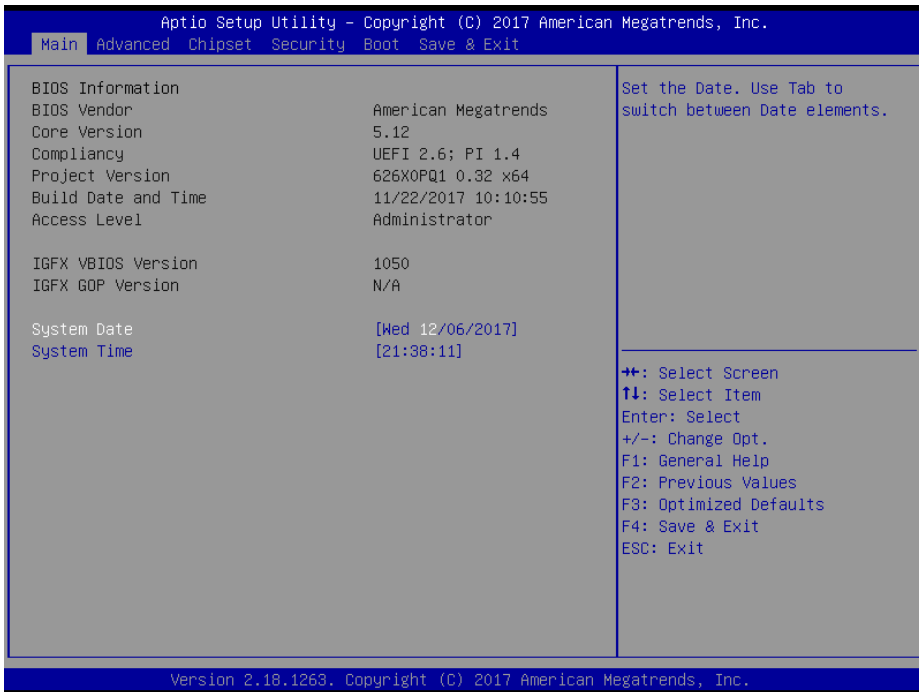
The language of the BIOS setup menu interface and help messages are shown in US English. You may use <↑> or <↓> key to select among the items and press <Enter> to confirm and enter the sub-menu. The following table provides the list of the navigation keys that you can use while operating the BIOS setup menu.

BIOS Setup Navigation Key	Description
<←> and <→>	Select a different menu screen (move the cursor from the selected menu to the left or right).
<↑> and <↓>	Select a different item (move the cursor from the selected item upwards or downwards)
<Enter>	Execute the command or select the sub-menu.
<F2>	Load the previous configuration values.
<F3>	Load the default configuration values.
<F4>	Save the current values and exit the BIOS setup menu.
<Esc>	Close the sub-menu. Trigger the confirmation to exit BIOS setup menu.

5.3 Main

Menu Path *Main*

The **Main** menu allows you to view the BIOS Information, change the system date and time, and view the user access privilege level. Use tab to switch between date elements. Use <↑> or <↓> arrow keys to highlight the item and enter the value you want in each item. This screen also displays the BIOS version (project) and BIOS Build Date and Time.



Main Screen

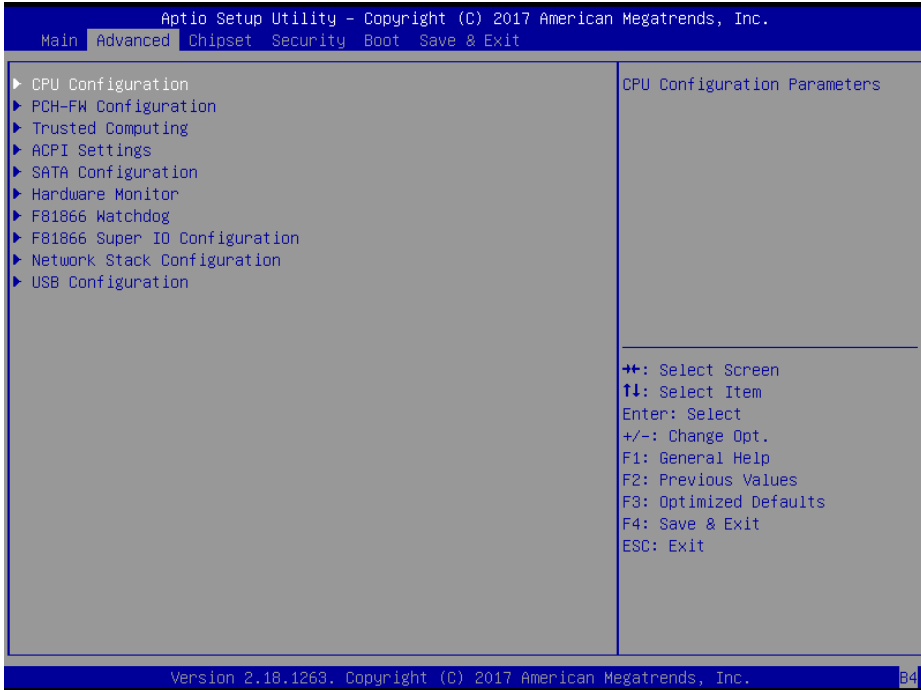
BIOS Setting	Options	Description/Purpose
BIOS Vendor	No changeable options	Displays the BIOS vendor.
Core Version	No changeable options	Displays the current BIOS core version.
Compliancy	No changeable options	Displays the current UEFI version.
Project Version	No changeable options	Displays the version of the BIOS currently installed on the platform.
Build Date and Time	No changeable options	Displays the date of current BIOS version.
Access Level	No changeable options	Displays the user access privilege level.
IGFX VBIOS Version	No changeable options	Displays the VBIOS version.

BIOS Setting	Options	Description/Purpose
IGFX GOP Version	No changeable options	Displays the GOP version.
System Date	Month, day, year	Sets the system date. The format is [Day Month/ Date/ Year]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it. The "Day" is automatically changed.
System Time	Hour, minute, second	Sets the system time. The format is [Hour: Minute: Second]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it.

5.4 Advanced

Menu Path *Advanced*

This menu provides advanced configurations such as CPU Configuration, PCH-FW Configuration, Trusted Computing, ACPI Settings, SATA Configuration, Hardware Monitor, F81866 Watchdog, F81866 Super IO Configuration, Network Stack Configuration and USB Configuration.



Advanced Menu Screen

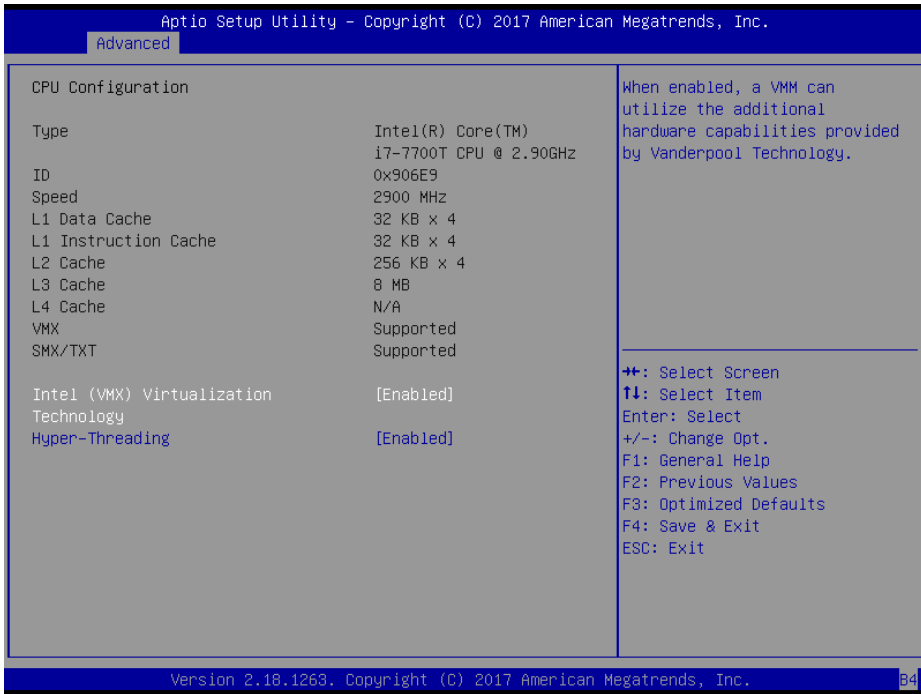
BIOS Setting	Options	Description/Purpose
CPU Configuration	Sub-Menu	CPU Configuration Parameters.
PCH-FW Configuration	Sub-Menu	Management Engine Technology Parameters.
Trusted Computing	Sub-Menu	Trusted Computing Settings.
ACPI Settings	Sub-Menu	System ACPI Parameters.
SATA Configuration	Sub-Menu	SATA Device Options Settings.
Hardware Monitor	Sub-Menu	Monitor hardware status.
F81866 Watchdog	Sub-Menu	F81866 Watchdog Parameters.
F81866 Super IO Configuration	Sub-Menu	System Super IO Chip Parameters.
Network Stack Configuration	Sub-Menu	Network Stack Settings.

BIOS Setting	Options	Description/Purpose
USB Configuration	Sub-Menu	USB Configuration Parameters.

5.4.1 Advanced – CPU Configuration

Menu Path *Advanced > CPU Configuration*

The **CPU Configuration** provides advanced CPU settings and some information about CPU.



CPU Configuration Screen

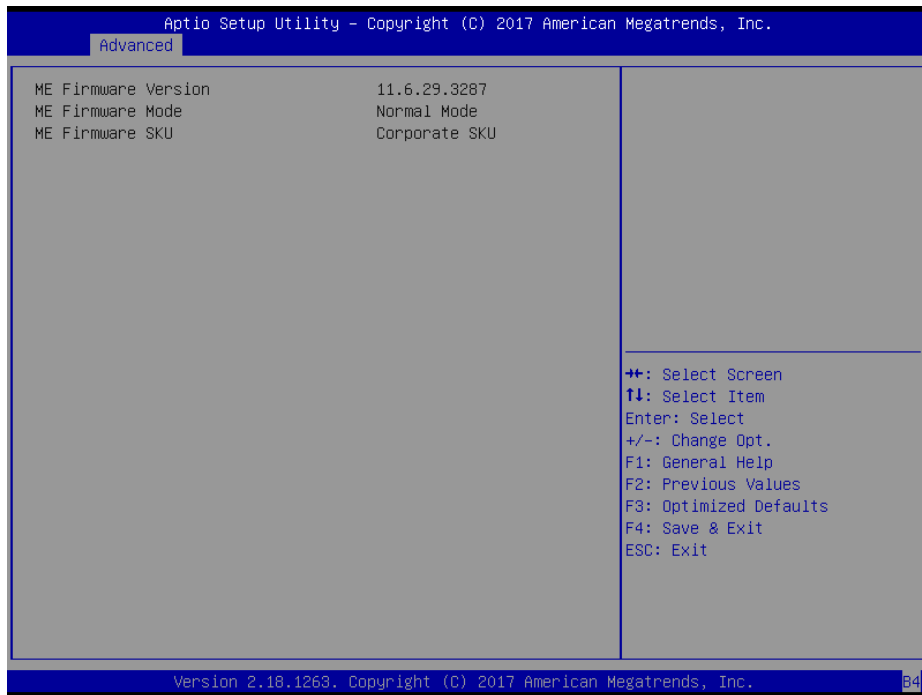
BIOS Setting	Options	Description/Purpose
CPU Configuration	No changeable options	Displays CPU configuration.
Type	No changeable options	Displays CPU Core.
ID	No changeable options	Displays CPU ID number.
Speed	No changeable options	Displays the CPU speed.
L1 Data Cache	No changeable options	Displays L1 Data Cache size.
L1 Instruction Cache	No changeable options	Displays L1 Instruction Cache size.
L2 Cache	No changeable options	Displays L2 Cache size.
L3 Cache	No changeable options	Displays L3 Cache size.
L4 Cache	No changeable options	Displays L4 Cache size.
VMX	No changeable options	Supports Intel VMX Technology.

BIOS Setting	Options	Description/Purpose
SMX/TXT	No changeable options	Reports if Intel Secure Mode Extensions Technology (SMX) /Trusted Execution Technology (TXT) is supported by the processor.
Intel (VMX) Virtualization Technology	<ul style="list-style-type: none"> - Disabled - Enabled 	When enabled, VMM can utilize the additional hardware capabilities provided by Vanderpool Technology. Previously codenamed "Vanderpool", VT-x represents Intel's technology for virtualization on the x86 platform.
Hyper-Threading	<ul style="list-style-type: none"> - Disabled - Enabled 	When disabled, only one thread per enabled core is enabled. Hyper Threading is Intel's term for its simultaneous multithreading implementation in their CPUs. Enable this function will improve parallelization of computation performed on PC microprocessor. For each processor core that is physically present, the operating system addresses two virtual processors, and shares the workload between them when possible.

5.4.2 Advanced – PCH-FW Configuration

Menu Path *Advanced > PCH-FW Configuration*

The **PCH-FW** allows users to view the information about ME (Management Engine) firmware information, such as ME firmware version, firmware mode and firmware SKU.



PCH-FW Configuration Screen

BIOS Setting	Options	Description/Purpose
ME Firmware Version	No changeable options	Displays the ME Firmware Version.
ME Firmware Mode	No changeable options	Displays the ME Firmware Mode.
ME Firmware SKU	No changeable options	Displays the ME Firmware SKU.

5.4.3 Advanced – Trusted Computing

Menu Path *Advanced > Trusted Computing*

The **Trusted Computing** allows users to enable/disable BIOS support for security device. The operating system will not show Security Device. The TCG EFI protocol and INT1A interface will not be available.



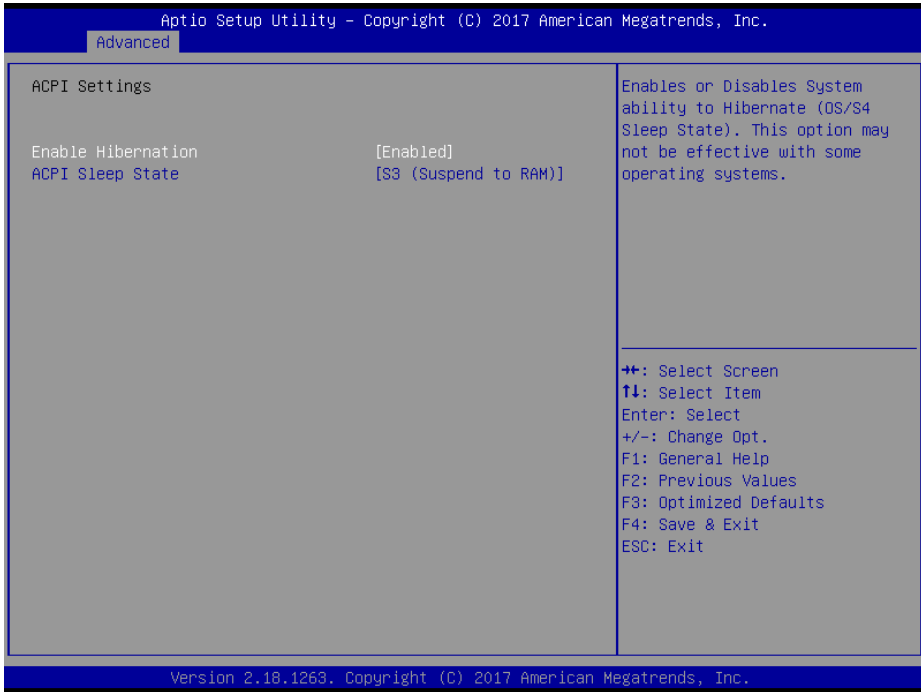
Trusted Computing Settings Screen

BIOS Setting	Options	Description/Purpose
Security Device Support	- Disable - Enable	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI Protocol and INT1A interface will not be available.

5.4.4 Advanced – ACPI Settings

Menu Path *Advanced > ACPI Settings*

The **ACPI Settings** allows users to configure relevant ACPI (Advanced Configuration and Power Management Interface) settings such as Enable/Disable Hibernation and ACPI sleep state.



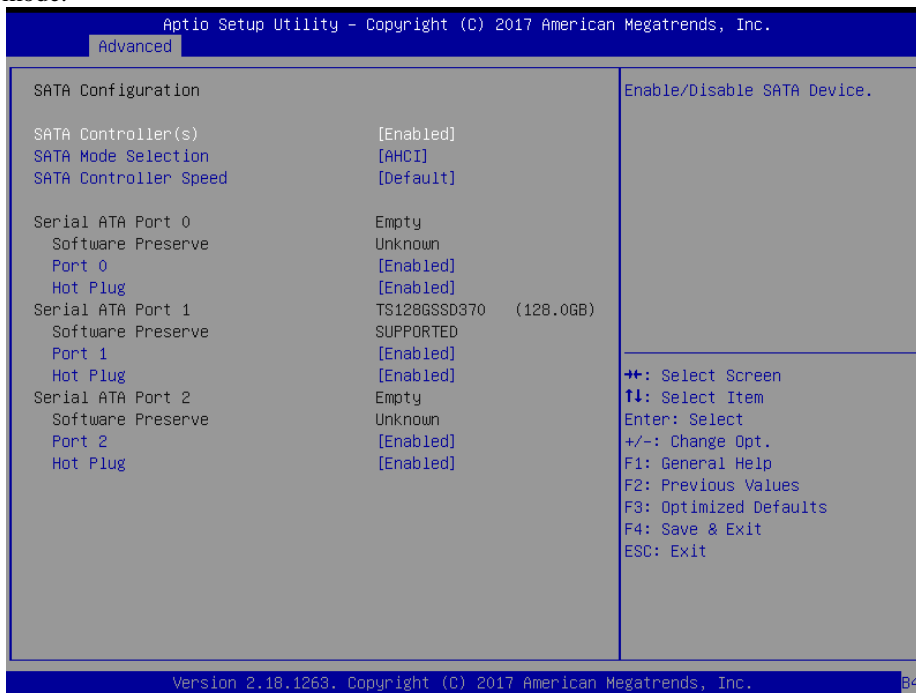
ACPI Settings Screen

BIOS Setting	Options	Description/Purpose
Enable Hibernation	- Disabled - Enabled	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	- Suspend Disabled - S3 (Suspend to RAM)	Selects the ACPI sleep state the system will enter when the SUSPEND button is pressed.

5.4.6 Advanced – SATA Configuration

Menu Path *Advanced > SATA Configuration*

The **SATA Configuration** allows users to enable / disable the SATA controller as well as the operational mode after the SATA controller is enabled. The following screen indicates the functions available when the SATA hard drive is set to work in AHCI mode.



SATA Configuration Screen

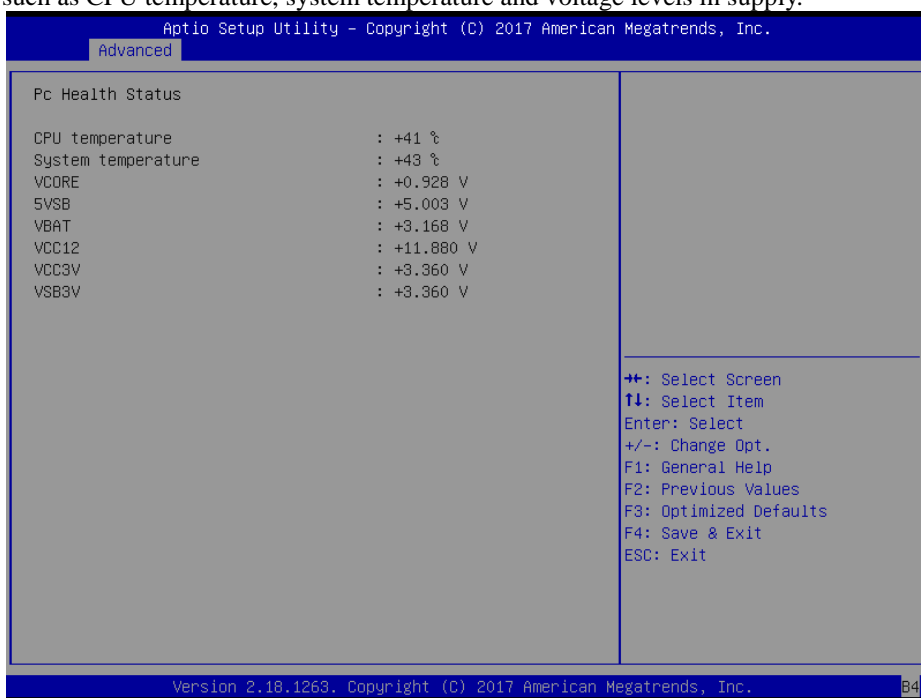
BIOS Setting	Options	Description/Purpose
SATA Controller(s)	- Disabled - Enabled	Enables or Disables the on-chip SATA Device. Default: Enabled.
SATA Mode Selection	- AHCI - Intel RST Premium	Determines how SATA controller(s) operate.
SATA Controller Speed	- Default - Gen1 - Gen2 - Gen3	Indicates the maximum speed the SATA controller can support.
Serial ATA Port 0 – 2	No changeable options	Displays the SATA device's name.
Software Preserve	No changeable options	Indicates whether the connected SATA device supports Software Setting Preservation (SSP).

BIOS Setting	Options	Description/Purpose
Port 0 – 2	- Disabled - Enabled	Enables or Disables SATA Port Device.
Hot Plug	- Disabled - Enabled	Enables or Disables SATA Port Device HotPlug function to designate a SATA port device as hot-pluggable.

5.4.7 Advanced – Hardware Monitor

Menu Path *Advanced > Hardware Monitor*

The **Hardware Monitor** allows users to monitor the health and status of the system such as CPU temperature, system temperature and voltage levels in supply.



Hardware Monitor Screen

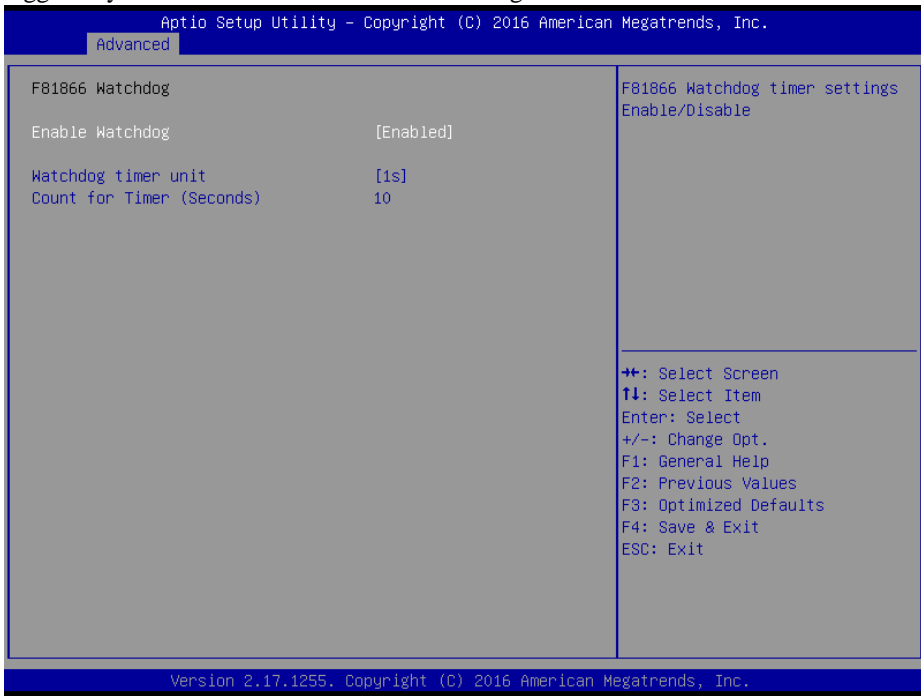
BIOS Setting	Options	Description/Purpose
CPU Temperature	No changeable options	Displays the processor's temperature.
System Temperature	No changeable options	Displays the system's temperature.
VCORE	No changeable options	Detects and displays the VCORE CPU voltage.
5VSB	No changeable options	Detects and displays VSB5V voltage.

BIOS Setting	Options	Description/Purpose
VBAT	No changeable options	Detects and displays the battery voltage.
VCC12	No changeable options	Detects and displays 12V voltage.
VCC3V	No changeable options	Detects and displays 3V voltage.
VSB3V	No changeable options	Detects and displays VSB3V voltage.

5.4.8 Advanced – F81866 Watchdog Configuration

Menu Path *Advanced > F81866 Watchdog*

If the system hangs or fails to respond, enable the F81866 watchdog function to trigger a system reset via the 255-level watchdog timer.

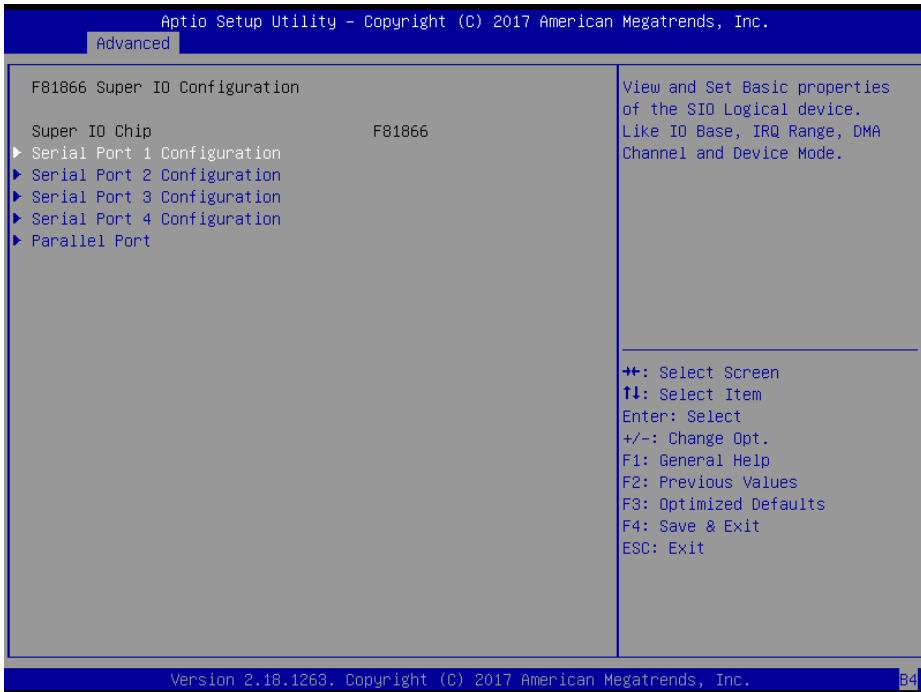


F81866 Watchdog Configuration Screen

BIOS Setting	Options	Description/Purpose
Enable Watchdog	- Enabled - Disabled	Enables/Disables F81866 Watchdog timer settings.
Watchdog timer unit	- 1s	Selects 1s (second) as the time unit of Watchdog timer.
Count for Timer (Seconds)	Numeric (from 1 to 255)	Sets the timeout for Watchdog timer. (Max. value: 255 seconds or minutes)

5.4.9 Advanced – F81866 Super IO Configuration

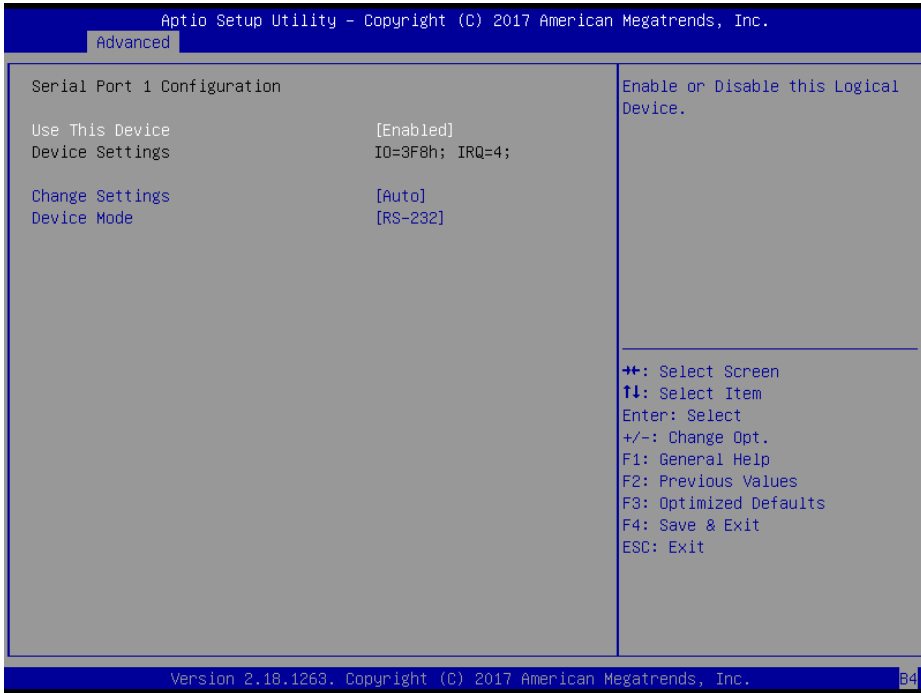
Menu Path *Advanced > F81866 Super IO Configuration*



F81866 Super IO Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port 1 Configuration	Sub-menu	Sets the parameters of Serial Port 1 (COMA).
Serial Port 2 Configuration	Sub-menu	Sets the parameters of Serial Port 2 (COMB).
Serial Port 3 Configuration	Sub-menu	Sets the parameters of Serial Port 3 (COMC).
Serial Port 4 Configuration	Sub-menu	Sets the parameters of Serial Port 4 (COMD).
Parallel Port	Sub-menu	Sets the parameters of Parallel Port.

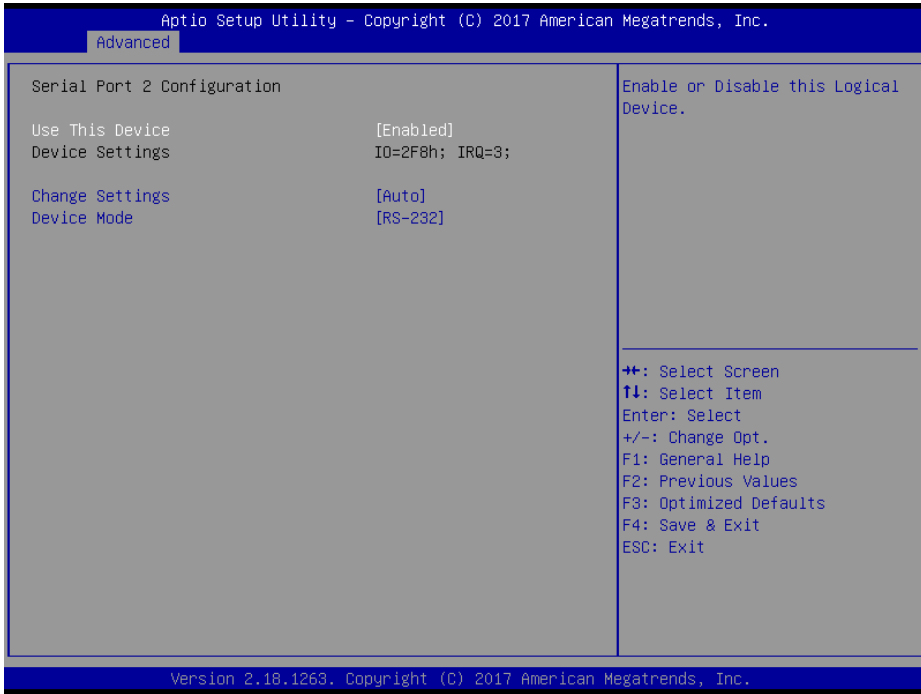
Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 1 Configuration*



Serial Port 1 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 1.
Device Settings	No changeable options	Displays the current settings of Serial Port 1.
Change Settings	- Auto - IO=3F8h; IRQ=4; - IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	Allows you to select specific IO address and IRQ for Serial Port 1.
Device Mode	- RS-232 - RS-422 - RS-485	Selects COM mode.

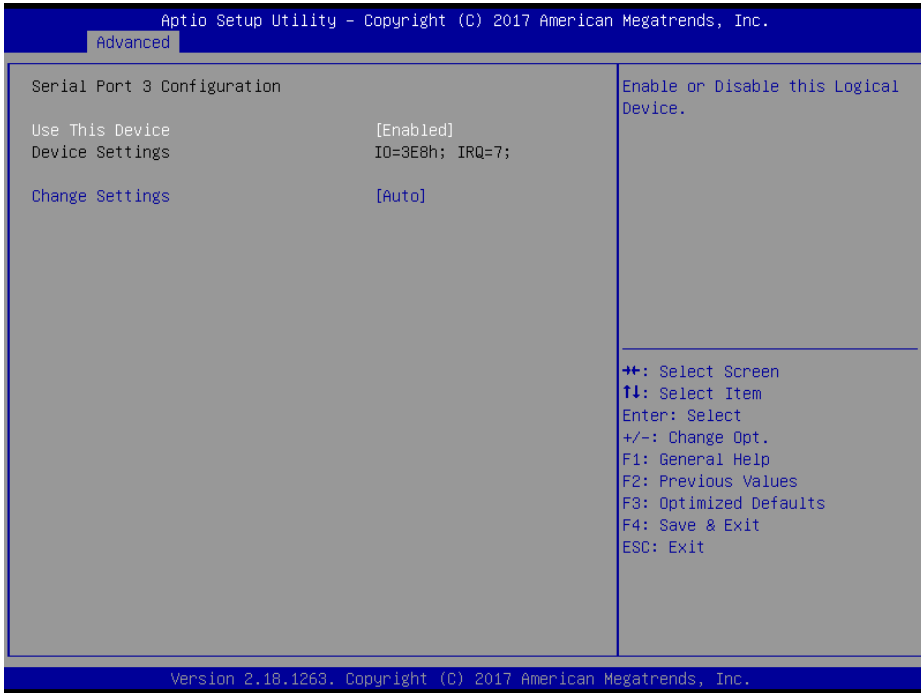
Menu Path *Advanced > F81866 Super IO Configuration >
Serial Port 2 Configuration*



Serial Port 2 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 2.
Device Settings	No changeable options	Displays the current settings of Serial Port 2.
Change Settings	- Auto - IO=2F8h; IRQ=3; - IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	Allows you to select specific IO address and IRQ for Serial Port 2.
Device Mode	- RS-232 - RS-422 - RS-485	RS-232/RS-422/RS-485 Selection.

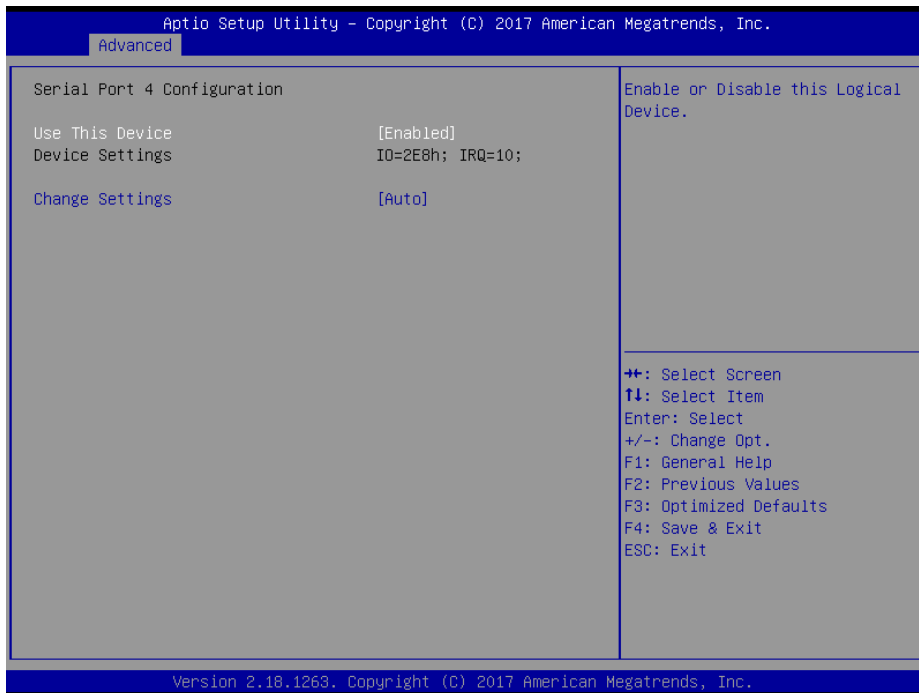
Menu Path *Advanced > F81866 Super IO Configuration >
Serial Port 3 Configuration*



Serial Port 3 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 3.
Device Settings	No changeable options	Displays the current settings of Serial Port 3.
Change Settings	- Auto - IO=3E8h; IRQ=7; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F0h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E0h; IRQ=3,4,5,6,7,9,10,11,12;	Allows you to select specific IO address and IRQ for Serial Port 3.

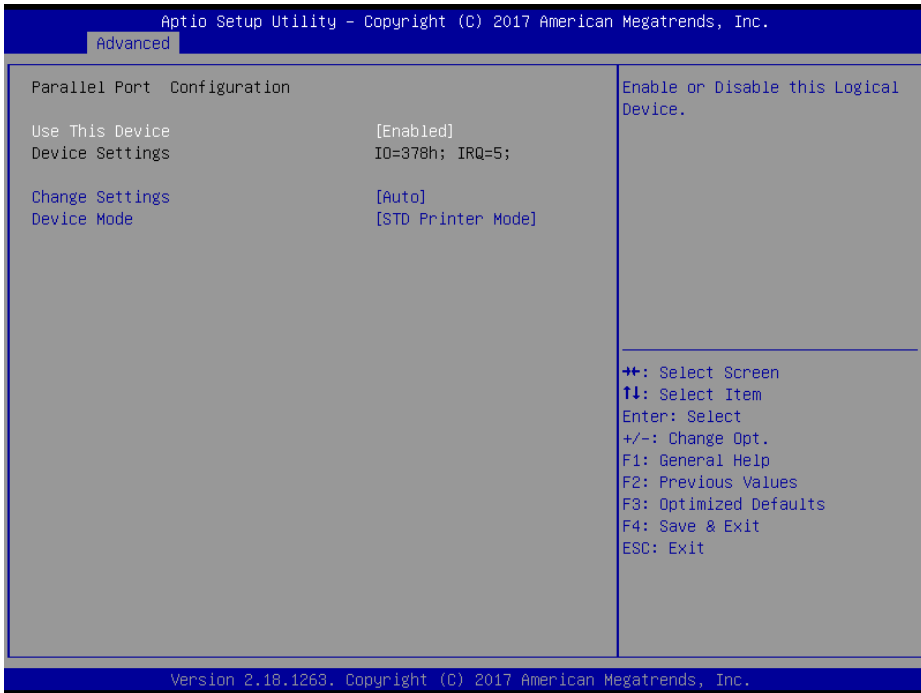
Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 4 Configuration*



Serial Port 4 Configuration Screen

BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables Serial Port 4.
Device Settings	No changeable options	Displays the current settings of Serial Port 4.
Change Settings	- Auto - IO=2E8h; IRQ=10; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F0h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E0h; IRQ=3,4,5,6,7,9,10,11,12;	Allows you to select specific IO address and IRQ for Serial Port 4.

Menu Path *Advanced > F81866 Super IO Configuration > Parallel Port Configuration*



Parallel Port Configuration Screen

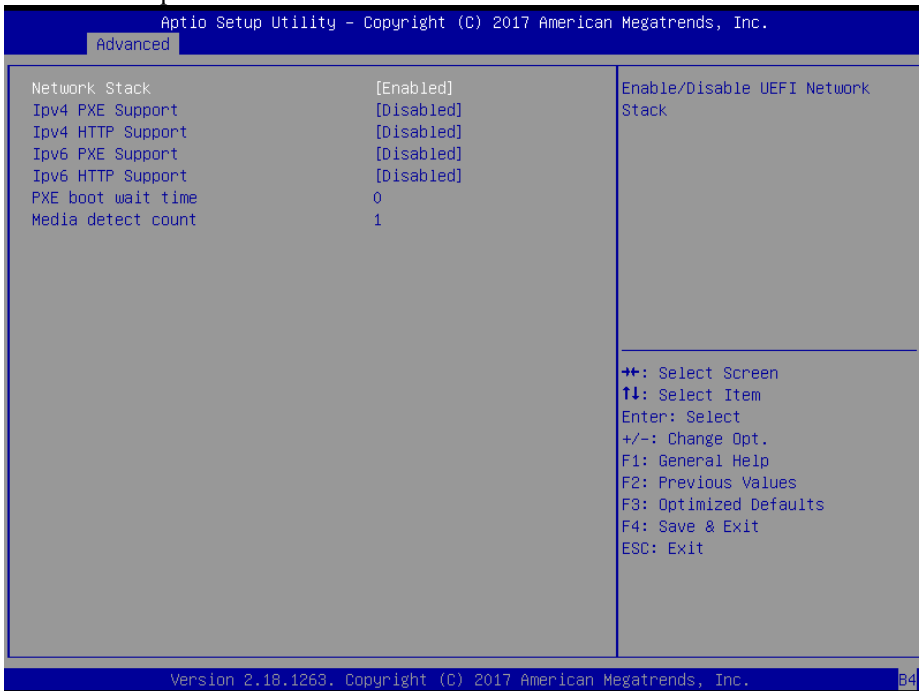
BIOS Setting	Options	Description/Purpose
Use This Device	- Disabled - Enabled	Enables or Disables parallel port.
Device Settings	No changeable options	Displays the current settings of Parallel Port.
Change Settings	- Auto - IO=378h; IRQ=5; - IO=378h; IRQ=5,6,7,9,10,11,12; - IO=278h; IRQ=5,6,7,9,10,11,12; - IO=3BCh; IRQ=5,6,7,9,10,11,12;	Allows you to select specific IO address and IRQ for Parallel Port.
Device Mode	- STD Printer Mode - SPP Mode - EPP-1.9 and SPP Mode - EPP-1.7 and SPP Mode - ECP Mode - ECP and EPP 1.9 Mode - ECP and EPP 1.7 Mode	Allows you to change Parallel Port mode.

5.4.10 Advanced – Network Stack Configuration

Menu Path *Advanced > Network Stack Configuration*

The **Network Stack Configuration** allows users to enable/disable UEFI Network Stack, IPv4/IPv6 PXE (Pre-Boot eXecution Environment) and IPv4/IPv6 HTTP (Hypertext Transfer Protocol) support and configure PXE boot wait time and detects the media presence.

PXE allows a workstation to boot from a server on a network prior to booting the operating system on the local hard drive. A PXE-enabled workstation connects its NIC to the LAN via a jumper, which keeps the workstation connected to the network even when the power is turned off.



Network Stack Configuration Screen

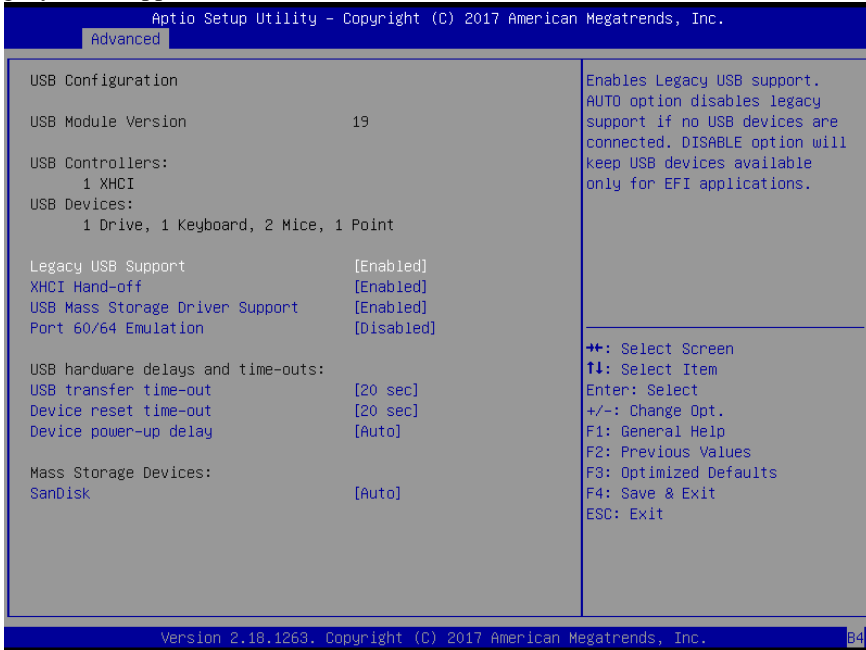
BIOS Setting	Options	Description/Purpose
Network Stack	- Disabled - Enabled	Enables or Disables UEFI Network Stack.
Ipv4 PXE Support	- Disabled - Enabled	Enables IPv4 PXE Boot Support. If disabled, IPv4 PXE boot option will not be created.
Ipv4 HTTP Support	- Disabled - Enabled	Enables IPv4 HTTP Boot Support. If disabled, IPv4 HTTP boot option will not

BIOS Setting	Options	Description/Purpose
		be created.
Ipv6 PXE Support	- Disabled - Enabled	Enables IPv6 PXE Boot Support. If disabled, IPv6 PXE boot option will not be created.
Ipv6 HTTP Support	- Disabled - Enabled	Enable IPv6 HTTP Boot Support. If disabled, IPv6 HTTP boot option will not be created.
PXE boot wait time	Numeric (from 0 to 5)	Number of seconds to wait for PXE boot to abort after the Esc key is pressed.
Media detect count	Numeric (from 1 to 50)	Number of times that the media presence will be checked.

5.4.11 Advanced – USB Configuration

Menu Path *Advanced > USB Configuration*

The **USB Configuration** allows users to configure advanced USB settings such as Legacy USB support.



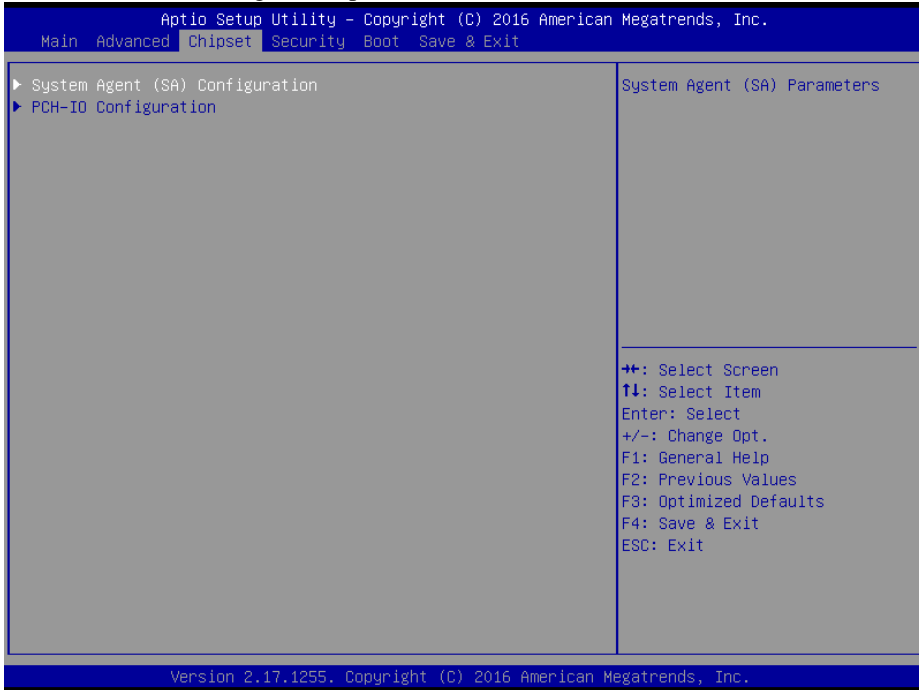
USB Configuration Screen

BIOS Setting	Options	Description/Purpose
Legacy USB Support	- Disabled - Enabled - Auto	Sets to “Enabled” if you want to use USB devices with the legacy operating systems that do not support USB.
XHCI Hand-off	- Enabled - Disabled	This is a workaround for Oses without XHCI hand-off support.
USB Mass Storage Driver Support	- Enabled - Disabled	Enables or Disables USB mass storage driver support.
Port 60/64 Emulation	- Disabled - Enabled	Enables I/O port 60h/64h emulation support.
USB transfer time-out	1- 20 sec	The time-out value for Control, Bulk, and Interrupt transfers.
Device reset time-out	1- 20 sec	USB mass storage device Start Unit command time-out.
Device power-up delay	- Auto - Manual	Maximum time the device will take before it properly reports itself to the Host Controller.

5.5 Chipset

Menu Path *Chipset*

This menu allows users to configure advanced Chipset settings such as System Agent (SA) and PCH-IO configuration parameters.



Chipset Screen

BIOS Setting	Options	Description/Purpose
System Agent (SA) Configuration	Sub-menu	System Agent (SA) parameters.
PCH-IO Configuration	Sub-menu	PCH parameters.

5.5.1 Chipset – System Agent (SA) Configuration

Menu Path *Chipset > System Agent (SA) Configuration*

The **System Agent Configuration** allows users to display DRAM information on the platform as well as configure graphics settings.



System Agent (SA) Configuration Screen

BIOS Setting	Options	Description/Purpose
System Agent (SA) Configuration	No changeable options	Configures System Agent Configuration.
SA PCIe Code Version	No changeable options	Displays the SA PCIe Code Version.
VT-d	No changeable options	Indicates whether Intel's VT-d (Virtualization Technology for Directed I/O) capability is supported. VT-d extends Intel's Virtualization Technology (VT) roadmap by providing hardware assists for virtualization solution, and helps end users improve security and reliability of the systems and also improves performance of I/O devices in virtualized environment.

BIOS Setting	Options	Description/Purpose
Memory Configuration	Sub-menu	Displays the DRAM information on the platform.
Graphics Configuration	Sub-menu	Configures Graphics Settings.
VT-d	- Disabled - Enabled	Enables or Disables VT-d function.

Chipset – SA Configuration - Memory Configuration

Menu Path *Chipset > System Agent (SA) Configuration > Memory Configuration*

The **Memory Configuration** allows users to check for the information about the memory frequency, total DRAM size, memory (RAM) timings and latency, etc.

Apdio Setup Utility - Copyright (C) 2017 American Megatrends, Inc.
Chipset

Memory Configuration

Memory RC Version 2.0.0.0
Memory Frequency 2133 MHz
Memory Timings (tCL-tRCD-tRP-tRAS) 15-15-15-36

Channel 0 Slot 0 Populated & Enabled
Size 8192 MB (DDR4)
Number of Ranks 2
Manufacturer UnKnown

Channel 0 Slot 1 Not Populated / Disabled

Channel 1 Slot 0 Populated & Enabled
Size 8192 MB (DDR4)
Number of Ranks 2
Manufacturer UnKnown

Channel 1 Slot 1 Not Populated / Disabled

++: Select Screen
F1: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit

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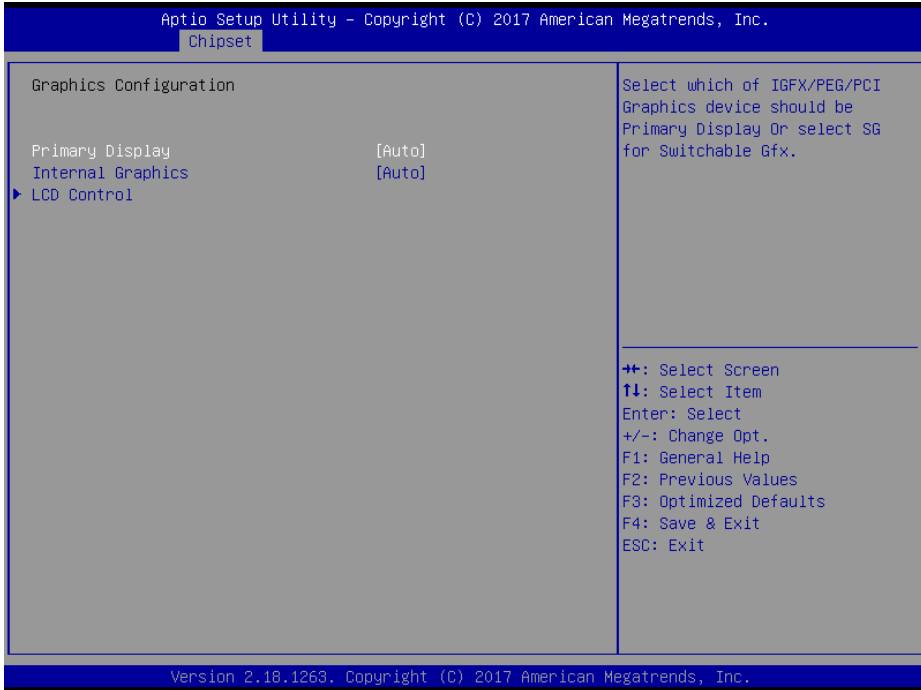
Memory Configuration Screen

BIOS Setting	Options	Description/Purpose
Memory RC Version	No changeable options	Displays the Memory RC Version.
Memory Frequency	No changeable options	Displays the Frequency of Memory.
Memory Timings (tCL-tRCD-tRP-tRAS)	No changeable options	Displays the Memory (RAM) timings and latency.

BIOS Setting	Options	Description/Purpose
		<ul style="list-style-type: none"> • CAS Latency (tCL) - This is the most important memory timing. CAS stands for Column Address Strobe. If a row has already been selected, it tells us how many clock cycles we'll have to wait for a result (after sending a column address to the RAM controller). • Row Address (RAS) to Column Address (CAS) Delay (tRCD) - Once we send the memory controller a row address, we'll have to wait this many cycles before accessing one of the row's columns. So, if a row hasn't been selected, this means we'll have to wait $tRCD + tCL$ cycles to get our result from the RAM. • Row Precharge Time (tRP) - If we already have a row selected, we'll have to wait this number of cycles before selecting a different row. This means it will take $tRP + tRCD + tCL$ cycles to access the data in a different row. • Row Active Time (tRAS) - This is the minimum number of cycles that a row has to be active for to ensure we'll have enough time to access the information that's in it. This usually needs to be greater than or equal to the sum of the previous three latencies ($tRAS = tCL + tRCD + tRP$).
Channel 0 Slot 0	No changeable options	Displays if Channel 0 Slot 0 is populated/enabled or not.
Channel 0 Slot 1	No changeable options	Displays if Channel 0 Slot 1 is populated/enabled or not.
Channel 1 Slot 0	No changeable options	Displays if Channel 1 Slot 0 is populated/enabled or not.
Channel 1 Slot 1	No changeable options	Displays if Channel 1 Slot 1 is populated/enabled or not.
Size	No changeable options	Displays the memory size.
Number of Ranks	No changeable options	Displays the Rank number.
Manufacturer	No changeable options	Displays the name of the memory manufacturer.

Chipset – System Agent (SA) Configuration – Graphics Configuration

Menu Path *Chipset > System Agent (SA) Configuration > Graphics Configuration*

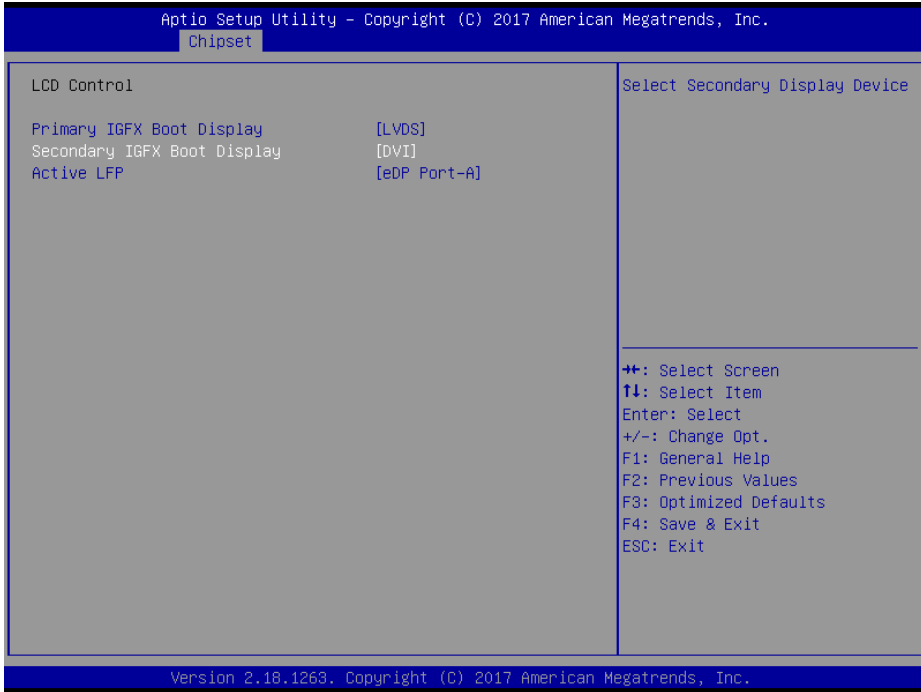


Graphics Configuration Screen

BIOS Setting	Options	Description/Purpose
Primary Display	- Auto - IGFX - PEG - PCI - SG	Selects which of IGFX/PEG/PCI Graphics device should be Primary Display or select SG for switchable Gfx.
Internal Graphics	- Auto - Disabled - Enabled	Keeps IGFX enabled based on the setup options.
LCD Control	Sub-menu	LCD Control sub-menu.

Menu Path *Chipset > System Agent (SA) Configuration > Graphics Configuration > LCD Control*

The **LCD Control** allows users to select the primary and secondary display device, configure LVDS resolution and enable/disable LVDS panel.



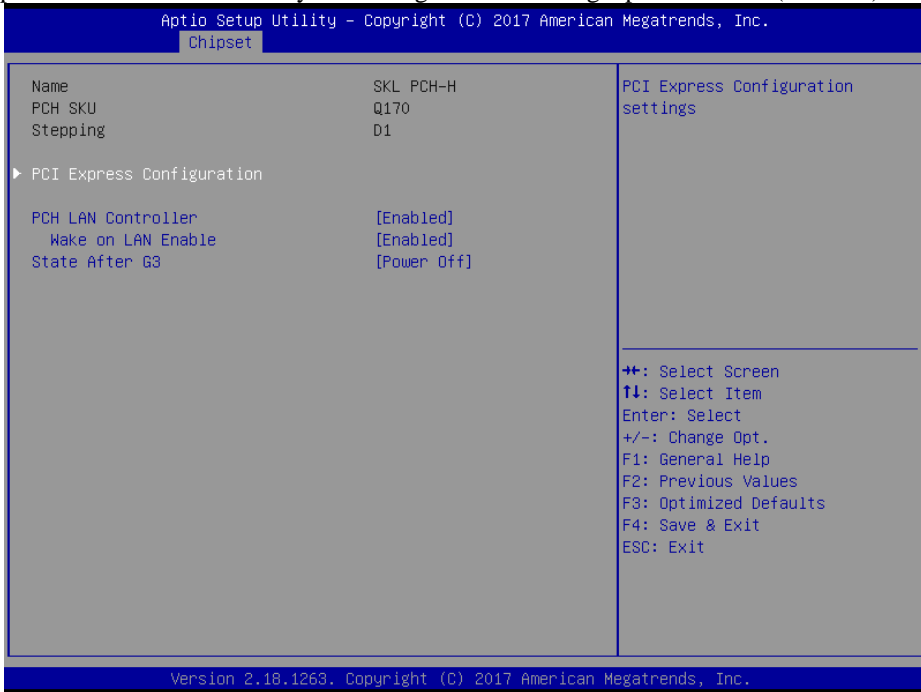
LCD Control Screen

BIOS Setting	Options	Description/Purpose
Primary IGFX Boot Display	- VBIOS default - DVI - LVDS - DVI-I to VGA	Selects Primary Display device.
Secondary IGFX Boot Display	- DVI - LVDS - DVI - DVI-I to VGA	Selects Secondary Display device.
Active LFP	- No LVDS - eDP Port-A	Enables or Disables LVDS panel.

5.5.2 Chipset – PCH-IO Configuration

Menu Path *Chipset > PCH-IO Configuration*

The **PCH-IO** Configuration allows users to set PCI Express configuration parameters, enable/disable PCH LAN Controller and Wake-On-LAN function and determine the power on/off state that the system will go into following a power failure (G3 state).



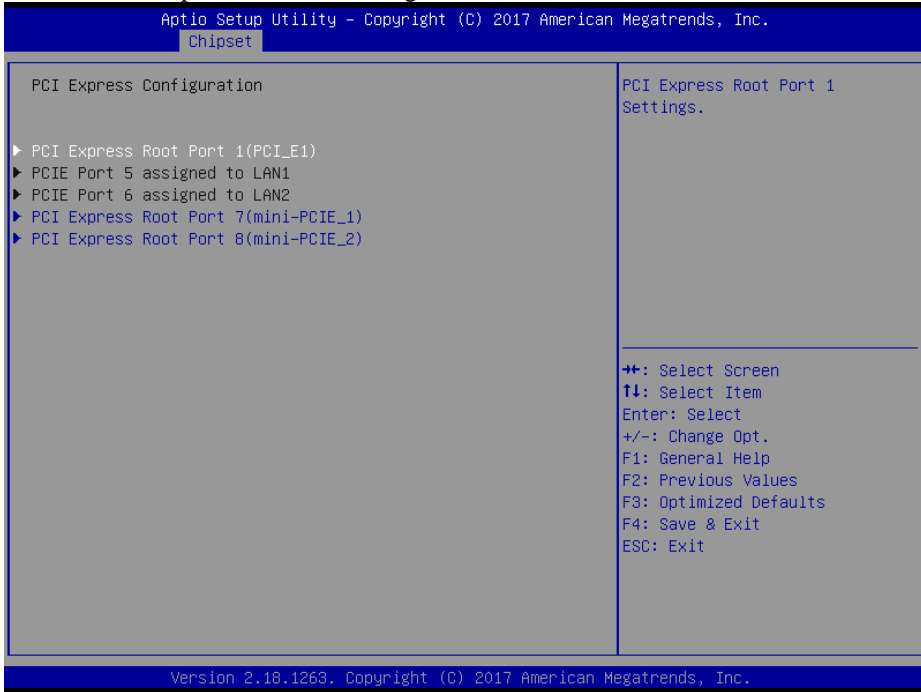
PCH-IO Configuration Screen

BIOS Setting	Options	Description/Purpose
Name	No changeable options	To display the PCH name.
PCH SKU	No changeable options	To display the PCH SKU version.
Stepping	No changeable options	To display the PCH stepping number.
PCI Express Configuration	Sub-menu	Configures PCI Express Configuration settings.
PCH LAN Controller	- Disabled - Enabled	Enables or Disables onboard NIC.
Wake on LAN Enable	- Disabled - Enabled	Enables or Disables integrated LAN to wake up the system. Default: Enabled.
State After G3	- Power On - Power Off	Specifies the Power On/Off state that the system will go into when the power is re-applied following a power failure (G3 state).

Chipset – PCH-IO Configuration – PCI Express Configuration

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration*

The **PCI Express Configuration** allows users to configure PCI Express root port 1 and Mini PCI Express Ports 1-2 settings.

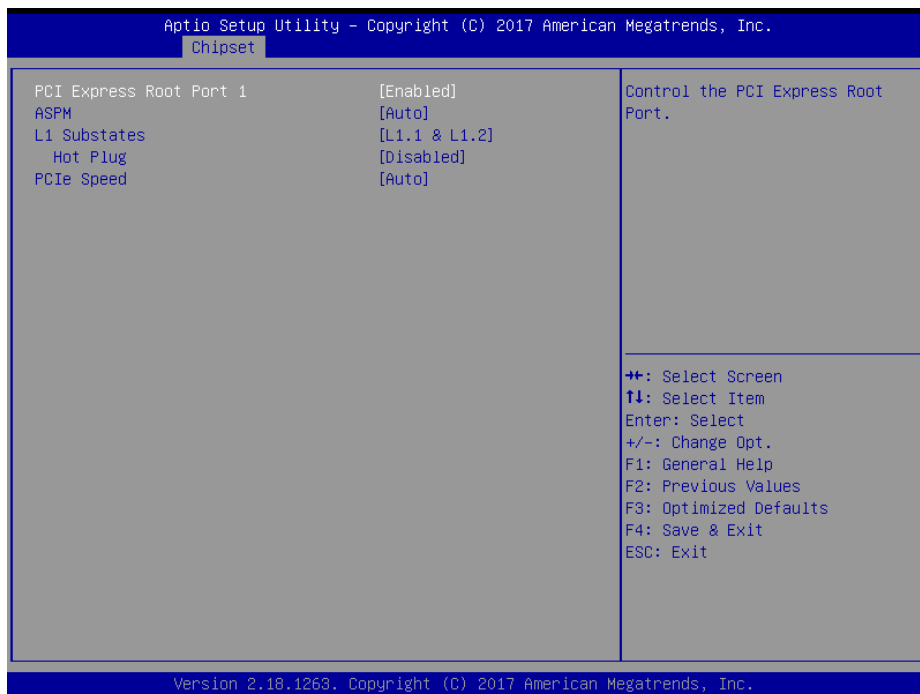


PCI Express Configuration Screen

BIOS Setting	Options	Description/Purpose
PCI Express Root Port 1(PCI_E1)	Sub-menu	Allows users to configure PCI Express Root Port 1 settings.
PCI Express Root Port 5 assigned to LAN1	No changeable options	Assigns LAN1 to PCI Express Port 5.
PCI Express Root Port 6 assigned to LAN2	No changeable options	Assigns LAN2 to PCI Express Port 6.
PCI Express Root Port 7(mini-PCI_E_1)	Sub-menu	Allows users to configure Mini PCI Express Port 1 settings.
PCI Express Root Port 8(mini-PCI_E_2)	Sub-menu	Allows users to configure Mini PCI Express Port 2 settings.

BIOS Setting	Options	Description/Purpose
Root Port 8(mini-PCIE_2)		Express Port 2 settings.

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration > PCI Express Root Port 1 (PCI_E1)*

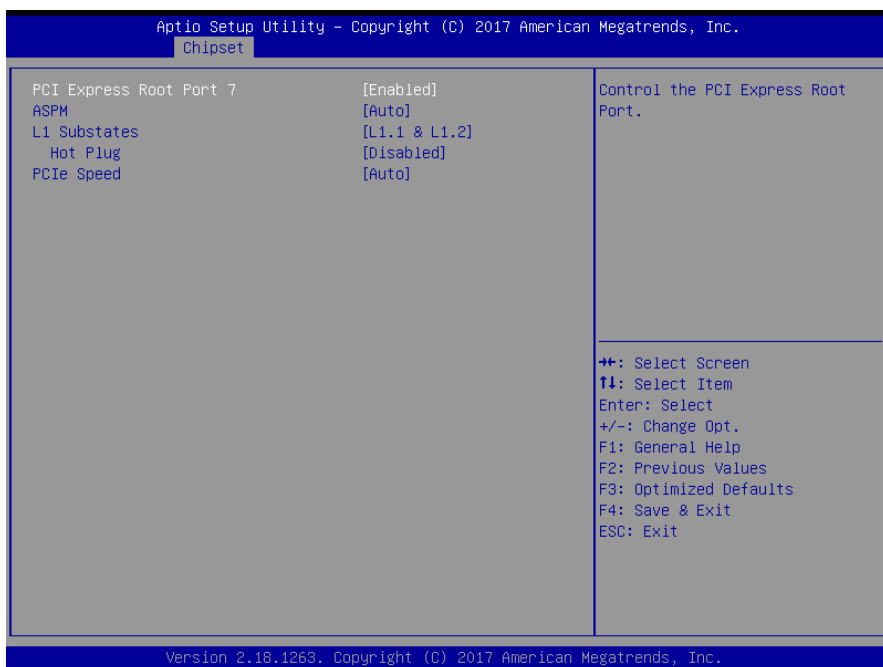


PCI Express Root Port 1 (PCI_E1) Configuration Screen

BIOS Setting	Options	Description/Purpose
PCI Express Root Port 1	- Disabled - Enabled	Controls the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1 - Auto	Sets the PCI Express ASPM (Active-State Power Management) Level. The option allows users to set the lower power mode that activates when the bus is not being used.
L1 Substates	- Disabled - L1.1 - L1.2	PCI Express L1 Substates settings.

BIOS Setting	Options	Description/Purpose
	- L1.1 & L1.2	
Hot Plug	- Disabled - Enabled	Enables or Disables PCI Express Hot Plug.
PCIe Speed	- Auto - Gen1 - Gen2 - Gen3	Selects PCI Express Port Speed.

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration > PCI Express Root Port 7 (mini-PCIE_1)*

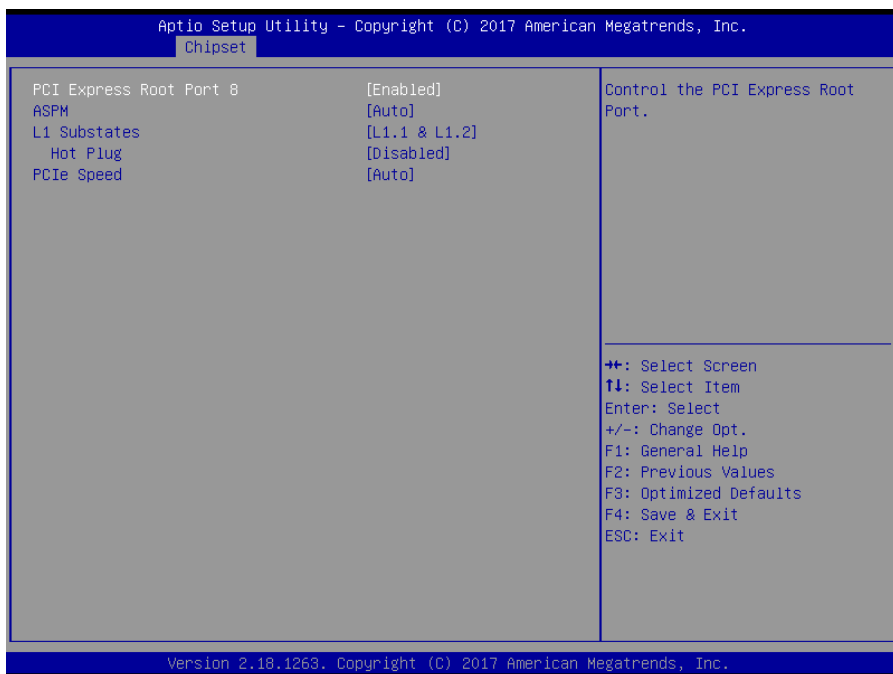


PCI Express Root Port 7 (mini-PCIE_1) Configuration Screen

BIOS Setting	Options	Description/Purpose
PCI Express Root Port 7	- Disabled - Enabled	Controls the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1	Sets the ASPM level.

BIOS Setting	Options	Description/Purpose
	- Auto	
L1 Substates	- Disabled - L1.1 - L1.2 - L1.1 & L1.2	PCI Express L1 Substates settings.
Hot Plug	- Disabled - Enabled	Enables or Disables PCI Express Hot Plug.
PCIe Speed	- Auto - Gen1 - Gen2 - Gen3	Selects PCI Express Port speed.

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration > PCI Express Root Port 8 (mini-PCIE_2)*



PCI Express Root Port 8 (mini-PCIE_2) Configuration Screen

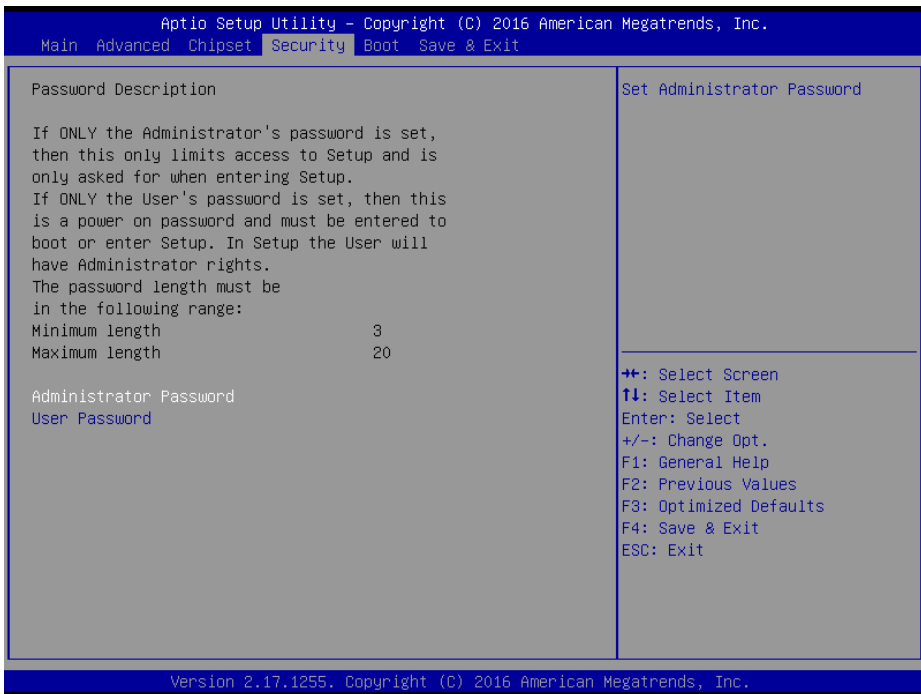
BIOS Setting	Options	Description/Purpose
PCI Express Root Port 8	- Disabled - Enabled	Controls the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1 - Auto	Sets the ASPM level.
L1 Substates	- Disabled - L1.1 - L1.2 - L1.1 & L1.2	PCI Express L1 Substates settings.
Hot Plug	- Disabled - Enabled	Enables or Disables PCI Express Hot Plug.
PCIe Speed	- Auto - Gen1 - Gen2 - Gen3	Selects PCI Express Port speed.

5.6 Security

Menu Path *Security*

From the **Security** menu, you are allowed to create, change or clear the administrator password. You will be asked to enter the configured administrator password before you can access the Setup Utility.

By setting an administrator password, you will prevent other users from changing your BIOS settings. You can configure an Administrator password and then configure a user password. An administrator has much more privileges over the settings in the Setup utility than a user. Heed that a user password does not provide access to most of the features in the Setup utility.



Security Screen

BIOS Setting	Options	Description/Purpose
Administrator Password	Password can be 3-20 alphanumeric characters.	Specifies the administrator password.
User Password	Password can be 3-20 alphanumeric characters.	Specifies the user password.

Create an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Enter the password you want to create. A password can be 3-20 alphanumeric characters. After you have configured the password, press <Enter> to confirm.
3. Type the new password again and press <Enter>.

Change an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Select the Administrator Password or User Password that you want to change. A password can be 3-20 alphanumeric characters. After you have changed the password, press <Enter> to confirm.
3. Type the changed password again and press <Enter>.

Remove an Administrator or User Password

1. Select the **Administrator Password / User Password** option from the Security menu and press <Enter>, and the password dialog entry box appears.
2. Select the configured Administrator Password or User Password that you want to delete. Leave the dialog box blank and press <Enter>.
3. Press <Enter> again when the password confirmation box appears.

5.7 Boot

Menu Path Boot

This menu provides control items for system boot configuration such as setting setup prompt timeout, enabling/disabling quiet boot and fast boot, changing the boot order from the available bootable device(s) and BBS (BIOS Boot Specification) option priorities, and setting CSM (Compatibility Support Module) configuration parameters to support legacy BIOS operation systems, various bootable devices and add-on devices for achieving better compatibility.



Boot Screen

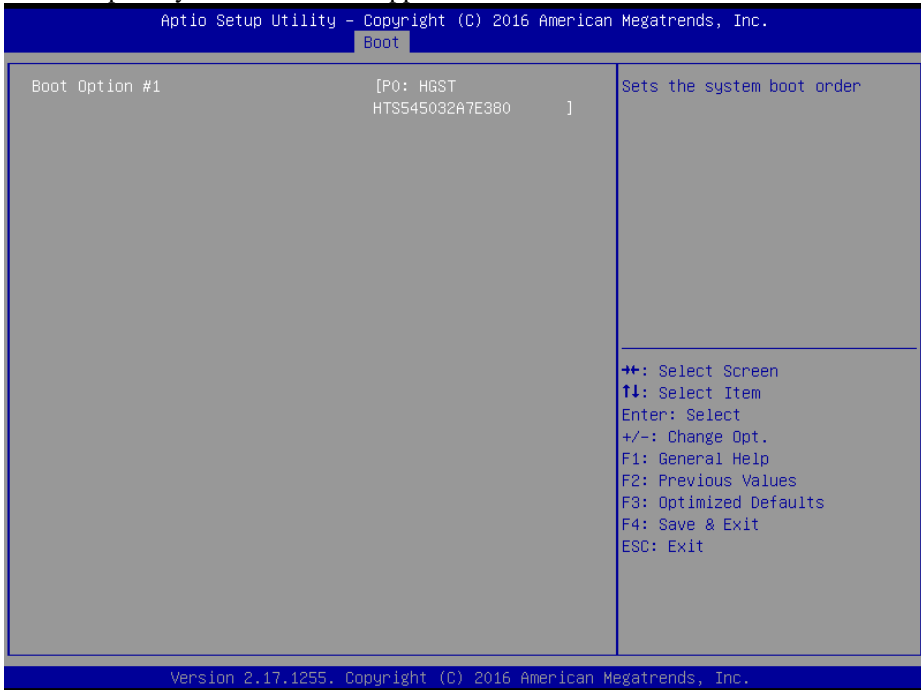
BIOS Setting	Options	Description/Purpose
Setup Prompt Timeout	Numeric (from 1 to 65535)	Number of seconds to wait for setup activation key.
Bootup NumLock State	- On - Off	Selects the NumLock state after the system is powered on. <ul style="list-style-type: none"> On: Enables the NumLock function automatically after the system is powered on. Off: Disables the NumLock function after the system is powered on.

BIOS Setting	Options	Description/Purpose
Quiet Boot	- Disabled - Enabled	Enables or Disables Quiet Boot options. When this option is set to “Disabled”, BIOS will display normal POST messages.
Boot Option #1~#n	- [Drive(s)] - Disabled	Allows users to change the boot order from the available device(s). Note that in the menu displayed, you will only see the device with the highest priority for a specific boot device type.
Fast Boot	- Disabled - Enabled	Enables or Disables Fast Boot options.
Hard Drive BBS Priorities	Sub-Menu	Defines the boot order for all the hard drives connected to the system, e.g. SATA, USB drive.
CSM Configuration	Sub-Menu	CSM configuration: Enable/Disable, Option ROM execution settings, etc.

5.7.1 Boot – Network Device BBS Priorities

Menu Path *Boot > Network Device BBS Priorities*

Select **Network Device BBS Priorities** from the **Boot** menu to configure the boot order and priority for devices that support Boot from LAN function.



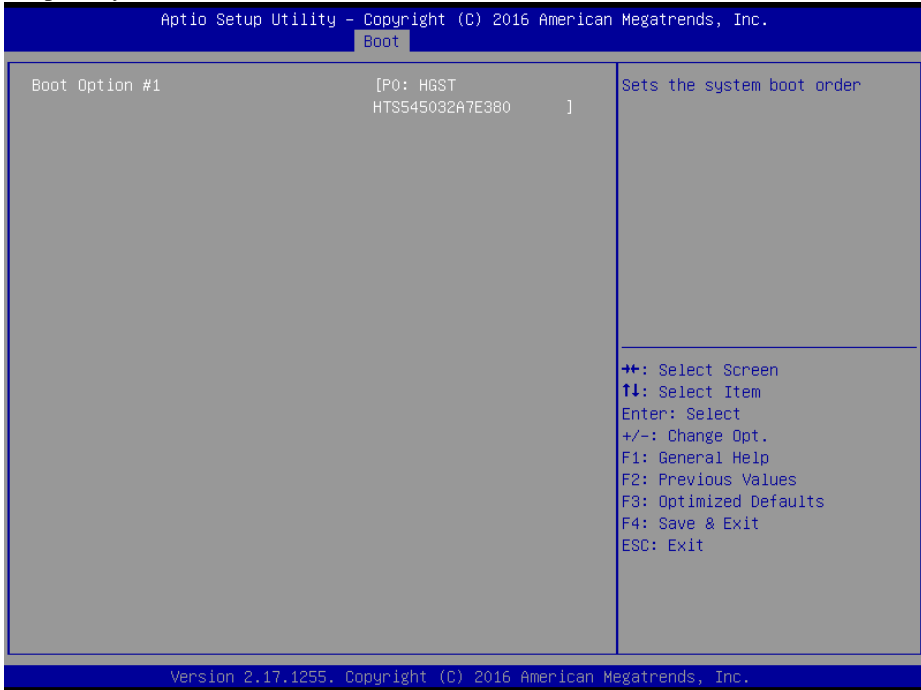
Network Device BBS Priorities Screen

BIOS Setting	Options	Description/Purpose
Boot Option #1~#n	- [Device(s)] - Enabled	Sets the system boot order for devices that support Boot from LAN function.

5.7.2 Boot – Hard Drive BBS Priorities

Menu Path *Boot > Hard Drive BBS Priorities*

Select **Hard Drive BBS Priorities** from the **Boot** menu to configure the boot order and priority of the available hard drives.



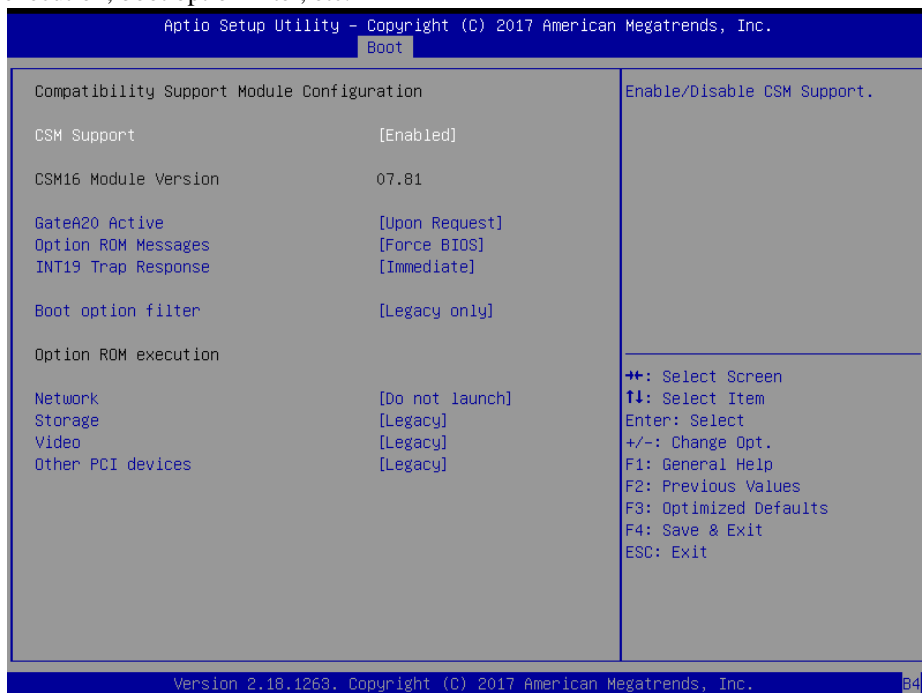
Hard Drive BBS Priorities Screen

BIOS Setting	Options	Description/Purpose
Boot Option #1~#n	- [Drive(s)] - Enabled	Set the system boot order for hard drives. Press Enter to enter the sub-menu and press <↑> or <↓> arrow keys to select the device. Another way is to press <+> or <-> to move the selected device up/down in the priority list.

5.7.3 Boot – CSM Configuration

Menu Path *Boot > CSM Configuration*

The **CSM Configuration** provides advanced CSM (Compatibility Support Module) configurations such as Enable/Disable CSM Support, configure Option ROM execution, boot option filter, etc.



CSM Configuration Screen

BIOS Setting	Options	Description/Purpose
CSM Support	- Disabled - Enabled	Enables or Disables CSM Support.
CSM16 Module Version	No changeable options	Displays the CSM 16 Module version.
GateA20 Active	-Upon Request -Always	<ul style="list-style-type: none"> • Upon Request: GA20 can be disabled using BIOS services. • Always: This option is useful when any RT code is executed above 1MB.

BIOS Setting	Options	Description/Purpose
Option ROM Messages	-Force BIOS -Keep Current	Sets display mode for Option ROM.
INT19 Trap Response	- Immediate - Postponed	<ul style="list-style-type: none"> • Immediate: Execute the trap immediately. • Postponed: Execute the trap during legacy boot.
Boot option filter	- UEFI and Legacy - Legacy only - UEFI only	This option controls Legacy/UEFI ROMs priority.
Network	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy PXE OpROM.
Storage	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Storage OpROM.
Video	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Video OpROM.
Other PCI devices	- Do not launch - UEFI - Legacy	Determines OpROM execution policy for devices other than Network, Storage or Video.

5.8 Save & Exit

Menu Path *Save & Exit*

The **Save & Exit** allows users to save or discard changed BIOS settings as well as load factory default settings.

Save Changed BIOS Settings

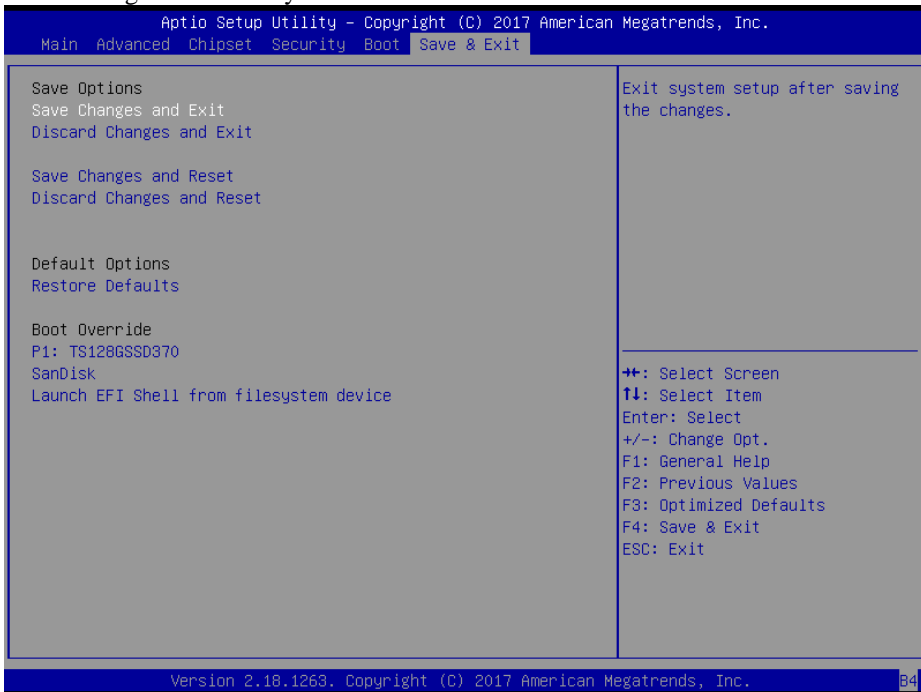
To save and validate the changed BIOS settings, select **Save Changes** from the **Save & Exit** menu, or you can select **Save Changes and Exit** (or press **F4**) to validate the changes and then exit the system. Select **Save Changes and Reset** to validate the changed BIOS settings and then restart the system

Discard Changed BIOS Settings

To cancel the BIOS settings you have previously configured, select **Discard Changes and Exit** from this menu, or simply press **Esc** to exit the BIOS setup. You can also select **Discard Changes and Reset** to discard any changes you have made and restore the factory BIOS defaults.

Restore Defaults

You may simply press **F3** at any time to load the optimized values which resets all BIOS settings to the factory defaults.



Save & Exit Screen

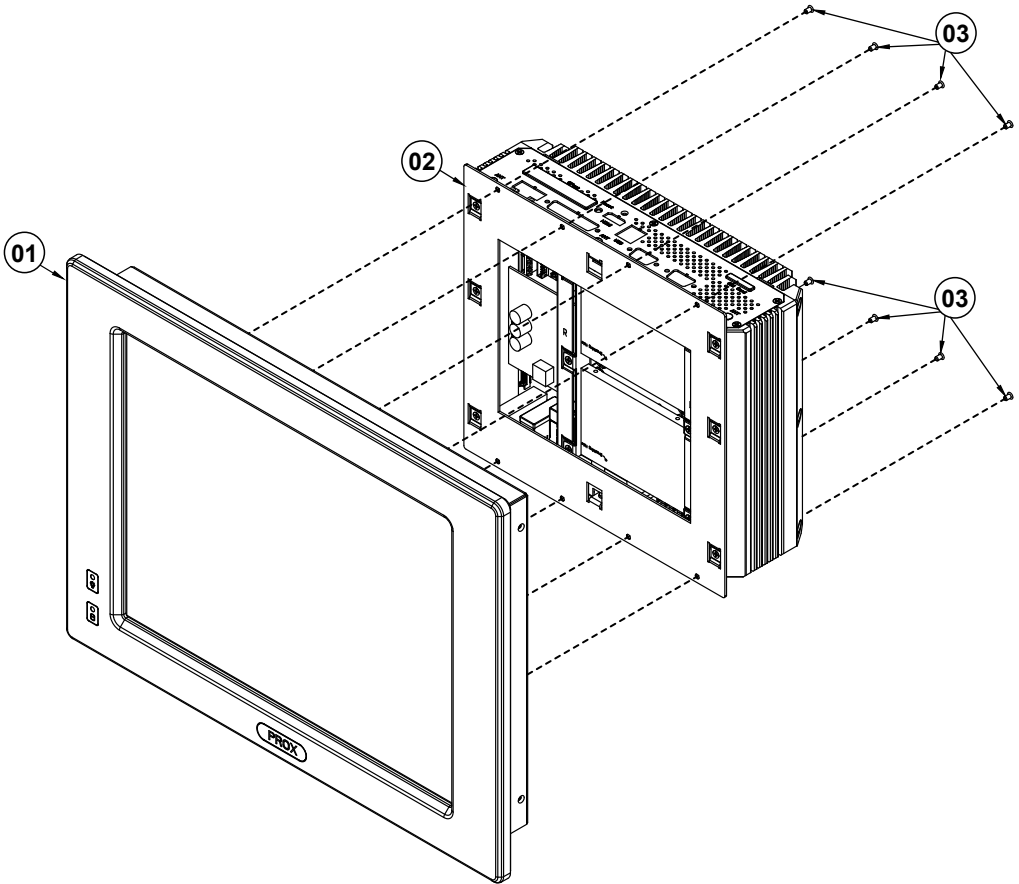
BIOS Setting	Options	Description/Purpose
Save Changes and Exit	No changeable options	Exits the system and saves the changes in NVRAM.
Discard Changes and Exit	No changeable options	Exits the system without saving any changes configured in BIOS settings.
Save Changes and Reset	No changeable options	Saves the changes in NVRAM and resets the system.
Discard Changes and Reset	No changeable options	Resets the system without saving any changes configured in BIOS settings.
Restore Defaults	No changeable options	Loads the optimized defaults for BIOS settings.
Boot Override	- [Drive(s)]	Forces to boot the system from selected [drive(s)].

Appendix A System Diagrams

This appendix includes the exploded diagrams of the system and the parts list as well as the part numbers of the SP-6265 system.

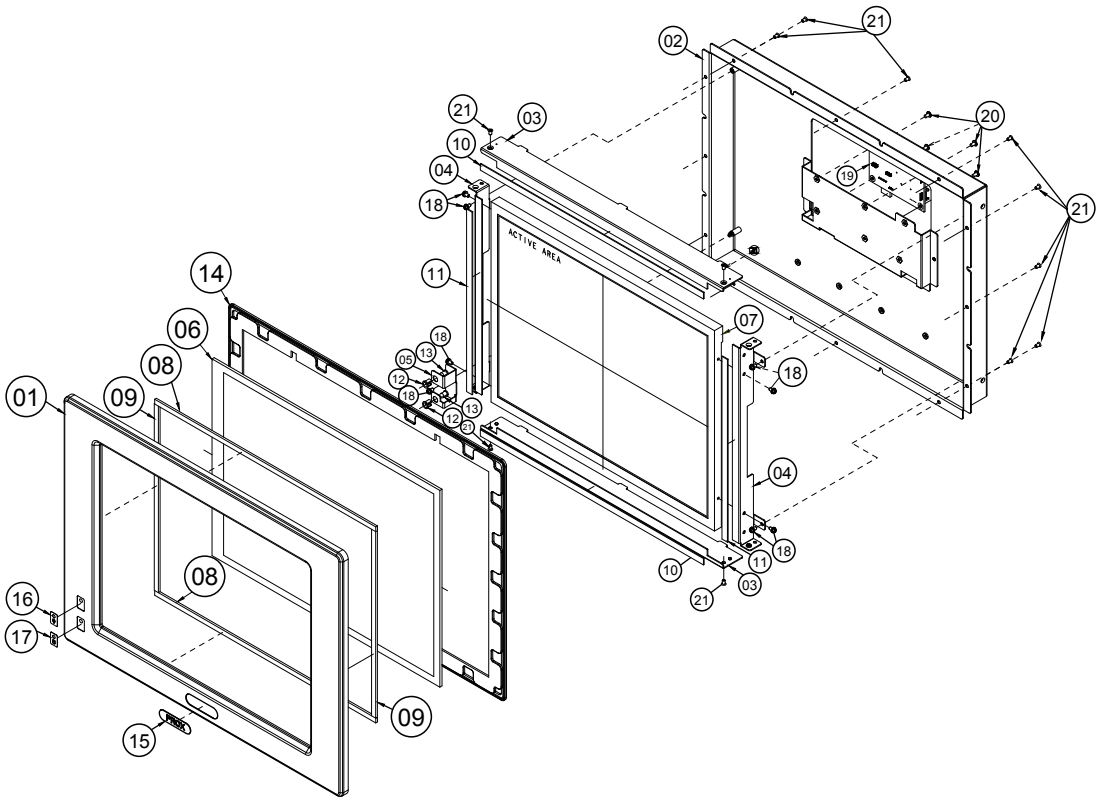
- SP-6265 Equipped Module PC Exploded Diagram
- SP-6265 LCD Display Exploded Diagram
- SP-6265 Module PC Exploded Diagram
- SP-6265 1st Memory Slot & CPU Installation Exploded Diagram
- SP-6265 2nd Memory Slot and SSD Installation Exploded Diagram
- SP-6265 Panel Mount Exploded Diagrams

SP-6265 Equipped Module PC Exploded Diagram



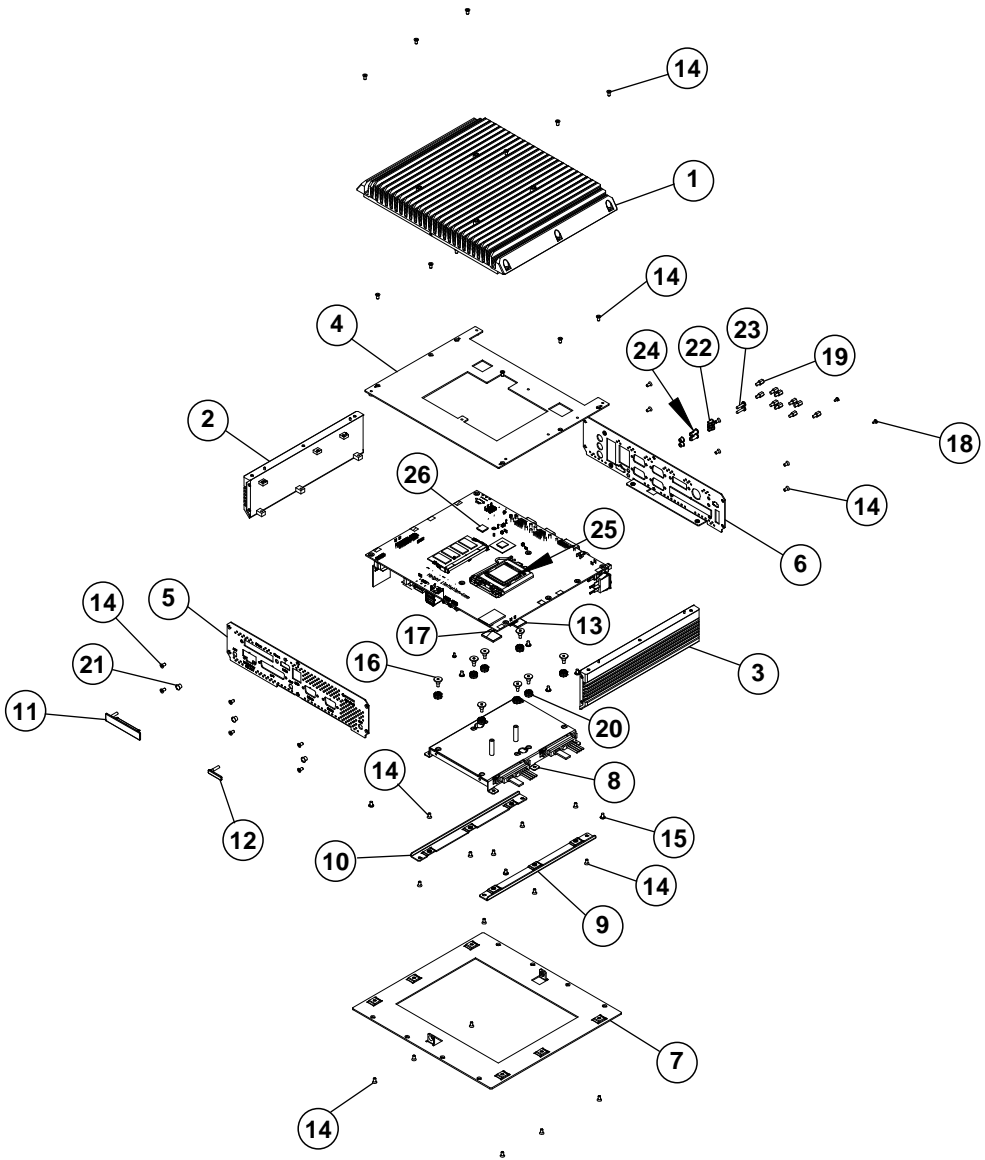
No.	Component Name	Part No.	Q'ty
1	SP-6155_LCD_Module_Exp	See page A-3	1
2	SE-8400-Explode	See page A-5 ~A-8	1
3	Flat Head Screw #2/ ϕ 5/ M3x0.5Px6mm (Black)	22-215-30006311	8

SP-6265 LCD Display Exploded Diagram



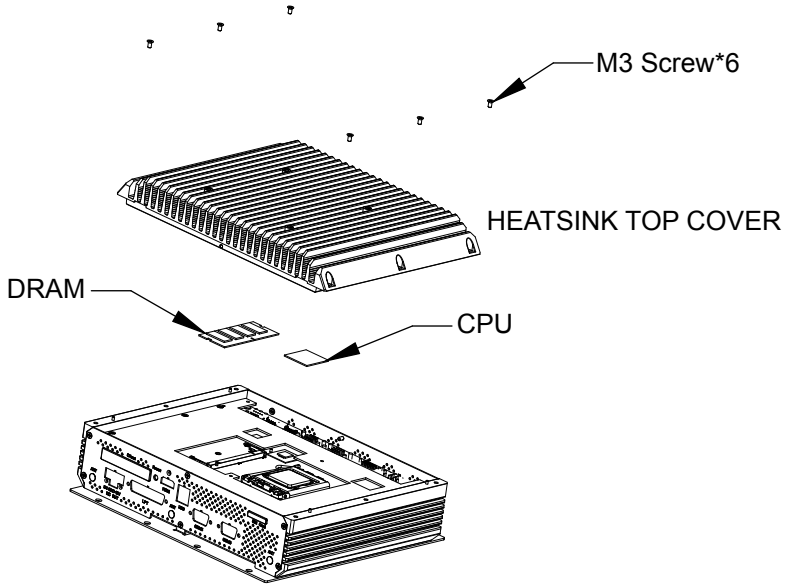
No.	Component Name	Part No.	Q'ty
1	SP-6205 Front Panel (w/Paint) (Black)	20-003-01061271	1
2	SP-6155 LCD Cover Kit (w/ Paint) (Black)	20-004-03061417	1
3	SP-6155 15LCD Link Holder-H	20-029-03001417	2
4	SP-6155 15LCD Link Holder-V	20-029-03002417	2
5	SP-6155 LED Support	20-002-03001417	1
6	15" 5-wire Resistance AccuTouch Panel	52-351-03650511	1
7	15" TFT LCD Panel (LED Backlight), 450nits, HD (1024x768)	52-351-03006802	1
8	SP-6205 Touch Panel EVA 2.5L (323x6x2.5mm)	30-013-15100271	2
9	SP-6205 Touch Panel EVA 2.5V (236x6x2.5mm)	30-013-15200271	2
10	SP-7145 Thin Gap LCD Poron H (326x8x1mm)	30-013-24100411	2
11	SP-7145 Thin Gap LCD Poron V (233x8x1mm)	30-013-24200411	2
12	LED Housing (Black)	30-014-04100009	2
13	SP-6205 Power & HDD LED Cable L=360mm (GREEN&RED)	27-018-27108111	1
14	SP-6145 Wall Waterproof Rubber	90-013-01100351	1
15	2016 Prox Flat Label	34-017-02103000	1
16	PPC-7360 LED Label For Power	34-017-02103009	1
17	LED Label For HDD	34-017-02101009	1
18	Round Head With Spring Washer Screw M3x0.5Px6mm	22-232-30060211	8
19	SP-6155 Daughter Board	SR-6145RA-D0N	1
20	Fillister Head Screw #2 / M3x0.5Px5mm	22-272-30049015	4
21	Flat Head Screw #2/ ϕ 5/ M3x0.5Px5mm (Black)	22-215-30005011	16

SP-6265 Module PC Exploded Diagram

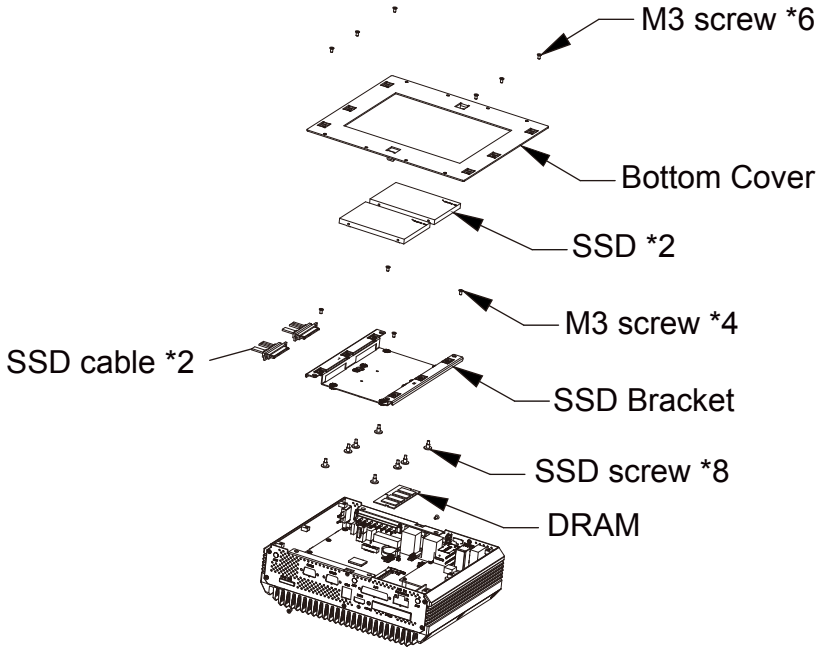


No.	Component Name	P/N No.	Q'ty
Metal (AL Extrude)			
1	SP-6265 HEATSINK VESA TOP COVER ASSY(BLACK) (259.63x200x33mm)	81-002-16000002	1
2	SE-8400 HEAT SINK RIGHT (200x57.8x12mm)	81-002-10058001	1
3	SE-8400 HEAT SINK LEFT (200x57.8x12mm)	81-002-10058002	1
Metal (Bracket)			
4	PCB bracket	20-006-03004402	1
5	SE-8400 BOTTOM IO BRACKET (w/Paint) (Pantone 2188C)	20-006-03064402	1
6	SE-8400 TOP IO BRACKET (w/Paint) (Pantone 2188C)	20-006-03062402	1
7	SP-6265 PPC BOTTOM BRACKET (w/Paint) (Black)	20-006-03061425	1
8	Double HDD bracket	20-006-03001402	1
9	HDD Link R Bracket	20-006-03003402	1
10	HDD Link L Bracket	20-006-03002402	1
Rubber			
11	Cfast rubber cover	30-013-01100261	1
12	Simcard rubber cover	30-013-01200261	1
13	CPU rubber	30-013-01100402	2
Screw / Other Component			
14	FLAT HEAD SCREW #2/ ϕ 5 / M3x0.5Px6mm (Black)	22-215-30006311	40
15	FILLISTER HEAD SCREW#2 / M3x0.5Px6mm	82-275-30006018	8
16	FILLISTER HEAD SCREW M3x0.5Px4.8mm	82-272-30005013	8
17	ROUND WASHER HEAD SCREW #1 / M2x0.4Px4mm	22-232-20004311	2
18	PAN HEAD SCREW M2.0x0.4Px6mm	22-222-20060011	4
19	HEX CU BOSS UNC No.4-40, L=4.8, H=7mm	22-692-40048051	16
20	RUBBER WASHER (OD=9.62mm, ID=3.9mmx5.8T) (Blue)	23-680-39580963	8
21	HOLE PLUG (ϕ 6.3-6.5mm) (Black)	30-054-04100000	3
22	LED HOUSING (Black)	30-014-04100165	2
23	SNAP RIVET (ϕ 2.8mm) (SR-4LQN2BA)	90-042-04800000	2
24	SNAP RIVET (ϕ 4.45mm) (SR-4LQN2BZ)	90-042-04900000	2
25	Thermal Interface Pads, 27.8x27.8x1mm	21-006-82828001	1
26	Thermal Interface Pads, 10x10x1.0mm	81-006-81010003	2

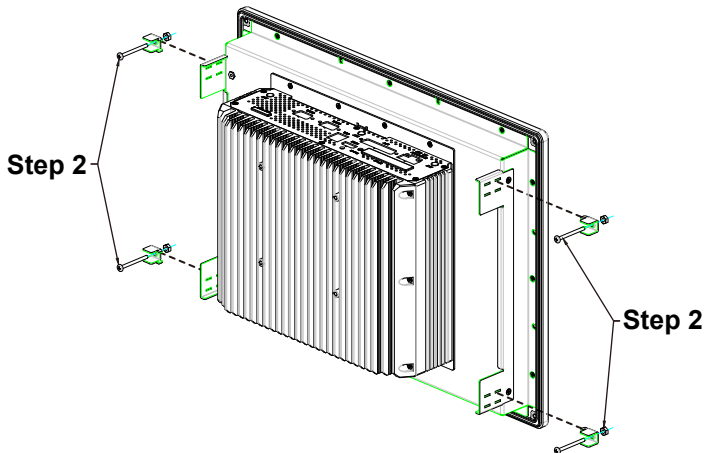
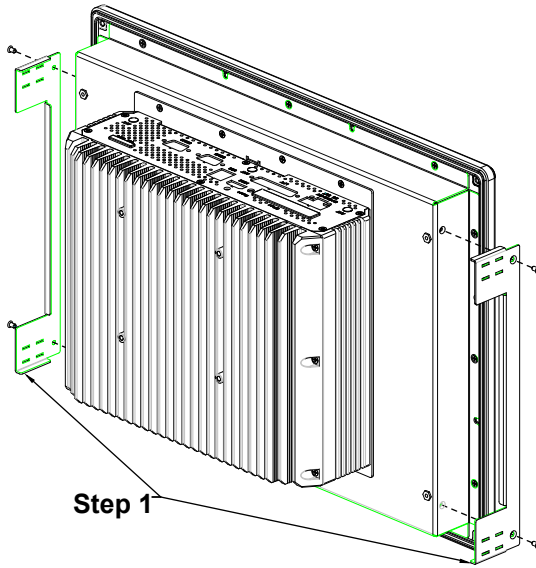
SP-6265 1st Memory Slot & CPU Installation Exploded Diagram



SP-6265 2nd Memory Slot and SSD Installation Exploded Diagram



SP-6265 Panel Mount Exploded Diagrams



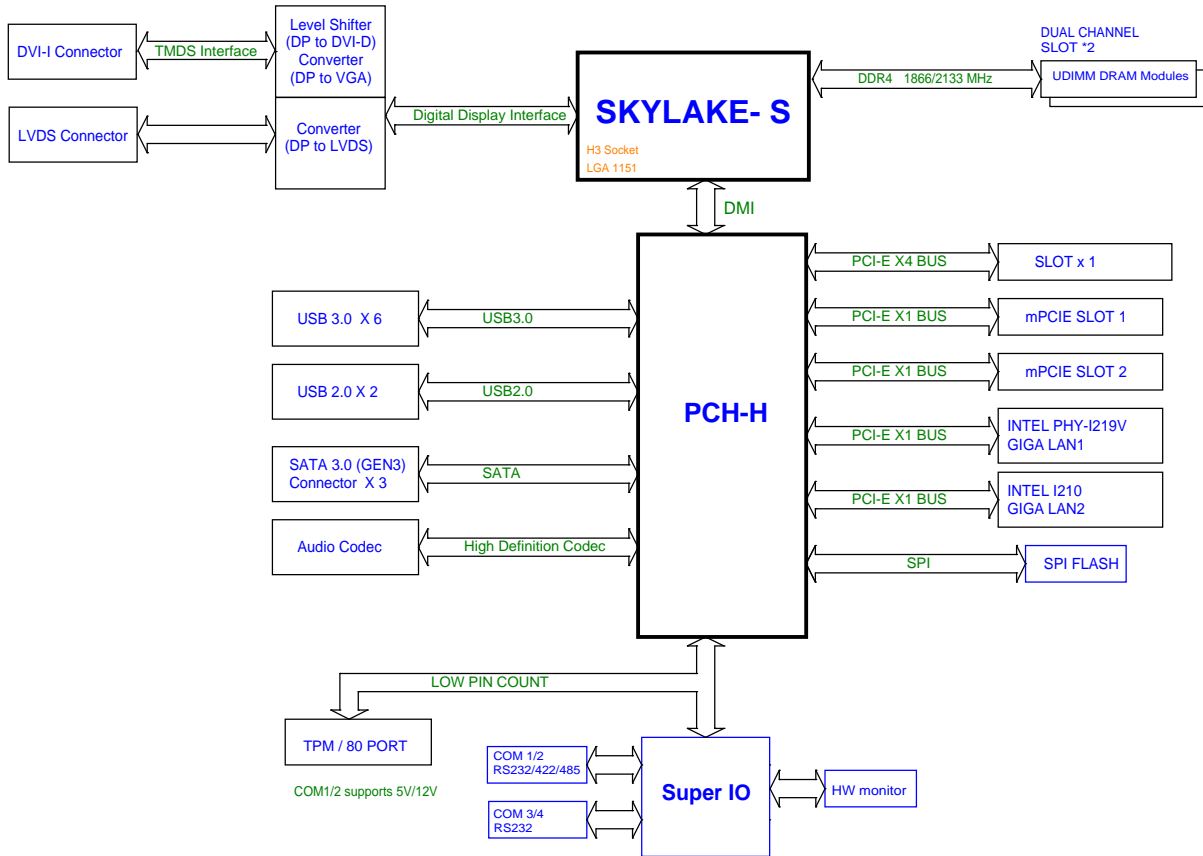
Appendix B Technical Summary

This appendix will give you a brief introduction of the allocation maps for the system resources.

The following topics are included:

- System Block Diagram
- Interrupt Map
- I/O Map
- Memory Map
- Configuring WatchDog Timer
- Flash BIOS Update

System Block Diagram



Interrupt Map

IRQ	Assignment
(ISA) IRQ 0	System timer
(ISA) IRQ 3	Communications Port (COM2)
(ISA) IRQ 4	Communications Port (COM1)
(ISA) IRQ 7	Communications Port (COM3)
(ISA) IRQ 10	Communications Port (COM4)
(ISA) IRQ 8	System CMOS/real time clock
(ISA) IRQ 11	Intel(R) HD Graphics 530
(ISA) IRQ 13	Numeric data processor
(ISA) IRQ 14	Motherboard resources
(ISA) IRQ 54	Microsoft ACPI-Compliant System
(ISA) IRQ 55	Microsoft ACPI-Compliant System
(ISA) IRQ 56	Microsoft ACPI-Compliant System
(ISA) IRQ 57	Microsoft ACPI-Compliant System
(ISA) IRQ 58	Microsoft ACPI-Compliant System
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(ISA) IRQ 65	Microsoft ACPI-Compliant System
(ISA) IRQ 66	Microsoft ACPI-Compliant System
(ISA) IRQ 67	Microsoft ACPI-Compliant System
(ISA) IRQ 68	Microsoft ACPI-Compliant System
(ISA) IRQ 69	Microsoft ACPI-Compliant System
(ISA) IRQ70	Microsoft ACPI-Compliant System

IRQ	Assignment
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(ISA) IRQ 72	Microsoft ACPI-Compliant System
(ISA) IRQ 73	Microsoft ACPI-Compliant System
(ISA) IRQ 74	Microsoft ACPI-Compliant System
(ISA) IRQ 75	Microsoft ACPI-Compliant System
(ISA) IRQ 76	Microsoft ACPI-Compliant System
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IRQ	Assignment
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IRQ	Assignment
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(ISA) IRQ 435	Microsoft ACPI-Compliant System
(ISA) IRQ 436	Microsoft ACPI-Compliant System
(ISA) IRQ 437	Microsoft ACPI-Compliant System
(ISA) IRQ 438	Microsoft ACPI-Compliant System
(ISA) IRQ 439	Microsoft ACPI-Compliant System
(ISA) IRQ 440	Microsoft ACPI-Compliant System
(ISA) IRQ 441	Microsoft ACPI-Compliant System
(ISA) IRQ 442	Microsoft ACPI-Compliant System
(ISA) IRQ 443	Microsoft ACPI-Compliant System
(ISA) IRQ 444	Microsoft ACPI-Compliant System
(ISA) IRQ 445	Microsoft ACPI-Compliant System
(ISA) IRQ 446	Microsoft ACPI-Compliant System
(ISA) IRQ 447	Microsoft ACPI-Compliant System
(ISA) IRQ 448	Microsoft ACPI-Compliant System
(ISA) IRQ 449	Microsoft ACPI-Compliant System
(ISA) IRQ 450	Microsoft ACPI-Compliant System
(ISA) IRQ 451	Microsoft ACPI-Compliant System
(ISA) IRQ 452	Microsoft ACPI-Compliant System
(ISA) IRQ 453	Microsoft ACPI-Compliant System
(ISA) IRQ 454	Microsoft ACPI-Compliant System
(ISA) IRQ 455	Microsoft ACPI-Compliant System
(ISA) IRQ 456	Microsoft ACPI-Compliant System
(ISA) IRQ 457	Microsoft ACPI-Compliant System
(ISA) IRQ 458	Microsoft ACPI-Compliant System
(ISA) IRQ 459	Microsoft ACPI-Compliant System
(ISA) IRQ 460	Microsoft ACPI-Compliant System

IRQ	Assignment
(ISA) IRQ 461	Microsoft ACPI-Compliant System
(ISA) IRQ 462	Microsoft ACPI-Compliant System
(ISA) IRQ 463	Microsoft ACPI-Compliant System
(ISA) IRQ 464	Microsoft ACPI-Compliant System
(ISA) IRQ 465	Microsoft ACPI-Compliant System
(ISA) IRQ 466	Microsoft ACPI-Compliant System
(ISA) IRQ 467	Microsoft ACPI-Compliant System
(ISA) IRQ 468	Microsoft ACPI-Compliant System
(ISA) IRQ 469	Microsoft ACPI-Compliant System
(ISA) IRQ 470	Microsoft ACPI-Compliant System
(ISA) IRQ 471	Microsoft ACPI-Compliant System
(ISA) IRQ 472	Microsoft ACPI-Compliant System
(ISA) IRQ 473	Microsoft ACPI-Compliant System
(ISA) IRQ 474	Microsoft ACPI-Compliant System
(ISA) IRQ 475	Microsoft ACPI-Compliant System
(ISA) IRQ 476	Microsoft ACPI-Compliant System
(ISA) IRQ 477	Microsoft ACPI-Compliant System
(ISA) IRQ 478	Microsoft ACPI-Compliant System
(ISA) IRQ 479	Microsoft ACPI-Compliant System
(ISA) IRQ 480	Microsoft ACPI-Compliant System
(ISA) IRQ 481	Microsoft ACPI-Compliant System
(ISA) IRQ 482	Microsoft ACPI-Compliant System
(ISA) IRQ 483	Microsoft ACPI-Compliant System
(ISA) IRQ 484	Microsoft ACPI-Compliant System
(ISA) IRQ 485	Microsoft ACPI-Compliant System
(ISA) IRQ 486	Microsoft ACPI-Compliant System
(ISA) IRQ 487	Microsoft ACPI-Compliant System

IRQ	Assignment
(ISA) IRQ 488	Microsoft ACPI-Compliant System
(ISA) IRQ 489	Microsoft ACPI-Compliant System
(ISA) IRQ 490	Microsoft ACPI-Compliant System
(ISA) IRQ 491	Microsoft ACPI-Compliant System
(ISA) IRQ 492	Microsoft ACPI-Compliant System
(ISA) IRQ 493	Microsoft ACPI-Compliant System
(ISA) IRQ 494	Microsoft ACPI-Compliant System
(ISA) IRQ 495	Microsoft ACPI-Compliant System
(ISA) IRQ 496	Microsoft ACPI-Compliant System
(ISA) IRQ 497	Microsoft ACPI-Compliant System
(ISA) IRQ 498	Microsoft ACPI-Compliant System
(ISA) IRQ 499	Microsoft ACPI-Compliant System
(ISA) IRQ 500	Microsoft ACPI-Compliant System
(ISA) IRQ 501	Microsoft ACPI-Compliant System
(ISA) IRQ 502	Microsoft ACPI-Compliant System
(ISA) IRQ 503	Microsoft ACPI-Compliant System
(ISA) IRQ 504	Microsoft ACPI-Compliant System
(ISA) IRQ 505	Microsoft ACPI-Compliant System
(ISA) IRQ 506	Microsoft ACPI-Compliant System
(ISA) IRQ 507	Microsoft ACPI-Compliant System
(ISA) IRQ 508	Microsoft ACPI-Compliant System
(ISA) IRQ 509	Microsoft ACPI-Compliant System
(ISA) IRQ 510	Microsoft ACPI-Compliant System
(ISA) IRQ 511	Microsoft ACPI-Compliant System
(PCI) IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family Integrated Sensor Hub - A135
(PCI) IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family Thermal

IRQ	Assignment
	subsystem - A31
(PCI) IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
(PCI) IRQ 11	Intel(R) Xeon(R) E3 - 1200/1500 v5/6th Gen Intel(R) Core(TM) Gaussian Mixture Model - 1911
(PCI) IRQ 16	High Definition Audio Controller
(PCI) IRQ 16	Intel(R) Serial IO I2C Host Controller - A160
(PCI) IRQ -13	Intel(R) Management Engine Interface
(PCI) IRQ -12	Intel(R) I210 Gigabit Network Connection
(PCI) IRQ -11	Intel(R) I210 Gigabit Network Connection
(PCI) IRQ -10	Intel(R) I210 Gigabit Network Connection
(PCI) IRQ -9	Intel(R) I210 Gigabit Network Connection
(PCI) IRQ -8	Intel(R) I210 Gigabit Network Connection
(PCI) IRQ -7	Intel(R) I210 Gigabit Network Connection
(PCI) IRQ -6	Intel(R) USB3.0 eXtensible Host Controller - 1.0 (Microsoft)
(PCI) IRQ -5	Intel(R) HD Graphics 630
(PCI) IRQ -4	Intel(R) Ethernet Connection (2) I219-LM
(PCI) IRQ -3	Standard SATA AHCI Controller
(PCI) IRQ -2	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115

I/O Map

I/O Map	Assignment
0x00000000-0x00000CF7	PCI Express Root Complex
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x0000002E-0x0000002F	Motherboard resources
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x00000040-0x00000043	System timer
0x0000004E-0x0000004F	Motherboard resources
0x00000050-0x00000053	System timer
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000070-0x00000070	Motherboard resources
0x00000070-0x00000077	System CMOS/real time clock
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller

I/O Map	Assignment
0x000000B2-0x000000B3	Motherboard resources
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000000F0-0x000000F1	Numeric data processor
0x000002E8-0x000002EF	Communication Port (COM4)
0x000002F8-0x000002FF	Communication Port (COM2)
0x00000378-0x0000037F	Printer Port (LPT1)
0x000003E8-0x000003EF	Communication Port (COM3)
0x000003F8-0x000003FF	Communication Port (COM1)
0x000004D0-0x000004D1	Programmable interrupt controller
0x00000680-0x0000069F	Motherboard resources
0x00000800-0x0000087F	Motherboard resources
0x00000A00-0x00000A0F	Motherboard resources
0x00000A10-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x00000D00-0x0000FFFF	PCI Express Root Complex
0x0000164E-0x0000164F	Motherboard resources
0x00001800-0x000018FE	Motherboard resources
0x00001854-0x00001857	Motherboard resources
0x0000E000-0x0000EFFF	Intel(R) 100 series/C230 Series Chipset Family PCI Express Root Port #6 - A115
0x0000F000-0x0000F03F	Intel(R) HD Graphics 630
0x0000F040-0x0000F05F	Intel(R) 100 series/C230 Series Chipset Family SMBus - A123
0x0000F060-0x0000F07F	Standard SATA AHCI Controller
0x0000F080-0x0000F083	Standard SATA AHCI Controller

I/O Map	Assignment
0x0000F090-0x0000F097	Standard SATA AHCI Controller
0x0000FF00-0x0000FFFE	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources

Memory Map

Memory Map	Assignment
0x000A0000-0x000BFFFF	Microsoft Basic Display Adapter
0x000A0000-0x000BFFFF	PCI Express Root Complex
0x90000000-0xDFFFFFFF	PCI Express Root Complex
0xC0000000-0xCFFFFFFF	Intel(R) HD Graphics 630
0xDE000000-0xDEFFFFFF	Intel(R) HD Graphics 630
0xDF000000-0xDF0FFFFF	Intel(R) I210 Gigabit Network Connection
0xDF000000-0xDF1FFFFF	Intel(R) 100 Series/C230 Series Chipset Family PCI Express Root Port #6 - A115
0xDF100000-0xDF103FFF	Intel(R) I210 Gigabit Network Connection
0xDF200000-0xDF21FFFF	Intel(R) Ethernet Connection (2) I219-LM
0xDF220000-0xDF22FFFF	High Definition Audio Controller
0xDF230000-0xDF23FFFF	Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft)
0xDF240000-0xDF243FFF	High Definition Audio Controller
0xDF244000-0xDF247FFF	Intel(R) 100 Series/C230 Series Chipset Family PMC - A121
0xDF248000-0xDF249FFF	Standard SATA AHCI Controller
0xDF24A000-0xDF24A0FF	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
0xDF24B000-0xDF24B7FF	Standard SATA AHCI Controller
0xDF24C000-0xDF24C0FF	Standard SATA AHCI Controller
0xDF250000-0xDF250FFF	Intel(R) 100 Series/C230 Series Chipset Family Thermal subsystem - A31
0xDF251000-0xDF251FFF	Intel(R) 100 Series/C230 Series Chipset Family Integrated Sensor Hub - A135
0xDF252000-0xDF252FFF	Intel(R) Xeon(R) E3 - 1200/1500 v5/6th Gen Intel(R) Core(TM) Gaussian Mixture Model - 1911
0xDFFE0000-0xDFFFFFFF	Motherboard resources
0xE0000000-0xEFFFFFFF	Motherboard resources

Memory Map	Assignment
0xFD000000-0xFDABFFFF	Motherboard resources
0xFD000000-0xFD7FFFFF	PCI Express Root Complex
0xFDAC0000-0xFDACFFFF	Motherboard resources
0xFDAD0000-0xFDADFFFF	Motherboard resources
0xFDAE0000-0xFDAEFFFF	Motherboard resources
0xFDAF0000-0xFDAFFFFF	Motherboard resources
0xFDB00000-0xFDFFFFFF	Motherboard resources
0xFE000000-0xFE01FFFF	Motherboard resources
0xFE036000-0xFE03BFFF	Motherboard resources
0xFE03D000-0xFE3FFFFF	Motherboard resources
0xFE40E000-0xFE40EFFF	Intel(R) Series IO I2C Host Controller - A160
0xFE40F000-0xFE40FFFF	Intel(R) Management Engine Interface
0xFE410000-0xFE7FFFFF	Motherboard resources
0xFED00000-0xFED003FF	High precision event timer
0xFED10000-0xFED17FFF	Motherboard resources
0xFED18000-0xFED18FFF	Motherboard resources
0xFED19000-0xFED19FFF	Motherboard resources
0xFED20000-0xFED3FFFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFED90000-0xFED93FFF	Motherboard resources
0xFEE00000-0xFEEFFFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Legacy device
0xFF000000-0xFFFFFFFF	Motherboard resources

Configuring WatchDog Timer

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. User must first assign the address of register by writing address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

Configuration Sequence

To program F81866 configuration registers, the following configuration sequence must be followed:

(1) Enter the extended function mode

To place the chip into the Extended Function Mode, two successive writes of 0x87 must be applied to Extended Function Enable Registers (EFERs, i.e. 2Eh or 4Eh).

(2) Configure the configuration registers

The chip selects the Logical Device and activates the desired Logical Devices through Extended Function Index Register (EFIR) and Extended Function Data Register (EFDR). The EFIR is located at the same address as the EFER, and the EFDR is located at address (EFIR+1). First, write the Logical Device Number (i.e. 0x07) to the EFIR and then write the number of the desired Logical Device to the EFDR. If accessing the Chip (Global) Control Registers, this step is not required. Secondly, write the address of the desired configuration register within the Logical Device to the EFIR and then write (or read) the desired configuration register through the EFDR.

(3) Exit the extended function mode

To exit the Extended Function Mode, writing 0xAA to the EFER is required. Once the chip exits the Extended Function Mode, it is in the normal running mode and is ready to enter the configuration mode.

Code example for watch dog timer

```

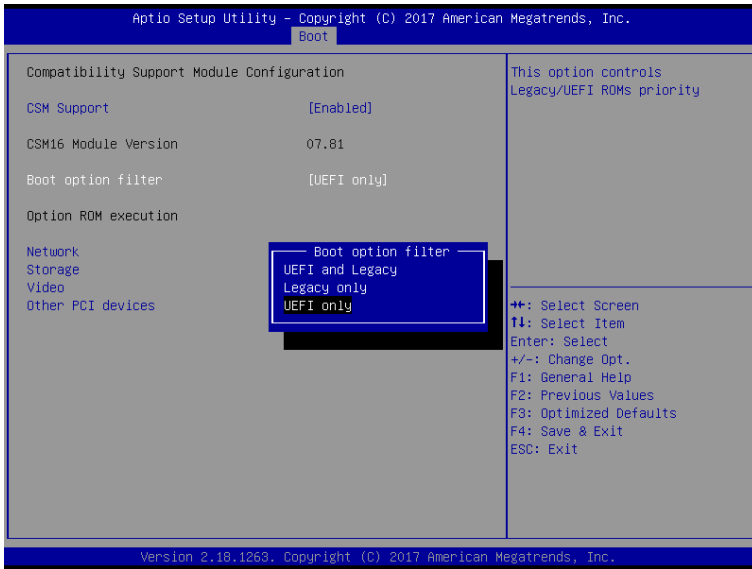
; ----- Enter to extended function mode -----
mov     dx,      2eh
mov     al,      87h
out     dx,      al
out     dx,      al
; ----- Select Logical Device 7 of watchdog timer -----
mov     al,      07h
out     dx,      al
inc     dx
mov     al,      07h
out     dx,      al
; ----- Enable Watch dog feature -----
mov     al,      030h
out     dx,      al
inc     dx
mov     al,      01h
out     dx,      al
; ----- Enable Watch PME -----
dec     dx
mov     al,      0FAh
out     dx,      al
inc     dx
in      al,      dx
and     al,      51h
out     dx,      al
; ----- Set second as counting unit -----
dec     dx
mov     al,      0f5h
out     dx,      al
inc     dx
in      al,      dx
and     al,      30h
out     dx,      al
; ----- Set timeout interval as 30seconds and start counting -----
dec     dx
mov     al,      0f6h
out     dx,      al
inc     dx
mov     al,      1Eh
out     dx,      al
; ----- Exit the extended function mode -----
dec     dx
mov     al,      0aah
out     dx,      al

```

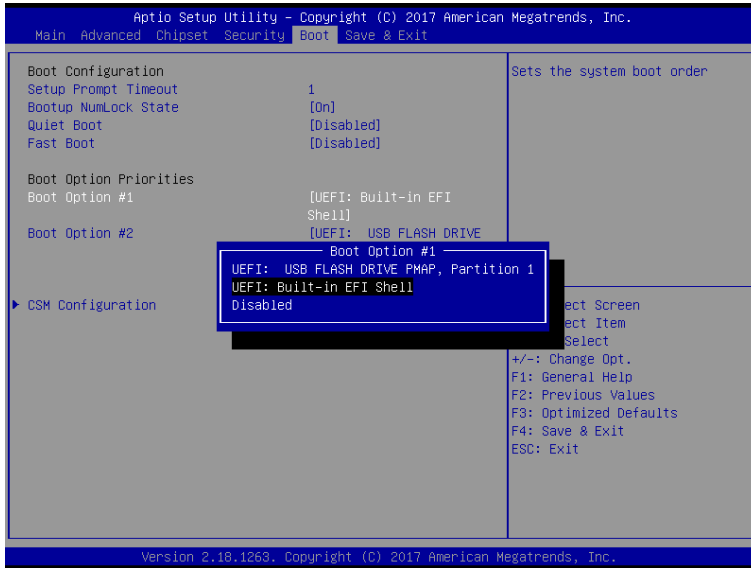
Flash BIOS Update

I. Prerequisites

- 1 Prepare a USB storage device which can save the required files for BIOS update.
- 2 Download and save the BIOS file (e.g. 626XPQ1.bin) to the storage device.
- 3 Copy AMI flash utility – AFUEFIx64.exe (v5.09.01) into the storage device. The utility and BIOS file should be saved to the same path.
- 4 Make sure the target system can first boot to the EFI shell environment.
 - (1) Connect the USB storage device.
 - (2) Turn on the computer and press <ESC> or key during boot to enter BIOS Setup.
 - (3) System will go into the BIOS setup menu.
 - (4) Select [**Boot**] menu and enter into [**CSM Configuration**] menu.
 - (5) Set [**Boot option filter**] to [**UEFI Only**] and press <F4> key to save the configuration and restart the system.



- (6) Press <ESC> or to enter into BIOS setup menu again.
- (7) Select [Boot] menu and set [UEFI: Built-in EFI Shell] as the 1st boot device.
- (8) Press <F4> key to save the configuration and restart the system to boot into EFI Shell environment.



AFUEFIx64 Command for System BIOS Update

AFUEFIx64.efi is the AMI firmware update utility; the command line is shown as below:

AFUEFIx64 <ROM File Name> [option1] [option2]....

Users can type “AFUEFIx64 /?” to view the definition of each control option. The recommended options for BIOS ROM update include the following parameters:

- /P:** Program main BIOS image.
- /B:** Program Boot Block.
- /N:** Program NVRAM.
- /X:** Don't check ROM ID.

III. BIOS Update Procedure

- 1 Boot into EFI Shell, change to the path where you put BIOS image and AFUEFIx64.

```
Shell> fs0:  
fs0:\> cd afuefix64
```

- 2 Type "**AFUEFIx64 626xxxx.bin /p /b /n /x**" and press Enter to start the flash procedure. (xxxx means the BIOS revision part, e.g. 1PQd1...)
- 3 During the BIOS update procedure, you will see the BIOS update process status and its percentage. Beware! Do not turn off the system power or reset your computer when the entire update procedure are not complete; otherwise, the BIOS ROM may be crashed and the system will be unable to boot up next time.
- 4 After the BIOS update procedure is completed, the following messages will be shown:

```
fs0:\afuefix64> afuefix64 626XPQ1.bin /p /b /n /x  
+-----+  
--+  
|           AMI Firmware Update Utility  v5.09.01.1317  
|  
|   Copyright (C) 2016 American Megatrends Inc. All Rights Reserved.   |  
+-----+  
--+  
Reading flash ..... done  
- ME Data Size Checking - OK  
- FFS checksums ..... OK.  
Erasing Boot Block ..... done  
Updating Boot Block ..... done  
Verifying Boot Block ..... done  
Erasing Main Block ..... done  
Updating Main Block ..... done  
Verifying Main Block ..... done  
Erasing NVRAM Block ..... done  
Updating NVRAM Block ..... done  
Verifying NVRAM Block ..... done  
  
fs0:\afuefix64
```

- 5 Restart the system and boot up with the new BIOS configurations.
- 6 The BIOS Update is completed after the system is restarted.
- 7 Reboot the system and verify if the BIOS version shown on the

initialization screen has been updated.

