

USER'S MANUAL

PA-2019 Series

POS System Powered by

Intel® 6th / 7th Gen. Core™ ,

Pentium® , Celeron® Processor

PA-2019 Series M6

PA-2019 Series POS System

With LCD / Touchscreen

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DISCLAIMER

This user's manual is meant to assist users in installing and setting up the system. The information contained in this document is subject to change without any notice.

CE NOTICE

This is a class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures.

FCC NOTICE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

You are cautioned that any change or modifications to the equipment not expressly approve by the party responsible for compliance could void your authority to operate such equipment.

CAUTION! Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

WARNING! Some internal parts of the system may have high electrical voltage. And therefore we strongly recommend that qualified engineers can open and disassemble the system. The LCD and Touchscreen are easily breakable, please handle them with extra care.

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INTRODUCTION

CHAPTER

1

This chapter gives you the information for the PA-2019. It also outlines the system specifications.

The following sections are included:

- About This Manual
- POS System Illustration
 - Panel PC
 - Normal Stand
 - POS with 2nd Display
 - Rear I/O View
- System Specifications
- Safety Precautions

Experienced users can jump to chapter 2 on page 2-1 for a quick start.

1-1. ABOUT THIS MANUAL

Thank you for purchasing our PA-2019 Series System. The PA-2019 is an updated system designed to be comparable with the highest performance of IBM AT personal computers. The PA-2019 provides faster processing speed, greater expandability and can handle more tasks than before. This manual is designed to assist you how to install and set up the whole system. It contains four chapters and two appendixes. Users can configure the system according to their own needs.

Chapter 1 Introduction

This chapter introduces you to the background of this manual. It also includes illustrations and specifications for the whole system. The final section of this chapter indicates some safety reminders on how to take care of your system.

Chapter 2 System Configuration

This chapter outlines the location of motherboard components and their functions. You will learn how to set the jumper and configure the system to meet your own needs.

Chapter 3 Software Utilities

This chapter contains helpful information for proper installations of the Intel Utility, VGA Utility, LAN Utility, Sound Utility, Touch Screen Utility and Fingerprint Utility (Optional).

Chapter 4 AMI BIOS Setup

This chapter indicates you how to change the BIOS configurations.

Appendix A System Assembly

This appendix gives you the exploded diagrams and part numbers of the PA-2019.

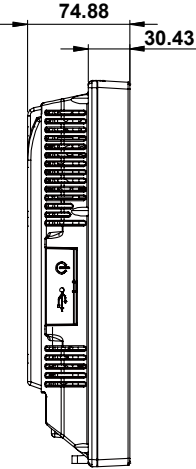
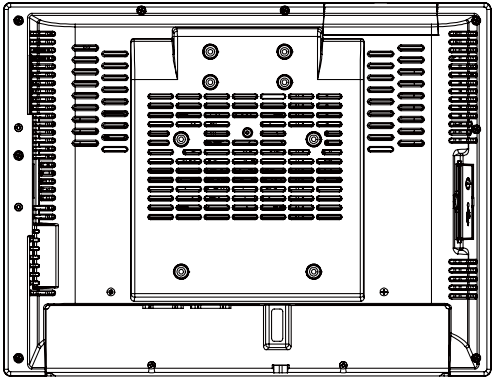
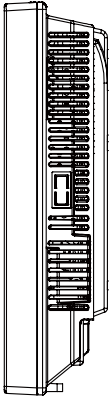
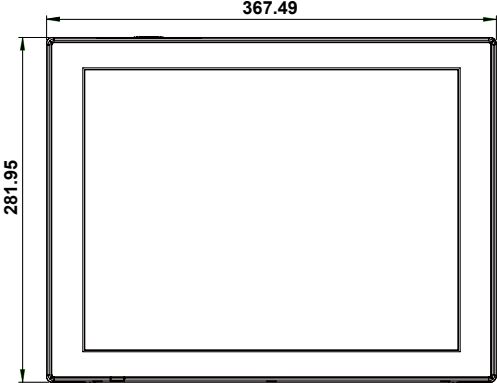
Appendix B Technical Summary

This appendix gives you the information about the allocation maps for the system resources, Watchdog Timer Configuration, and Flash BIOS Update.

1-2. POS SYSTEM ILLUSTRATION

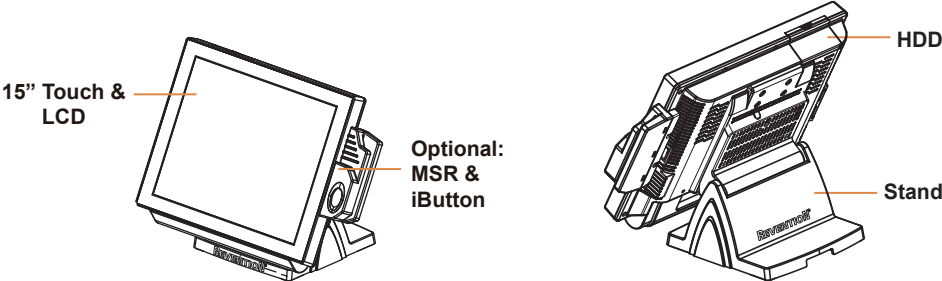
Unit: mm

1-2-1. Panel PC

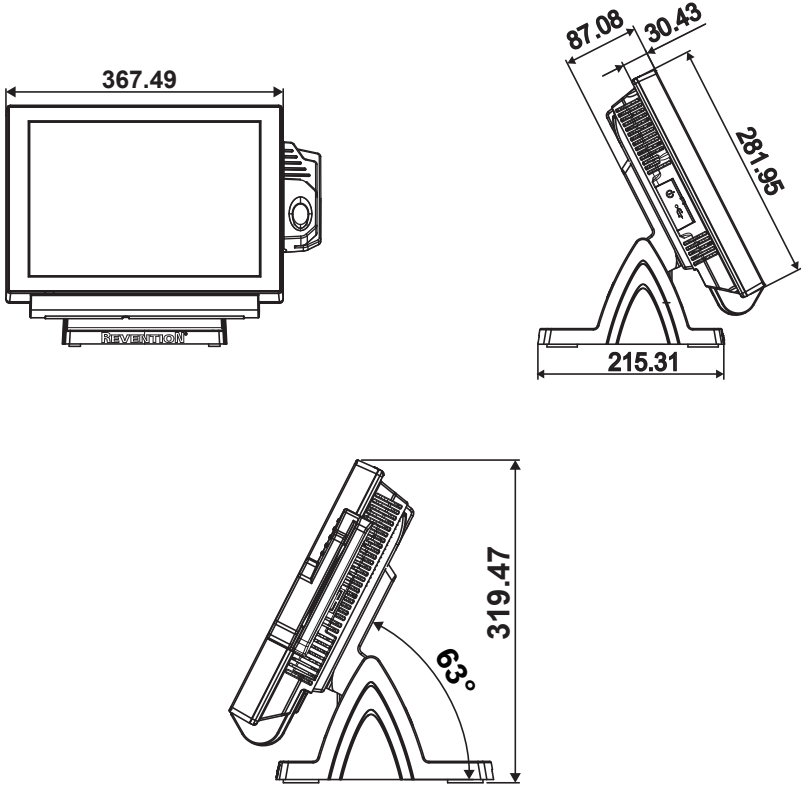


1-2-2. Normal Stand

Unit: mm

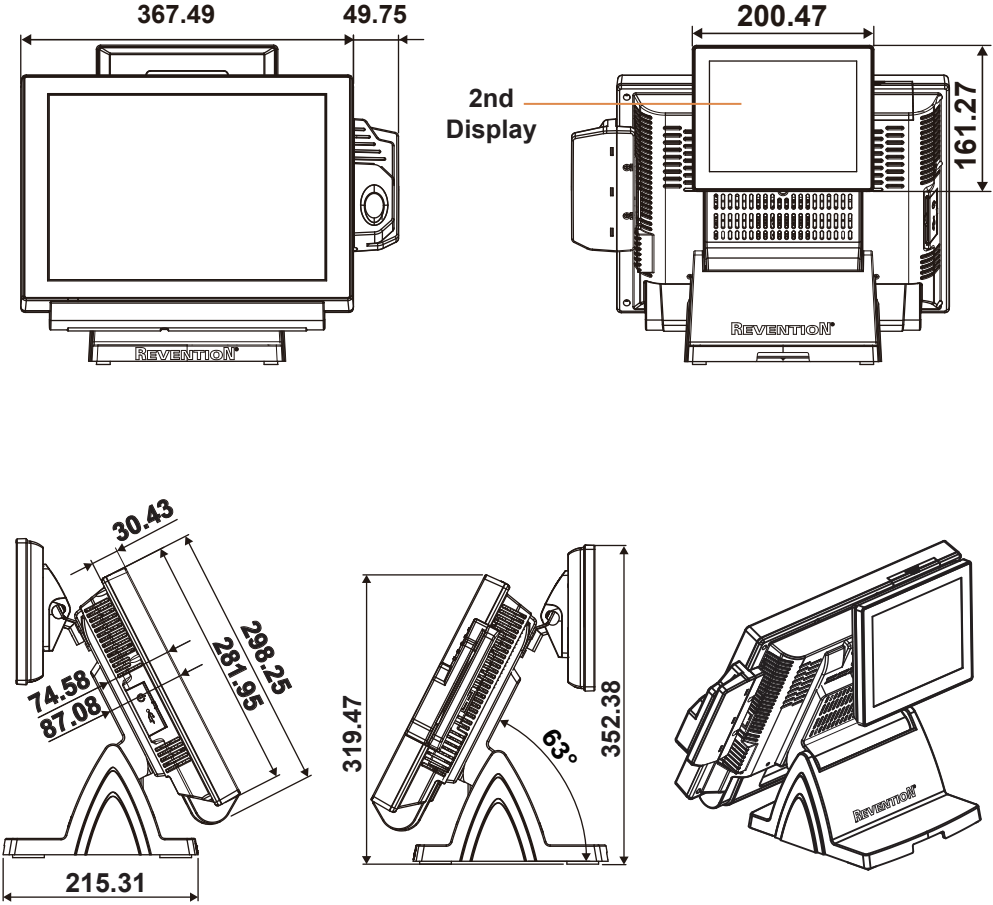


Adjustable angle 0-68 degree

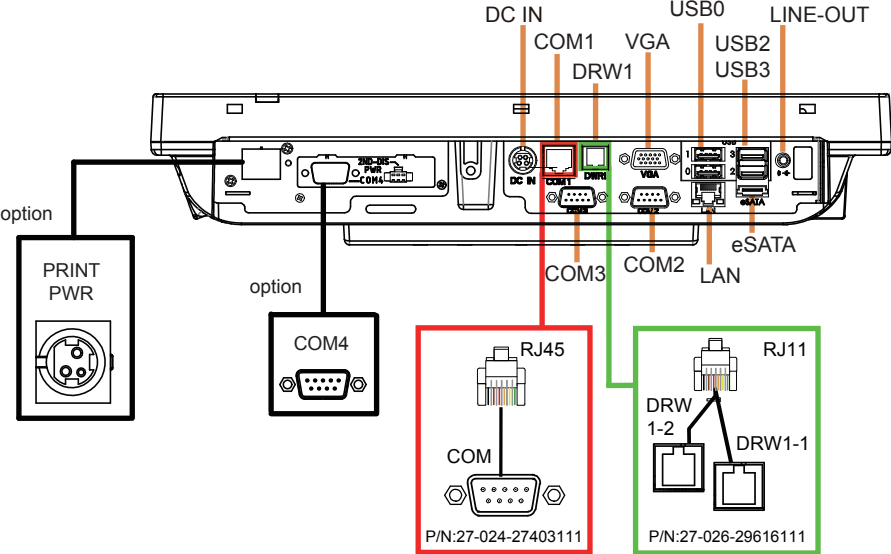


1-2-3. POS with 2nd Display

Unit: mm



1-2-4. Rear I/O View



1-3. SYSTEM SPECIFICATIONS**MAINBOARD (PB-6980)****System**

CPU support	6th Gen. Skylake	7th Gen. Kabylake
Core Logic	Intel® H110 Chipset	
Memory	2 x DDR4 SO-DIMM slot (up to 32GB)	
Network	10/100/1000Mbps Base-T Fast Ethernet	
OS Support	<ul style="list-style-type: none"> • Windows 7 Pro for Embedded Systems • Windows Embedded POSReady 7 • Windows Embedded 8.1 Industry Pro • Windows 10 IOT Enterprise • Windows 10 IOT Enterprise for Retail or Thin Client 	<ul style="list-style-type: none"> • Windows 10 • Windows 10 IOT Enterprise • Windows 10 IOT Enterprise for Retail or Thin Client
BIOS	AMI SPI BIOS, 8Mbits with VGA BIOS	
Power Supply	120 Watt power inside	
System Weight	6.6 kg (POS), 6.8 kg (POS + 2nd Display), 5.3 kg (PPC)	
Dimensions (WxHxD)	<ul style="list-style-type: none"> • 367.49 x 281.95 x 303.04mm (Normal Stand POS) • 417.24 x 352.38 x 215.31mm (POS + 2nd Display) • 367.49 x 281.95 x 74.88mm (PPC) 	
Certificate	FCC/CE	

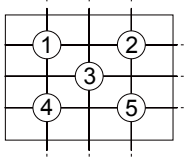
I/O Ports

USB	<ul style="list-style-type: none"> • 2 x USB 2.0 ports • 2 x USB 3.0 ports
eSATA (external SATA)	<ul style="list-style-type: none"> • 1 x eSATA port
Serial Port	<ul style="list-style-type: none"> • 3 x DB-9 (COM 2/3/4(option)) • 1 x RJ-45 (COM1) • +5V/12V selectable
LAN	1 x RJ-45
VGA	1 x DB-15 VGA Interface
Cash Drawer	1 x RJ-11 (+12V/+24V selectable, default: +12V)
DC IN	1 x 4-pin DC Power Jack
Line-Out	1 x phone jack

Storage

HDD	1 x 2.5" SATA HDD
SSD	1 x 2.5" SATA SSD (Optional)

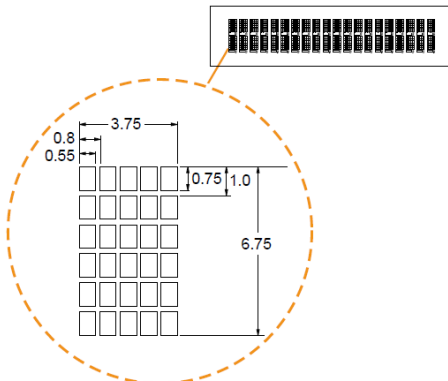
Display

15" TFT XGA LCD	Max. Resolution: 1024 x 768 Signal Interface: LVDS (18/24bit)
Touchscreen	5-wire Analog resistive or Projected Capacitive
Brightness 	<ul style="list-style-type: none"> • Resistive Touchscreen: Minimum 160 cd/m² • Projected Capacitive Touchscreen: Minimum 180 cd/m²
Tilt Angle	<ul style="list-style-type: none"> • 0-68 degree with normal stand

Environment

Operating Temperature	0°C ~35°C (32°F ~95°F)
Storage Temperature	-20°C ~60°C (-4°F ~140°F)
Humidity	20%~90%

Optional Accessories

MSR	JIS-I or II, ISO Track1+2+3
2 nd Display	<ul style="list-style-type: none"> 8" LCD (Resolution: 1024x768)
Customer Display	<ul style="list-style-type: none"> Interface: RS-232C Baud Rate: 9600/19200 bps Placement: 20 columns and 2 lines, each column is 5 x 7 dots  <ul style="list-style-type: none"> Standard Code CP-437, Katakana, CP-737, CP-850, CP-852, CP-857, CP-860, CP-862, CP-863, CP-865, CP-866, CP-1250, CP-1251, CP-1252, CP-1253, CP-1254, CP-1255, CP-1257 International Characters USA, FRANCE, GERMANY, UK, DENMARK I, SWDEN, ITALY, SPAIN I, JAPAN, NORWAY, DENMARK II, SPAIN II, LATIN, KOREA, RUSSIA, SLAVONIC
Fingerprint	8-bit grayscale reader

1-4. SAFETY PRECAUTIONS

The following messages are safety reminders on how to protect your systems from damages, and extending the life cycle of the system.

1. Check the Line Voltage

- (1) The operating voltage for the power supply should be within the range of 100V to 240V AC; otherwise, the system may be damaged.

2. Environmental Conditions

- (1) Place your PA-2019 on a sturdy, level surface. Be sure to allow enough space around the system to have easy access needs.
- (2) Avoid installing your PA-2019 Series POS system in extremely hot or cold places.
- (3) Avoid exposure to sunlight for a long period of time (for example, in a closed car in summer time. Also avoid the system from any heating device.). Or do not use the PA-2019 when it has been left outdoors in a cold winter day.
- (4) Bear in mind that the operating ambient temperature is between 0°C and 35°C (32°F and 95°F).
- (5) Avoid moving the system rapidly from a hot place to a cold place, and vice versa, because condensation may occur inside the system.
- (6) Protect your PA-2019 against strong vibrations, which may cause hard disk failure.
- (7) Do not place the system too close to any radio-active device. Radio-active device may cause signal interference.
- (8) Always shut down the operating system before turning off the power.

3. Handling

- (1) Avoid placing heavy objects on the top of the system.
- (2) Do not turn the system upside down. This may cause the hard drive to malfunction.
- (3) Do not allow any objects to fall into this product.
- (4) If water or other liquid spills into the product, unplug the power cord immediately.

4. Good Care

- (1) When the outside case gets stained, remove the stains using neutral washing agent with a dry cloth.
- (2) Never use strong agents such as benzene and thinner to clean the surface of the case.
- (3) If heavy stains are present, moisten a cloth with diluted neutral washing agent or alcohol and then wipe thoroughly with a dry cloth.
- (4) If dust is accumulated on the case surface, remove it by using a special vacuum cleaner for computers.

SYSTEM CONFIGURATION

CHAPTER

2

Helpful information that describes the jumper and connector settings, and component locations.

The following sections are included:

- Jumper & Connector Quick Reference Table
- Main Board Component Locations
- Main Board Configuration and Jumper settings
- Main Board Connector Pin Assignments
- 2nd Display Overview
- Setting VFD Board Connectors and Jumpers

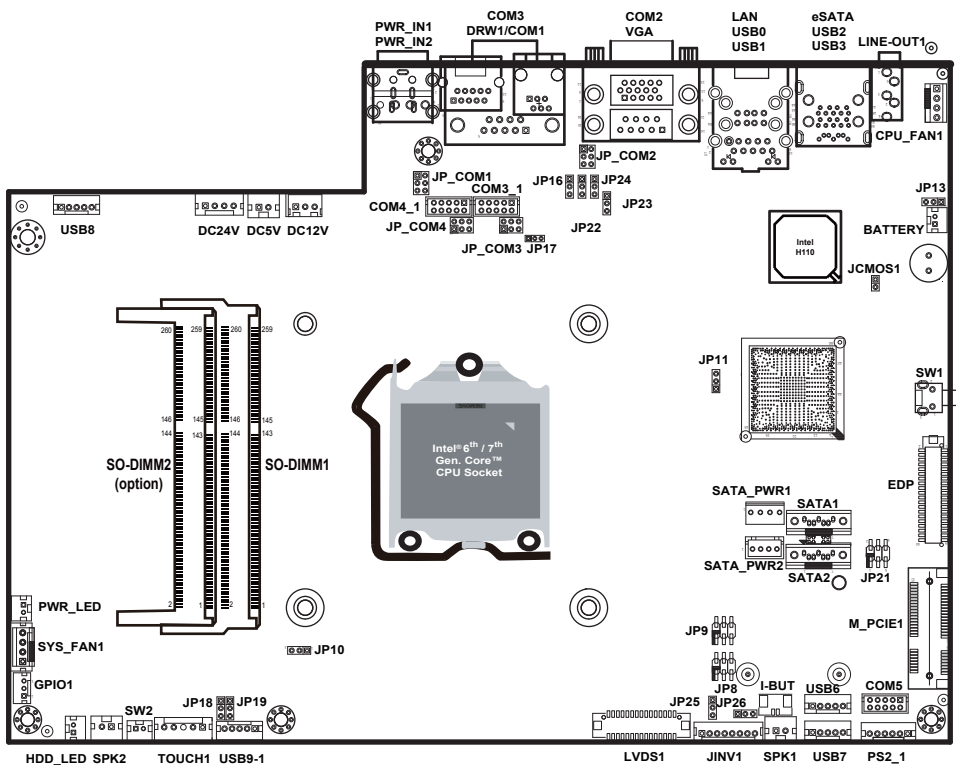
2-1. JUMPER & CONNECTOR QUICK REFERENCE TABLE

JUMPER / CONNECTOR	NAME
COM Port and VGA Connector	COM1, COM_VGA (COM2 + VGA Port) COM3, COM4 (Option), COM3_1, COM4_1, COM5
COM Port RI and Voltage Selection	JP_COM1, JP_COM2, JP_COM3, JP_COM4
i-Button Connector	I-BUT
i-Button Function Selection	JP22, JP23, JP24
LAN & USB Port	LAN, USB0, USB1
Internal USB 2.0 Connector	USB6, USB7, USB8, USB9-1, USB4_1
USB 3.0 Connector	USB2, USB3, eSATA
Cash Drawer Connector	DRW1
Cash Drawer Selection	JP17
Cash Drawer Power Selection	JP16
2nd Display Power Port	2nd DIS PWR
Printer Power Port (Option)	PRINT PWR (option)
LED Connector	PWR_LED, HDD_LED (option)
System / CPU Fan Connector	SYS_FAN1, CPU_FAN1
Power Input Connector	PWR_IN1, PWR_IN2
Power Connector	DC24V, DC12V, DC5V
Power Switch Connector	SW1 (option), SW2
External Speaker Connector	SPK1, SPK2 (option)
Speaker Selection	JP13
Inverter Connector	JINV1
LVDS Connector	LVDS1
LVDS Power Selection	JP25
LVDS Backlight Type Selection	JP26
Card Reader Connector	PS2_1
SATA & SATA Power Connector	SATA1, SATA2, SATA_PWR1, SATA_PWR2
Touch Panel Connector	TOUCH1
Touch Panel and USB9-1 Selection	JP18, JP19
LVDS Output Resolution Selection	JP8, JP9
Mini-PCIe/mSATA Connector	M_PCIE1, M_PCIE2

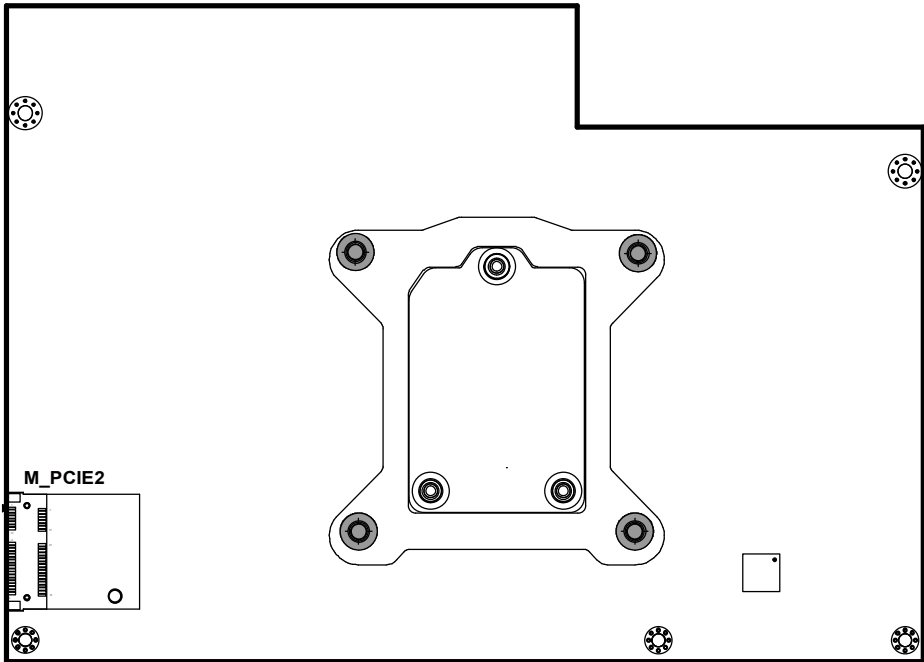
JUMPER / CONNECTOR	NAME
Mini-PCIe and USB6 Selection	JP21
EDP Connector (option)	EDP
Configuration / Recovery Selection	JP11
VCCIO / REFIN Selection	JP10
Clear CMOS Data Selection	JCMOS1
General Purpose Input / Output (GPIO) Connector	GPIO1
Audio Jack	LINE-OUT1

2-2. MAIN BOARD COMPONENT LOCATIONS

M/B: PB-6980



PB-6980 Front Connector, Jumper and Component Locations



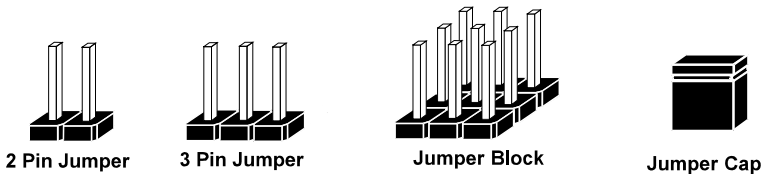
PB-6980 Rear Connector, Jumper and Component Locations

2-3. HOW TO SET THE JUMPERS

You can configure your board by setting the jumpers. A jumper consists of two or three metal pins with a plastic base mounted on the card, and by using a small plastic "cap", also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can set-up your hardware configuration by "opening" or "closing" pins.

Jumpers can be combined into sets that called jumper blocks. When jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows what this looks like.

JUMPERS AND CAPS

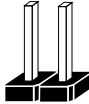


If a jumper has three pins for example, labelled PIN1, PIN2, and PIN3. You can connect PIN1 & PIN2 to create one setting and shorting. You can either connect PIN2 & PIN3 to create another setting. The same jumper diagrams are applied all through this manual. The figure below shows what the manual diagrams look and what they represent.

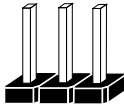
JUMPER DIAGRAMS



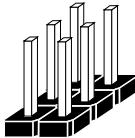
Jumper Cap looks like this



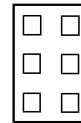
2 pin Jumper looks like this



3 pin Jumper looks like this



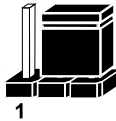
Jumper Block looks like this



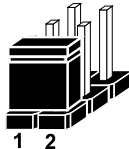
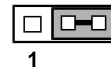
JUMPER SETTINGS



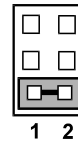
2 pin Jumper closed(enabled)
looks like this



3 pin Jumper
2-3 pin closed(enabled)
looks like this



Jumper Block
1-2 pin closed(enabled)
looks like this



2-4. MAIN BOARD CONNECTORS AND JUMPERS

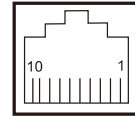
2-4-1. COM PORT & VGA CONNECTOR

There are multiple COM ports enhanced in this board namely: COM1, COM_VGA (COM2+VGA Port), COM3, COM3_1, COM4 (option), COM4_1 and COM5.

COM1: COM1 Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	COM1_DCDJ_I	6	COM1_DSRJ_I
2	COM1_RX_I	7	COM1_RTSJ_I
3	COM1_TX_I	8	COM1_CTSJ_I
4	COM1_DTRJ_I	9	COM1_RI_SEL
5	GND	10	NC

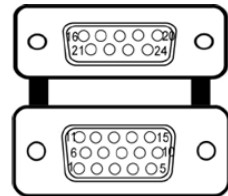


COM1

COM_VGA: COM2 & D-Sub 15-pin VGA Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	RED	13	HSYNC
2	GREEN	14	VSYNC
3	BLUE	15	DDCA CLK
4	NC	16	DCD2
5	GND	17	RXD2
6	GND	18	TXD2
7	GND	19	DTR2
8	GND	20	GND
9	+5V	21	DSR2
10	GND	22	RTS2
11	NC	23	CTS2
12	DDCA DATA	24	RI/+5V/+12V selectable

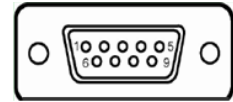


**COM2/
VGA**

COM3/COM4 (Option): COM3, COM4 Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	COM3_DCDJ_I	6	COM3_DSRJ_I
2	COM3_RX_I	7	COM3_RTSJ_I
3	COM3_TX_I	8	COM3_CTSJ_I
4	COM3_DTRJ_I	9	RI / +5V / +12V selectable
5	GND	-	-



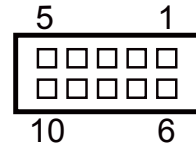
**COM3/
COM4
(Option)**

COM3 and COM3_1 can't be used simultaneously.
COM4 and COM4_1 can't be used simultaneously.

COM3_1: COM3_1 Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	COM3_DCDJ_I	6	COM3_DSRJ_I
2	COM3_RX_I	7	COM3_RTSJ_I
3	COM3_TX_I	8	COM3_CTSJ_I
4	COM3_DTRJ_I	9	COM3_RI_SEL
5	GND	10	NC

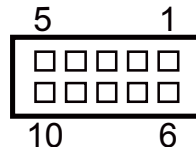


COM3_1

COM4_1: COM4_1 Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	COM4_DCDJ_I	6	COM4_DSRJ_I
2	COM4_RX_I	7	COM4_RTSJ_I
3	COM4_TX_I	8	COM4_CTSJ_I
4	COM4_DTRJ_I	9	COM4_RI_SEL
5	GND	10	NC

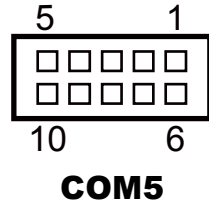


COM4_1

COM5: COM5 Connector

The pin assignments are as follows:

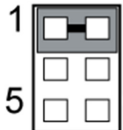
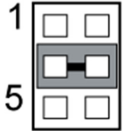
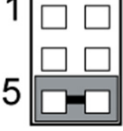
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	COM5_DCDJ_I	6	COM5_DSRJ_I
2	COM5_RX_I	7	COM5_RTSJ_I
3	COM5_TX_I	8	COM5_CTSJ_I
4	COM5_DTRJ_I	9	COM5_RI_SEL
5	GND	10	NC



2-4-2. COM PORT RI & VOLTAGE SELECTION

JP_COM1 , JP_COM2, JP_COM3, JP_COM4: COM Port RI & Voltage Selection, pin-headers on board. The voltage of COM1, COM2, COM3 and COM4 is made to control by the jumpers on board.

The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
RI	1-2	
VCC12	3-4	
VCC	5-6	

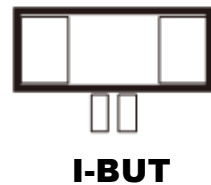
Note: Manufacturing Default is **RI**.

2-4-3. i-BUTTON CONNECTOR

I-BUT: i-Button Connector

The pin assignments are as follows:

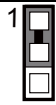

PIN	ASSIGNMENT
1	COM3_DTR_R_I
2	COM3_RXD_R_I



2-4-4. i-BUTTON FUNCTION SELECTION

JP22, JP23, JP24: i-Button Function Selection

The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
COM2	1-2	 JP22/JP23/JP24
i-Button*	2-3	 JP22/JP23/JP24

Note: Manufacturing Default is COM2.

*When these jumpers are set as 'i-Button', the COM3_1 connector will not function.

2-4-5. LAN & USB PORT

LAN & USB0, USB1 Connector

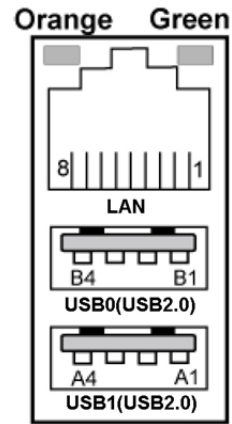
The pin assignments are as follows:

USB0 and USB1: USB 2.0 Connector, USB Type A ports

PIN	ASSIGNMENT	PIN	ASSIGNMENT
A1	VCC5	B1	VCC5
A2	USB0-	B2	USB1-
A3	USB0+	B3	USB1+
A4	GND	B4	GND

LAN: a Giga LAN RJ-45 port (rear I/O)

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	LAN1_MDI0_DP	5	LAN1_MDI2_DP
2	LAN1_MDI0_DN	6	LAN1_MDI2_DN
3	LAN1_MDI1_DP	7	LAN1_MDI3_DP
4	LAN1_MDI1_DN	8	LAN1_MDI3_DN



**LAN/
USB0/
USB1**

Left Side LAN LED Indicator

Orange Color Blinking	LAN Message Active
Off	No LAN Message Active

Right Side LAN LED Indicator

Green Color On	10/100Mbps LAN Speed Indicator
Orange Color On	Giga LAN Speed Indicator
Off	No LAN switch / hub connected

2-4-6. Internal USB 2.0 CONNECTOR

USB6, USB7, USB9-1: Internal USB 2.0 connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	5V (Maximum current: 0.5A)
2	D-
3	D+
4	GND
5	GND

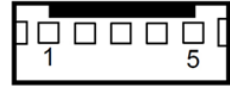
Note:

USB6 signal is shared from "MINI-PCIE" port.

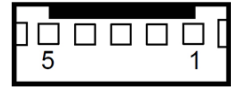
USB6 could be functioned when JP21 are set 1-3, 2-4 [short].

USB9-1 signal is shared from "MINI-PCIE" port.

USB9-1 could be functioned when JP18, JP19 are set 1-2 [short].



**USB6/
USB7**



USB9-1

2-4-7. USB 3.0 CONNECTOR

USB2: USB 3.0 connector

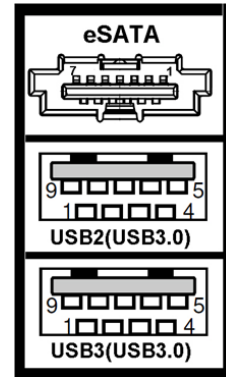
The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	VCC5	6	RX2_DP
2	USBP3N	7	GND
3	USBP3P	8	TX3_DN
4	GND	9	TX3_DP
5	RX2_DN	10	-

USB3: USB 3.0 connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	VCC5	6	BP_RX_DP
2	USBP4N	7	GND
3	USBP4P	8	BP_TX_DN
4	GND	9	BP_TX_DP
5	BP_RX_DN	10	-



**USB2/
USB3**

eSATA (external SATA): a combo eSATA/USB 3.0 connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	5	SATA_RXN_2_C
2	SATA_TXP_2_C	6	SATA_RXP_2_C
3	SATA_TXN_2_C	7	GND
4	GND	-	-

eSATA (external Serial Advanced Technology Attachment) is a 7-wire/7-pin technology. The maximum cable length is 6 1/2 feet (2 meters). eSATA and SATA have the same number of wires/pins and their signal formats are the same

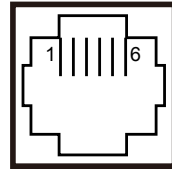
2-4-8.CASH DRAWER CONNECTOR

DRW1: RJ-11 Cash Drawer Connector (+12V/+24V selectable, default: +12V).

DRW1 is used by default.

The pin assignments are as follows:

PIN	ASSIGNMENT
1	DRW2 Sense
2	GPIO1 / DRW1
3	DRW1 Sense
4	12V/24V (Max. current 1A)
5	GPIO2 / DRW2
6	GND

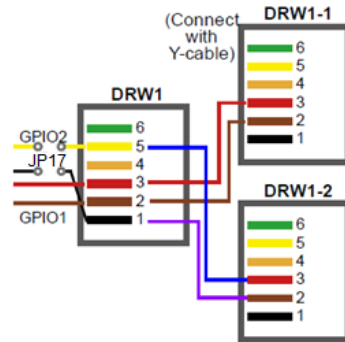


DRW1

2-4-9. CASH DRAWER SELECTION



JP17: DRW1, DRW1-1, DRW1-2
 DRW1 port is used by default. You can add a second port via either of the methods below:

Method 1:
 DRW1 includes two groups of GPIO pins. The second group is normally unused but can be enabled by the jumper. Set the pin header jumper JP17 as 1-2 connected if necessary.

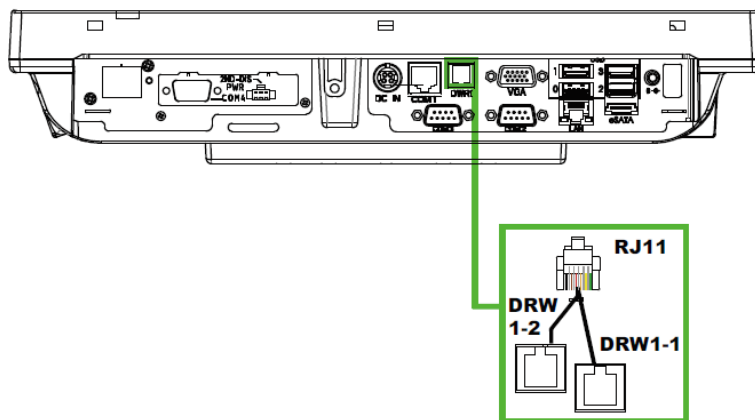


Method 2:
 You can split DRW1 into two channels of DRW1-1 & DRW1-2 using the Y-Cable (option).

JP17: Cash Drawer 2 Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Casher Drawer 2	1-2	 <p>JP17</p>
Casher Drawer1	2-3	 <p>JP17</p>

Note: Manufacturing Default is **Cash Drawer 1**.

**Step 3.**

DRW1, DRW1-1, DRW1-2 shares the same power source.
(Default: 12V).

SIO Address	
Cash drawer 1	LDN 06, 0x91 bit 4
Cash drawer 2	LDN 06, 0x91 bit 5

Cash Drawer Configuration

The I/O port address of the cash drawer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. User must first assign the address of register by writing address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

Configuration Sequence

To program [F81866](#) configuration registers, the following configuration sequence must be followed:

- (1) Enter the extended function mode
- (2) Configure the configuration registers
- (3) Exit the extended function mode

(1) Enter the extended function mode

To place the chip into the Extended Function Mode, [two successive writes of 0x87](#) must be applied to Extended Function Enable Registers (EFERs, i.e. 2Eh or 4Eh).

(2) Configure the configuration registers

The chip selects the Logical Device and activates the desired Logical Devices through Extended Function Index Register (EFIR) and Extended Function Data Register (EFDR). The EFIR is located at the same address as the EFER, and the EFDR is located at address (EFIR+1). First, write the Logical Device Number (i.e. 0x06) to the EFIR and then write the number of the desired Logical Device to the EFDR. If accessing the Chip (Global) Control Registers, this step is not required. Secondly, write the address of the desired configuration register within the Logical Device to the EFIR and then write (or read) the desired configuration register through the EFDR.

(3) Exit the extended function mode

To exit the Extended Function Mode, [writing 0xAA to the EFER](#) is required. Once the chip exits the Extended Function Mode, it is in the normal running mode and is ready to enter the configuration mode.



Code example for open the cash drawer 1

```
;----- Enter to extended function mode -----  
mov dx, 2eh  
mov al, 87h  
out dx, al  
out dx, al  
;----- Select Logical Device 6 of Cash drawer -----  
mov al, 07h  
out dx, al  
inc dx  
mov al, 06h  
out dx, al  
dec dx  
;----- Open the Cash drawer 1 -----  
mov al, 91h  
out dx, al  
inc dx  
mov al, 04h  
out dx, al  
;----- Exit the extended function mode -----  
dec dx  
mov al, 0aah  
out dx, al
```

2-4-10. CASH DRAWER POWER SELECTION

JP16: Cash Drawer Power Selection

The jumper settings are as follows:

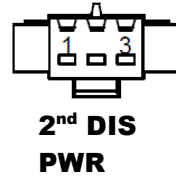
SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
24V	1-2	 JP16
12V	2-3	 JP16

Note: Manufacturing Default is 12V.

2-4-11. 2nd Display Power Port

2nd DIS PWR: DC12V power supply for 2nd display.

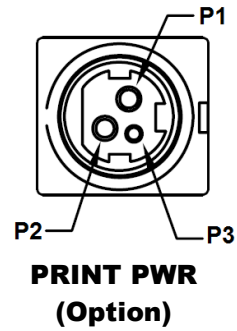
PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	VCC12	3	VCC12
2	GND	-	-



2-4-12. Printer Power Port (Option)

PRINT PWR: DC24V power supply for the stand-printer.

PIN	ASSIGNMENT
P1	GND
P2	+24V
P3	NA

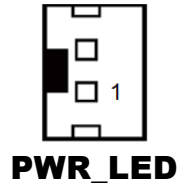


2-4-13. LED CONNECTOR

PWR_LED: Power indication LED Connector

The pin assignments are as follows:

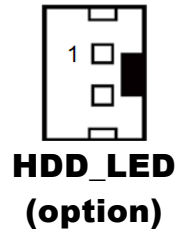
PIN	ASSIGNMENT
1	VCC5
2	GND



HDD_LED (option): HDD indication LED Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	VCC3_3
2	PCH_SATA_LED_N

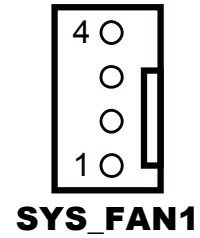


2-4-14. SYSTEM / CPU FAN CONNECTOR

SYS_FAN1: System Fan Connector 1

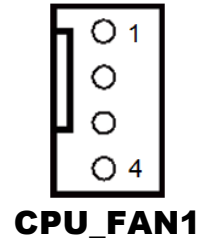
The pin assignments are as follows:

PIN	ASSIGNMENT
1	GND
2	VCC12
3	SYS_FANIN
4	SYS_FANOUT



CPU_FAN1: CPU Fan Connector 1

PIN	ASSIGNMENT
1	GND
2	VCC12
3	CPU_FANIN
4	CPU_FANOUT

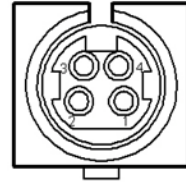


2-4-15. POWER INPUT CONNECTOR

PWR_IN1: Power Input Connector 1

The pin assignments are as follows:

PIN	ASSIGNMENT
1	GND
2	GND
3	24VIN
4	24VIN

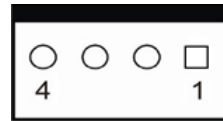


PWR_IN1

PWR_IN2: Power Input Connector 2

The pin assignments are as follows:

PIN	ASSIGNMENT
1	GND
2	24VIN
3	24VIN
4	GND



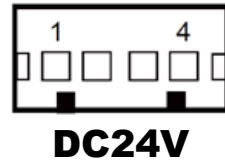
PWR_IN2

2-4-16. POWER CONNECTOR

DC24V: Power for Thermal Printer Connector

The pin assignments are as follows:

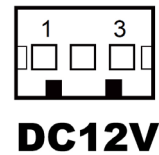
PIN	ASSIGNMENT
1	24VIN
2	24VIN
3	GND
4	GND



DC12V: DC 12Voltage Provider Connector

The pin assignments are as follows:

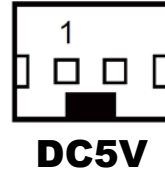
PIN	ASSIGNMENT
1	VCC12_GT
2	NC
3	GND



DC5V: DC 5Voltage Provider Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	V_5P0_A
2	GND

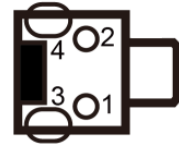


2-4-17. POWER SWITCH CONNECTOR

SW1: Power Switch Connector 1

The pin assignments are as follows:

PIN	ASSIGNMENT
1	GND
2	LPC_PWRBTNJ
3	GND
4	GND

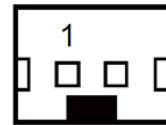


SW1
(option)

SW2: Power Switch Connector 2

The pin assignments are as follows:

PIN	ASSIGNMENT
1	LPC_PWRBTNJ
2	GND



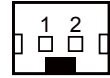
SW2

2-4-18. EXTERNAL SPEAKER CONNECTOR

SPK1: External Speaker Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	HD_FRONT-OUT1-R
2	HD_FRONT-OUT1-L



**SPK1/
SPK2
(option)**

SPK2: External Speaker Connector



The pin assignments are as follows:

PIN	ASSIGNMENT
1	HD_FRONT-OUT2-R
2	HD_FRONT-OUT2-L

2-4-19. SPEAKER SELECTION

JP13: SPK1/SPK2 Selection

The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
SPK1&SPK2	1-2	 JP13
Only SPK1	Open	 JP13

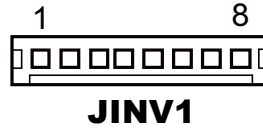
Note: Manufacturing Default is **SPK1&SPK2**.

2-4-20. INVERTER CONNECTOR

JINV1: Inverter Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	GND
2	VCC12
3	VCC12
4	VCC12
5	GND
6	LED_PWM
7	GND
8	PANLE_BKLTEN



2-4-21. LVDS CONNECTOR

LVDS1: LVDS Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	LVDS_VCC	16	LVDS_CLKAP
2	GND	17	LVDS_CLKAM
3	LVDS_CLKBM	18	GND
4	LVDS_CLKBP	19	LVDS_YAP2
5	GND	20	LVDS_YAM2
6	LVDS_YBM2	21	GND
7	LVDS_YBP2	22	LVDS_YAP1
8	GND	23	GND
9	LVDS_YBM1	24	GND
10	LVDS_YBP1	25	LVDS_YAP0
11	LVDS_YBP3	26	LVDS_YAM0
12	LVDS_YBM3	27	LVDS_YAP3
13	LVDS_YBP0	28	LVDS_YAM3
14	LVDS_YBM0	29	LVDS_VCC
15	GND	30	LVDS_VCC





LVDS1

2-4-22. LVDS POWER SELECTION

JP25: LVDS Power Selection

The jumper settings are as follows:



SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
3.3V	1-2	 JP25
5V	2-3	 JP25

Note: Manufacturing Default is 3.3V.

2-4-23. LVDS BACKLIGHT TYPE SELECTION

JP26: LVDS Backlight Type Selection

The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
3.3V	1-2	 JP26
5V	2-3	 JP26

Note: Manufacturing Default is 3.3V.

2-4-24. CARD READER CONNECTOR

PS2_1: Card Reader Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	KCLK_KB (Output)
2	KCLK_C (Input)
3	KDAT_C (Input)
4	KDAT_KB (Output)
5	+5V
6	GND



PS2_1

2-4-25. SATA & SATA POWER CONNECTOR

SATA1, SATA2: Serial ATA Connectors

The pin assignments are as follows:

PIN	ASSIGNMENT
1	G1
2	TX+
3	TX-
4	G2
5	RX-
6	RX+
7	G3



**SATA1/
SATA2**

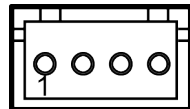
SATA_PWR1, SATA_PWR 2: Serial ATA Power Connectors

The pin assignments are as follows:

PIN	ASSIGNMENT
1	VCC
2	GND
3	GND
4	VCC12



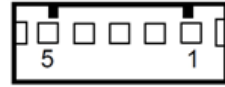
SATA_PWR1



SATA_PWR2

2-4-26. TOUCH PANEL CONNECTOR

TOUCH1: Touch Panel Connector
The pin assignments are as follows:




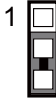
TOUCH1

PIN	ASSIGNMENT
1	L+
2	L-
3	COM
4	U+
5	U-

2-4-27. TOUCH PANEL & USB9-1 SELECTION

JP18, JP19: Touch Panel and USB9-1 Selection

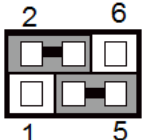
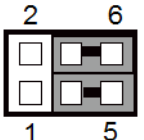
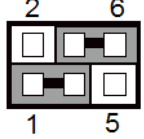
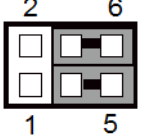
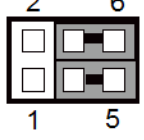
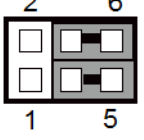
The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
USB9-1 Connector (Capacitor Panel Selection)	1-2	 JP18/JP19
TOUCH1 (Resistor Panel Selection)	2-3	 JP18/JP19

Note: Manufacturing Default is **TOUCH1**.

2-4-28. LVDS OUTPUT RESOLUTION SELECTION

JP8 & JP9: LVDS Output Resolution Selection

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION	
1024x768 (24 bit)	JP8(3-5) JP8(2-4) JP9(3-5) JP9 (4-6)	 <p>JP8</p>	 <p>JP9</p>
1024x768 (18 bit)	JP8(1-3) JP8(4-6) JP9(3-5) JP9(4-6)	 <p>JP8</p>	 <p>JP9</p>
800x600 (18 bit)	JP8(3-5) JP8(4-6) JP9(3-5) JP9(4-6)	 <p>JP8</p>	 <p>JP9</p>

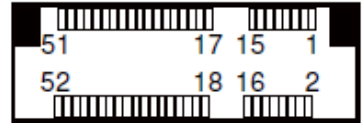
2-4-29. Mini-PCle/mSATA CONNECTOR

M_PCIE1, M_PCIE2:

MINI PCIE/mSATA Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	WAKE#	27	GND
2	+3.3V	28	+1.5V
3	Reserved	29	GND
4	GND	30	SMB_CLK
5	Reserved	31	PETn2
6	+1.5V	32	SMB_DATA
7	CLKREQ#	33	PETp2
8	Reserved	34	GND
9	GND	35	GND
10	Reserved	36	USB D-
11	REFCLK1-	37	GND
12	Reserved	38	USB D+
13	REFCLK1+	39	+3.3V
14	Reserved	40	GND
15	GND	41	+3.3V
16	Reserved	42	Reserved
17	Reserved	43	GND
18	GND	44	Reserved
19	Reserved	45	NC
20	Reserved	46	Reserved
21	GND	47	NC
22	PERST#	48	+1.5V
23	PERn0	49	NC
24	+3.3SB	50	GND
25	PERp0	51	Reserved
26	GND	52	+3.3V

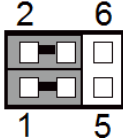
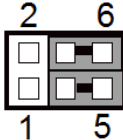


M_PCIE1/ M_PCIE2

2-4-30. Mini-PCle and USB6 Selection

JP21: Mini-PCle and USB6 Selection

The selection is as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
USB signal to USB6 wafer	1-3, 2-4	 <p>JP21</p>
USB signal to mini-PCle (Disabled)	3-5, 4-6	 <p>JP21</p>

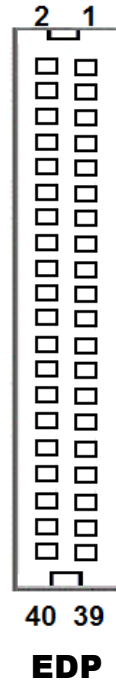
Note: Manufacturing Default is **USB signal to mini-PCle (Disabled)**.

2-4-31. Embedded Display Port (EDP) CONNECTOR (option)

EDP: EDP Connector

The pin assignments are as follows:

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	NC	21	VCC3_3
2	GND	22	NC
3	EDP_TX3_DN	23	GND
4	EDP_TX3_DP	24	GND
5	GND	25	GND
6	EDP_TX2_DN	26	GND
7	EDP_TX2_DP	27	EDP_LVDS_HPD
8	GND	28	GND
9	EDP_TX1_DN	29	GND
10	EDP_TX1_DP	30	GND
11	GND	31	GND
12	EDP_TX0_DN	32	EDP_BKLTEN
13	EDP_TX0_DP	33	EDP_BKLTCTL
14	GND	34	NC
15	EDP_AUX_DP_C	35	NC
16	EDP_AUX_DN_C	36	VCC12
17	GND	37	VCC12
18	VCC3_3	38	VCC12
19	VCC3_3	39	VCC12
20	VCC3_3	40	NC

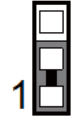




eDP (Embedded DisplayPort) was developed to be used specifically in embedded display applications, such as Notebook and Notepad PCs. eDP is based on the VESA DisplayPort Standard. It aims to define a standardized display panel interface for internal connections; e.g., graphics cards to notebook display panels. It has advanced power-saving features including seamless refresh rate switching. It has become the new mainstream display panel interface for LCD panels with the realized higher resolution.

2-4-32. CONFIGURATION / RECOVERY SELECTION

JP11: Configuration / Recovery Selection




The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
Normal	1-2	 1 JP11
Configure	2-3	 1 JP11
Recovery	Open	 1 JP11

2-4-33. VCCIO / REFIN SELECTION

JP10: VCCIO / Refine Selection



The jumper settings are as follows:

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
1.0V	1-2	 1 JP10
0.95V	2-3	 1 JP10
1.0V	Open	 1 JP10

Note: Manufacturing Default is **1.0V**.

2-4-34. CLEAR CMOS DATA SELECTION

JCMOS1: Clear CMOS Data Selection

SELECTION	JUMPTER SETTING	JUMPER ILLUSTRATION
Normal	Open	 JCMOS1
Clear CMOS Data	1-2	 JCMOS1

Note 1: Manufacturing Default is **Normal**.

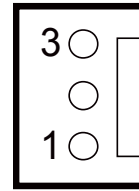
Note 2: To clear CMOS data, users must power off the computer and set the jumper to “Clear CMOS Data” as shown above. After five to six seconds, set the jumper back to “NC” and power on the computer.

2-4-35. GPIO CONNECTOR

GPIO1: General Purpose Input / Output Connector

The pin assignments are as follows:

PIN	ASSIGNMENT
1	3.3V(Maximum current: 0.5A)
2	GND
3	GPIO



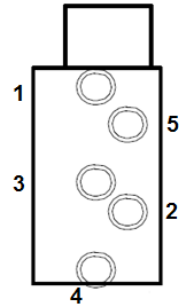
GPIO1

2-4-36. AUDIO JACK

LINE-OUT1: External audio phone jack port

The pin assignments are as follows:

PIN	ASSIGNMENT
1	HD_GND
2	LINE-OUT-R
3	NC
4	VCC_AUD
5	LINE-OUT-L

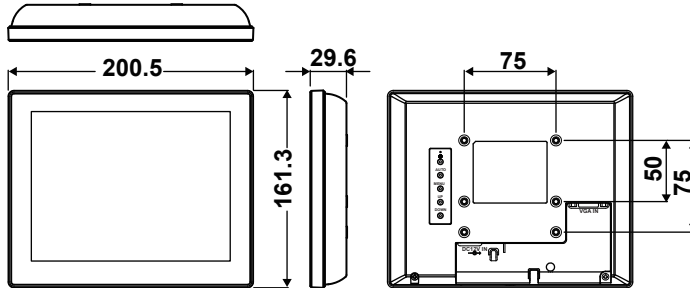


LINE-OUT1

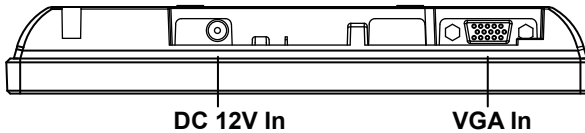
2nd Display Overview

2-5. 2nd Display Overview

Unit: mm



I/O View



Key Function:

	Power	Use the power switch to turn the power ON or OFF. We recommend turning your system power on first, then the LCD monitor.
	Auto Adjust	Pressing to execute AUTO-Adjusting function. This function will optimize the display image. Sometimes this function can't achieve the optimal effects. Please follow the manual adjusting procedure. After OSD menu is activated, pressing this key can turn off the OSD menu.
	Menu	Press Menu key to activate OSD Menu. This key also means "OK"; Press "Up, Down" key to move the indicator to the desired item, then press Menu key to select and read to adjust. After adjustment, press this key to return to previous Menu.
	Up	When OSD mode is activated (When "MENU" key is pressed), these two keys are used to select the adjustment items.
	Down	When adjustment item is selected, these two keys are defined to decrease or increase the value of each selected item.

VFD Board Component Locations & Pin Assignment

2-6. VFD Board: MB-4103, LD720 (option)

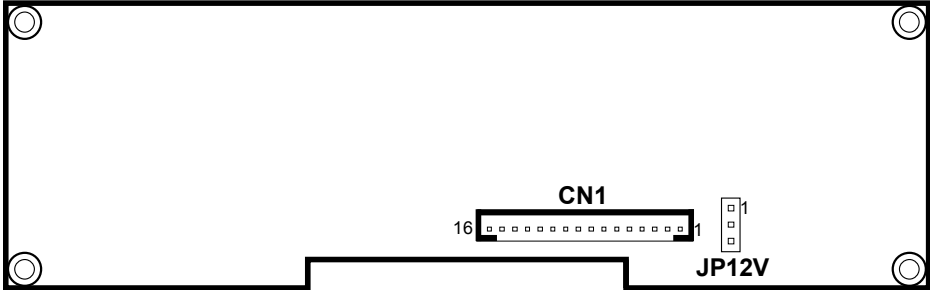


Figure 2-1. MB-4103 & LD720 VFD Board Component Locations

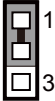

Jumper & Connector Quick Reference Table

Jumper / Connector	NAME
Power Switch Selection	JP12V
RS-232 Serial Interface Connector	CN1

Setting MB-4103 & LD720 VFD Board Connectors and Jumpers

2-6-1. Power Switch Selection

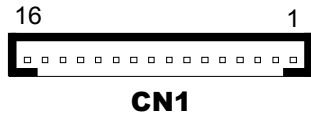
JP12V: Power Switch Selection

SELECTION	JUMPER SETTING	JUMPER ILLUSTRATION
OFF	1-2	 JP12V
ON (Default)	2-3	 JP12V

2-6-2. RS-232 Serial Interface Connector

CN1: RS-232 serial interface wafer

PIN	ASSIGNMENT	PIN	ASSIGNMENT
1	GND	9	NC
2	TXD	10	NC
3	RXD	11	NC
4	DTR	12	NC
5	DSR	13	NC
6	RTS	14	NC
7	CTS	15	NC
8	+12V/+5V	16	NC



SOFTWARE UTILITIES

CHAPTER
3

This chapter provides the detailed information users need to install driver utilities for the system.

The following sections are included:

- Driver
 - Intel® Chipset Software Installation Utility
 - VGA Driver Utility
 - LAN Driver Utility
 - Sound Driver Utility
 - Touch Screen Driver Utility
- Embedded Peripheral Device
 - VFD
- API

3-1. DRIVER

3-1-1. INTRODUCTION

Enclosed with the PA-2019 Series package is our driver utilities, which comes in a DVD-ROM disc. See the following table for driver locations.

FILE NAME (Assume that DVD-ROM drive is D:)	PURPOSE
<ul style="list-style-type: none"> ▪ D:\Driver\Platform\Win7 (32/64-bit)\Main Chip ▪ D:\Driver\Platform\Win8.1 (64-bit)\Main Chip ▪ D:\Driver\Platform\Win10 (64-bit)\Main Chip 	Intel® Chipset Software Installation Utility
<ul style="list-style-type: none"> ▪ D:\Driver\Platform\ Win7 (32/64-bit)\ME ▪ D:\Driver\Platform\ Win8.1 (64-bit)\ME ▪ D:\Driver\Platform\ Win10 (64-bit)\ME 	Intel® Management Engine Driver Installation
<ul style="list-style-type: none"> ▪ D:\Driver\Platform\ Win7(32/64-bit)\KMDF 	Intel® Kernel-Mode Driver Framework Driver Installation
<ul style="list-style-type: none"> ▪ D:\Driver\Platform\ Win7 (32/64-bit)\VGA ▪ D:\Driver\Platform\ Win8.1(64-bit)\VGA ▪ D:\Driver\Platform\ Win10 (64-bit)\VGA 	Intel® HD Graphics 530 Driver installation
<ul style="list-style-type: none"> ▪ D:\Driver\Platform\ Win7 (32/64-bit)\LAN ▪ D:\Driver\Platform\ Win8.1 (64-bit)\LAN ▪ D:\Driver\Platform\ Win10 (64-bit)\LAN 	Intel® Ethernet connection I219-V for LAN Driver Installation
<ul style="list-style-type: none"> ▪ D:\Driver\Platform\ Win7 (32/64-bit)\Sound ▪ D:\Driver\Platform\ Win8.1 (64-bit)\Sound ▪ D:\Driver\Platform\ Win10 (64-bit)\Sound 	Realtek® ALC888S for Sound Driver Installation
<ul style="list-style-type: none"> ▪ D:\Driver\Device\Platform\ Win7(32/64-bit)\USB3.0 	For USB3.0 Driver Installation
<ul style="list-style-type: none"> ▪ D:\Driver\Device 	Driver installation for Touch screen , wireless devices, etc.
<ul style="list-style-type: none"> ▪ D:\Driver\FLASH 	Driver installation for BIOS update utility (AMI)

Note: Be sure to install the driver utilities right after the OS is fully installed.

3-1-2. INTEL® CHIPSET SOFTWARE INSTALLATION UTILITY

Introduction

The Intel® Chipset Software Installation Utility installs to the target system the Windows* INF files that outline to the operating system how the chipset components will be configured. This is required for the following features to function properly:

- Core PCI and ISAPNP Services
- AGP Support
- SATA Storage Support
- USB Support
- Identification of Intel® Chipset Components in Device Manager

Installation of Intel® Chipset Driver

The utility pack is to be installed only for Windows 7 (32/64-bit), Windows 8.1/10 (64-bit only) series, and it should be installed right after the OS installation. Please follow the steps below:

1. Connect the USB DVD-ROM device to PA-2019 and insert the driver disk inside.
2. Enter the “Main Chip” folder where the Chipset driver is located (depending on your OS platform).
3. Click **Setup.exe** file for driver installation.
4. Follow the on-screen instructions to complete the installation.
5. Once the installation is completed, shut down the system and restart the PA-2019 for the changes to take effect.

3-1-3. VGA DRIVER UTILITY

The VGA interface embedded with the PA-2019 series can support a wide range of display types. You can have dual displays via CRT and LVDS interfaces work simultaneously.

Installation of VGA Driver

To install the VGA Driver, follow the steps below:

1. Connect the USB DVD-ROM device to PA-2019 and insert the driver disk inside.
2. Enter the “VGA” folder where the VGA driver is located (depending on your OS platform).
3. Click **Setup.exe** file for driver installation.
4. Follow the on-screen instructions to complete the installation.
5. Once the installation is completed, shut down the system and restart the PA-2019 for the changes to take effect.

3-1-4. LAN DRIVER UTILITY

The PA-2019 Series is enhanced with LAN function that can support various network adapters. Installation platform for the LAN driver is listed as follows:

For more details on the Installation procedure, please refer to the Readme.txt file found on LAN Driver Utility.

Installation of LAN Driver

To install the LAN Driver, follow the steps below:

1. Connect the USB DVD-ROM device to PA-2019 and insert the driver disk inside.
2. Enter the “LAN” folder where the LAN driver is located (depending on your OS platform).
3. Click **Setup.exe** file for driver installation.
4. Follow the on-screen instructions to complete the installation.
5. Once the installation is completed, shut down the system and restart the PA-2019 for the changes to take effect.

3-1-5. SOUND DRIVER UTILITY

The sound function enhanced in this system is fully compatible with Windows 7 (32/64-bit), Windows 8.1/10 (64-bit only) series. Below you will find the content of the Sound driver.

Installation of Sound Driver

To install the Sound Driver, refer to the readme.txt file on the driver disc (:\Sound\Realtek\Readme.txt).

1. Connect the USB DVD-ROM device to PA-2019 and insert the driver disk inside.
2. Enter the “Sound” folder where the Sound driver is located (depending on your OS platform).
3. Click **Setup.exe** file for driver installation.
4. Follow the on-screen instructions to complete the installation.
5. Once the installation is completed, shut down the system and restart the PA-2019 for the changes to take effect.

3-1-6. TOUCH SCREEN DRIVER UTILITY

The touch screen driver utility can only be installed on a Windows platform (Windows 7 (32/64-bit), Windows 8.1/10 (64-bit only) series), and it should be installed right after the OS installation.

Installation of Touch Screen Driver

To install the Touch Screen Driver, follow the steps below:

1. Connect the USB DVD-ROM device to PA-2019 and insert the driver disk inside.
2. Enter the “Device/Touchscreen” folder where the Touch Screen Driver is located.
3. Click **Setup.exe** file for driver installation.
4. Follow the on-screen instructions to complete the installation.
5. Once the installation is completed, shut down the system and restart the PA-2019 for the changes to take effect.

3-1-7. Fingerprint Driver Utility (Optional)

The fingerprint driver utility can only be installed on a Windows platform, and it should be installed right after the OS installation is completed.

Installing Fingerprint Driver

To install the fingerprint driver, follow the steps below:

1. Connect the USB DVD-ROM device to PA-2019 and insert the driver disk.
2. Open the “Device\Embedded Finger Printer” folder where the fingerprint driver is located.
3. Click **Setup.exe** file for driver installation.
4. Follow the on-screen instructions to complete the installation.
5. Once the installation is completed, shut down the system and restart PA-2019 for the changes to take effect.

3-2 PERIPHERAL DEVICE

The Command lists and driver installation guide for peripheral device of the system - VFD – are explicitly included in the sections below:

3-2-1 VFD: MB-4003 (RS-232)

3-2-1-1 Commands List

1. VFD Registry Operation

Registry Path:

[HKEY_LOCAL_MACHINE\SOFTWARE\OLEforRetail\ServiceOPOS\LineDisplay\
Prox-PMP4000]

Registry Name	Default Data	Notes
Default Value	LineDisplay.PMP4000.1	-
BaudRate	9600	-
BitLength	8	-
Parity	0	-
Port	COM1	-
Stop	1	-

2. OPOS VFD Service Object and Method Relations

Method	Status of Support	Notes
Open	○	-
Close	○	-
ClaimDevice	○	-
ReleaseDevice	○	-
Enable	○	-
Disable	○	-
DisplayText	○	-
DisplayTextAt	○	-
ClearText	○	-

3-2-1-2 OPOS Driver

The **MB4000_OposSetup.exe** program sets up the registry information and example program of VFD for OPOS program uses.

1. Installation

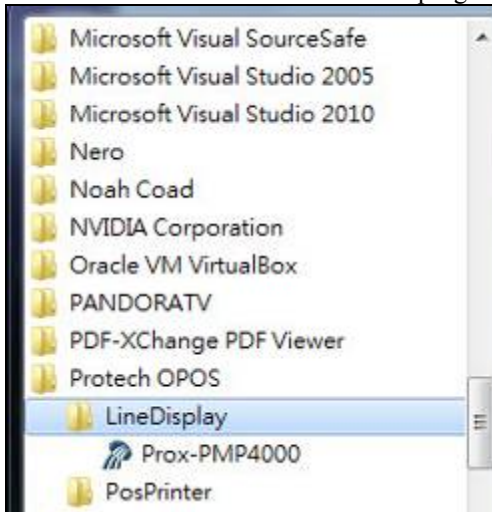
The steps below guide you to install the **MB4000_OposSetup** program.

- Run the **MB4000_OposSetup** setup file
- This setup also installs the **Prox-PMP4000** program.
- Follow the wizard instructions to complete the installation.

2. Launching the Program

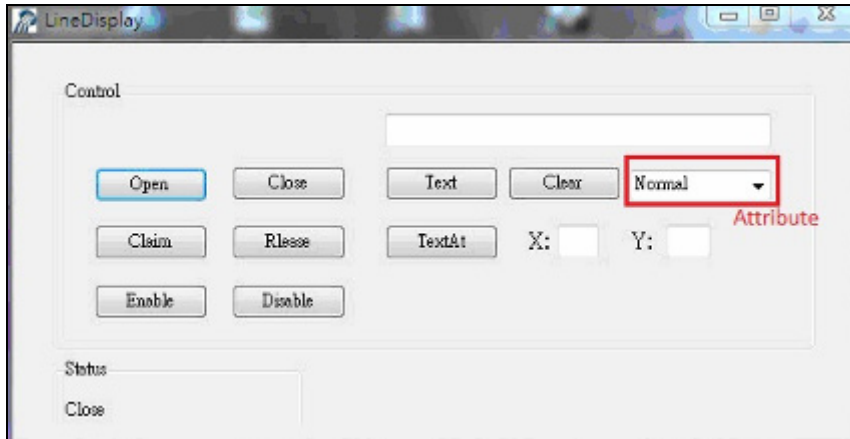
The steps below guide you to load the **Prox-PMP4000** program.

- Click the *LineDisplay* folder from the path: *Start/Programs/Protech OPOS*.
- Click **Prox-PMP4000** to launch the program.



3. OPOS Control Object of Prox-PMP4000 program

Main screen buttons:



Button/Item	Description
Text	Display the text at the current cursor position.
TextAt	Display the string of characters at the point of the specified “y-coordinate” and “x-coordinate”.
Clear	Clear the message shown in the current window.
Attribute	<ul style="list-style-type: none"> • Normal: Display the normal characters on the display screen. • Blink: Enable the display screen to blink. • Reverse: Enable the character printing in reverse black and white. • Blink+Reverse: Enable the display screen to blink and activate the character printing in reverse black and white.

4. MB4003 type

Key Name	Type	Default Value	Note
BaudRate	String	9600	UART Baud Rate (default)
BitLength	String	8	UART Data Bit (default)
Parity	String	0	UART Parity Bit (default)
Port	String	COM1	UART Port (default)
Stop	String	1	UART Stop Bit (default)

5. OPOS APIs Support List

	Category Type	Name	Mutability	OPOS APG Version	VFD .SO
Properties	common bool	AutoDisable	R/W	1.2	Not Applicable
Properties	common long	BinaryConversion	R/W	1.2	Not Applicable
Properties	common long	CapPowerReporting	Read only	1.3	Not Applicable
Properties	common string	CheckHealthText	Read only	1.0	Supported
Properties	common bool	Claimed	Read only	1.0	Supported
Properties	common long	DataCount	Read only	1.2	Not Applicable
Properties	common bool	DataEventEnabled	Read only	1.0	Not Applicable
Properties	common bool	DeviceEnabled	R/W	1.0	Not Applicable
Properties	common bool	FreezeEvents	R/W	1.0	Not Applicable
Properties	common long	OpenResult	Read only	1.5	Not Applicable
Properties	common bool	OutputID	Read only	1.0	Not Applicable
Properties	common bool	PowerNotify	R/W	1.3	Not Applicable
Properties	common bool	PowerState	Read only	1.3	Not Applicable
Properties	common long	ResultCode	Read only	1.0	Supported
Properties	common long	ResultCodeExtended	Read only	1.0	Not Applicable
Properties	common long	State	Read only	1.0	Supported
Properties	common string	ControlObject Description	Read only	1.0	Not Applicable
Properties	common long	ControlObject Version	Read only	1.0	Not Applicable
Properties	common string	ServiceObject Description	Read only	1.0	Supported
Properties	common long	ServiceObject Version	Read only	1.0	Supported
Properties	common string	DeviceDescription	Read only	1.0	Supported
Properties	common string	ControlObject Description	Read only	1.0	Not Applicable
Properties	specific long	CapBlink	Read only	1.0	Not Applicable
Properties	specific bool	CapBlinkRate	Read only	1.6	Not Applicable
Properties	specific bool	CapBrightness	Read only	1.0	Not Applicable
Properties	specific long	CapCharacterSet	Read only	1.0	Not Applicable
Properties	specific long	CapCursorType	Read only	1.6	Not Applicable
Properties	specific bool	CapCustomGlyph	Read only	1.6	Not Applicable
Properties	specific bool	CapDescriptors	Read only	1.0	Not Applicable
Properties	specific bool	CapHMarquee	Read only	1.0	Not Applicable
Properties	specific bool	CapICharWait	Read only	1.0	Not Applicable
Properties	specific long	CapReadBack	Read only	1.6	Not Applicable
Properties	specific long	CapReverse	Read only	1.6	Not Applicable
Properties	specific bool	CapVMarquee	Read only	1.0	Not Applicable

	Category Type	Name	Mutability	OPOS APG Version	VFD .SO
Properties	specific long	BlinkRate	R/W	1.6	Not Applicable
Properties	specific long	DeviceWindows	Read only	1.0	Not Applicable
Properties	specific long	DeviceRows	Read only	1.0	Not Applicable
Properties	specific long	DeviceColumns	Read only	1.0	Not Applicable
Properties	specific long	DeviceDescriptors	Read only	1.0	Not Applicable
Properties	specific long	DeviceBrightness	R/W	1.0	Not Applicable
Properties	specific long	CharacterSet	R/W	1.0	Not Applicable
Properties	specific string	CharacterSetList	Read only	1.0	Not Applicable
Properties	specific long	CurrentWindow	R/W	1.0	Not Applicable
Properties	specific long	Rows	Read only	1.0	Not Applicable
Properties	specific long	Columns	Read only	1.0	Not Applicable
Properties	specific long	CursorRow	R/W	1.0	Not Applicable
Properties	specific long	CursorColumn	R/W	1.0	Not Applicable
Properties	specific long	CursorType	R/W	1.6	Not Applicable
Properties	specific bool	CursorUpdate	R/W	1.0	Not Applicable
Properties	specific long	MarqueeType	R/W	1.0	Not Applicable
Properties	specific long	MarqueeFormat	R/W	1.0	Not Applicable
Properties	specific long	MarqueeUnitWait	R/W	1.0	Not Applicable
Properties	specific long	MarqueeRepeatWait	R/W	1.0	Not Applicable
Properties	specific long	InterCharacterWait	R/W	1.0	Not Applicable
Properties	specific string	CustomGlyphList	Read only	1.6	Not Applicable
Properties	specific long	GlyphHeight	Read only	1.6	Not Applicable
Properties	specific long	GlyphWidth	Read only	1.6	Not Applicable
Methods	common	Open	-	1.0	Supported
Methods	common	Close	-	1.0	Supported
Methods	common	Claim	-	1.0	Supported
Methods	common	ClaimDevice	-	1.0	Supported
Methods	common	Release	-	1.0	Supported
Methods	common	ReleaseDevice	-	1.0	Supported
Methods	common	CheckHealth	-	1.0	Not Applicable
Methods	common	ClearInput	-	1.0	Not Applicable
Methods	common	ClearOutput	-	1.0	Not Applicable
Methods	common	DirectIO	-	1.0	Not Applicable
Methods	specific	DisplayText	-	1.0	Supported
Methods	specific	DisplayTextAt	-	1.0	Supported
Methods	specific	ClearText	-	1.0	Supported
Methods	specific	ScrollText	-	1.0	Not Applicable
Methods	specific	SetDescriptor	-	1.0	Not Applicable
Methods	specific	ClearDescriptors	-	1.0	Not Applicable

	Category Type	Name	Mutability	OPOS APG Version	VFD .SO
Methods	specific	CreateWindow	-	1.0	Not Applicable
Methods	specific	DestroyWindow	-	1.0	Not Applicable
Methods	specific	RefreshWindow	-	1.0	Not Applicable)
Methods	specific	ReadCharacterAtCursor	-	1.6	Not Applicable
Methods	specific	DefineGlyph	-	1.6	Not Applicable
Events	common	DataEvent	-	1.0	Not Applicable
Events	common	DirectIOEvent	-	1.0	Not Applicable
Events	common	ErrorEvent	-	1.0	Not Applicable
Events	common	OutputComplete Event	-	1.0	Not Applicable
Events	common	StatusUpdate Event	-	1.3	Not Applicable

3-3 API

3-3-1 API Package Content

You can find the enclosed API Package files in the Protech Manual /Driver CD. Depending on the machine types, the API Package may include the following files:

Function DLL			
Directory	Function	File Name	Description
ProxAPI standard\	Cash Drawer	Cash Drawer.dll	Driver to control Cash Drawer
	WDT	Watchdog.dll	Driver to control Watchdog
	Hardware Monitor	Hardware Monitor.dll	Driver to read hardware data
	multilangXML.dll		Driver to open XML file
	Initial.xml		XML file to initiate the API Package
	ProxAP.exe		API program executable file
	XML Files\Model Name*\Initial.xml		XML file for each model
	Version.ini		Version Information

Sample Program		
Directory	Contents / File Name	Description
DEMO PROJECT\	DEMO PROJECT\GPIO Sample Code	C# VB6 VB.net Source Code
	DEMO PROJECT\Digital Sample Code	C# VB6 VB.net Source Code
	DEMO PROJECT\Watchdog Sample Code	C# VB6 VB.net MFC Source Code

3-3-2 API Procedure

Take **VB2005 .NET** for example. Follow the instructions below to perform the API procedure:

Step 1. Declare a function. You may create a module in your project and fill in the function.

Example: Cash drawer

Declare Function GetCashDrawerStatus Lib CashDrawer.dll (ByVal num_drawer as short) As Boolean

Declare Function CashDrawerOpen Lib CashDrawer.dll (ByVal num_drawer as short) As Boolean

Step 2. Create a button to call API Function.

a.) Call Cash drawer open event:

```
Private Sub cash_btn1_Click (ByVal Sender As System.Object, ByVal e As System.EventArgs) Handles cash_btn1.Click
    CashDrawerOpen(1), "1" specifies the cash drawer 1 port
    CashDrawerOpen(2), "2" specifies the cash drawer 2 port
    Timer1.start
```

b.) Detect Cash drawer status:

A timer event can be created.

```
Private Sub Timer1_Tick (ByVal Sender As System.Object,ByVal e As System.EventArgs) Handles Timer1.Tick
    Dim Receive_Status1 as Boolean
    Dim Receive_Status2 as Boolean
    Receive_Status1 = CashDrawerOpen(&H1)
    If Receive_Status1 = true then
        Text1.text = "cash drawer1 open" 'enter text into textbox.
    Else
        Text1.text = "cash drawer1 close" 'enter text into textbox.
    End if
    '=====
    Receive_Status2 = CashDrawerOpen(&H2)
    If Receive_Status2 = true then
        Text2.text = "cash drawer2 open" 'enter text into textbox.
    Else
        Text2.text = "cash drawer2 close" 'enter text into textbox.
    End if
```

```
End sub
```

Sample Code

(1) VB Declaration Method

```
Declare Function GetCashDrawerStatus Lib CashDrawer.dll (ByVal num_drawer  
as short) As Boolean
```

```
Declare Function CashDrawerOpen Lib CashDrawer.dll (ByVal num_drawer as  
short) As Boolean
```

(2) Call Function

Open cash drawer:

```
CashDrawerOpen(1)
```

Open cash drawer1

```
CashDrawerOpen(2)
```

Open cash drawer2

Check cash drawer status:

```
Dim receive_status as Boolean
```

Check cash drawer1 status

```
Receive_Status = CashDrawerOpen(&H1)
```

Check cash drawer2 status

```
Receive_Status = CashDrawerOpen(&H2)
```

(1) C# Declaration Method

```
Public class PortAccess  
{  
[DllImport("CashDrawer.dll",EntryPoint = "Initial_CashDrawer")]  
Public static extern void Initial_CashDrawer();  
[DllImport("CashDrawer.dll",EntryPoint= "GetCashDrawerStatus")]  
Public static extern bool GetCashDrawerStatus()  
[DllImport("CashDrawer.dll",EntryPoint = "CashDrawerOpen")]  
Public static extern bool CashDrawerOpen(short num_drawer);}
```

(2) Call Function

Open cash drawer1

```
PortAccess.CashDrawerOpen(0x01); //check cash drawer1 status
```

Open cash drawer2

```
PortAccess.CashDrawerOpen(0x02); //check cash drawer2 status
```

```
Bool bstatus;  
bstatus = PortAccess.GetCashDrawerStatus(0x01);  
bstatus = PortAccess.GetCashDrawerStatus(0x02); //Before get cash drawer  
status, need to initial cash drawer first
```

VB.NET external function:

```
Declare Function SetMinSec Lib "WatchDog.dll" (ByVal kind As Short,ByVal  
delay_time As Short) As Boolean
```

```
Declare Function Stopwatchdog Lib "WatchDog.dll" () As Short
```

```
Declare Function Setwatchdog Lib "WatchDog.dll" (ByVal value As Short) As  
Boolean
```

```
Declare Function Digital_Initial Lib "Digital.dll" () As Long
```

```
Declare Function Digital_Set Lib "Digital.dll"(ByVal hex_value As Short) As  
Long
```

```
Declare Function Digital_Get Lib "Digital.dll" () As Short
```

```
Declare Function GPIO_Initial Lib "GPIO.dll" () As Long
```

```
Declare Function GPIO_SetPort Lib "GPIO.dll"(ByVal direct As long)
```

```
Declare Function GPIO_Set Lib "GPIO.dll"(ByVal dout_value As long) As  
Boolean
```

```
Declare Function GPIO_Get Lib "GPIO.dll"() As Short
```

```
Declare Function GetCashDrawerStatus Lib CashDrawer.dll (ByVal num_drawer  
as short) As Boolean
```

```
Declare Function CashDrawerOpen Lib CashDrawer.dll (ByVal num_drawer as  
short) As Boolean
```

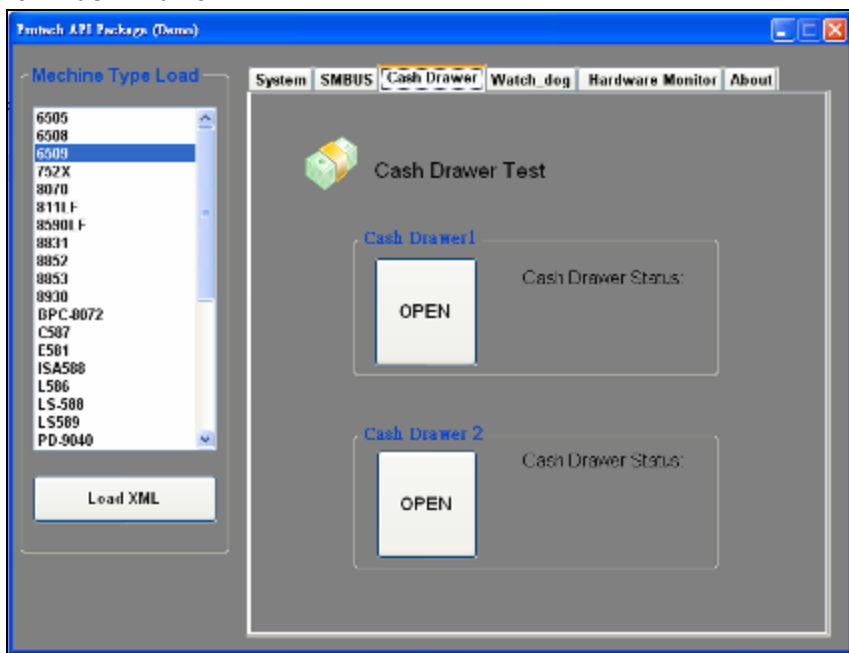
VB 6 external function:

```
Declare Function CashDrawerOpen Lib "CashDrawer.dll" (ByVal num_drawer  
As Integer) As Boolean
```

```
Declare Function GetCashDrawerStatus Lib "CashDrawer.dll" (ByVal  
num_drawer As Integer) As Boolean
```

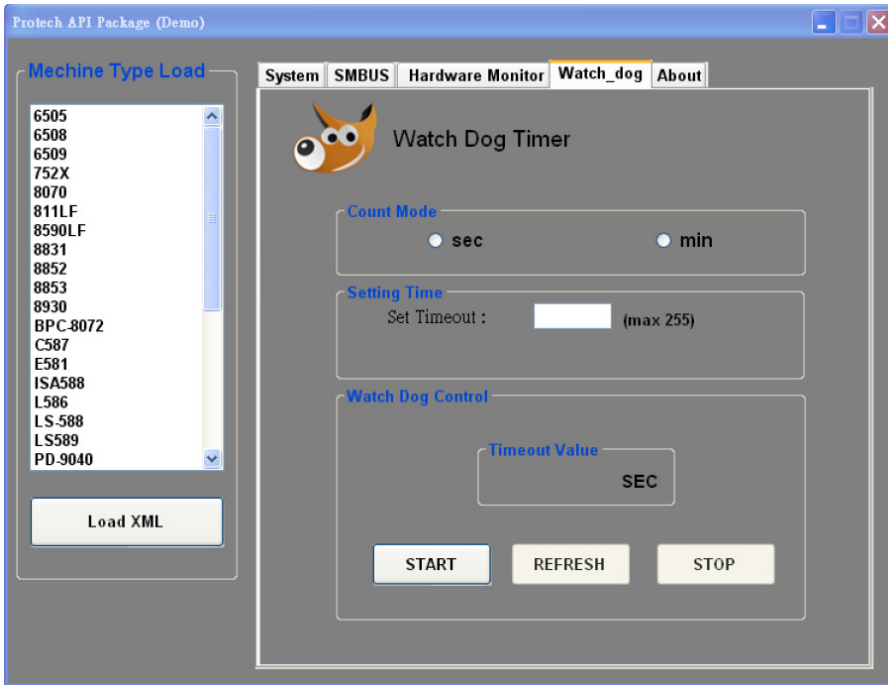
Note: VB.net short = integer VB6

3-3-3 Cash Drawer



Button/Item	Description
OPEN (button)	Tap to open the cash drawer.
Cash Drawer Status	<p>Cash drawer status will be displayed after OPEN is tapped.</p> <ul style="list-style-type: none"> • Cash Drawer is closed when the following picture is shown: <div style="text-align: center; border: 1px solid gray; padding: 5px; margin: 10px 0;"> Cash Drawer Status: Close </div> • Cash Drawer is opened when the following picture is shown: <div style="text-align: center; border: 1px solid gray; padding: 5px; margin: 10px 0;"> Cash Drawer Status: Open </div>

3-3-4 Watchdog



Button/Item	Description
Count Mode (radio button)	Select second or minute as the time unit of the watchdog timer.
Setting Time	Set the timeout for the watchdog timer. (Maximum value: 255 seconds or minutes)
Watch Dog Control	<ul style="list-style-type: none"> • Timeout Value: Simulation timer of the API program. The running watchdog timeout will be displayed (in seconds). It is not as accurate as a hardware watchdog clock. • START: Tap to start the watchdog timer. Meanwhile, the REFRESH and STOP buttons will be enabled. • STOP: Tap to stop the watchdog timer. • REFRESH: Tap to restart the watchdog timer.

3-4 API Functions

The API program-related sample programs, developed in VB.Net and C#, are provided for easy use of the API Package. Refer to the main API functions listed as below:

API Function		DLL	
Cash Drawer	CashDrawerOpen GetCashDrawerStatus	multilangXML.dll	CashDrawer.dll
Watchdog (WD)	Watchdog_Set Watchdog_Stop Watchdog_SetMinSec Watchdog_Recount		WatchDog.dll
Hardware Monitor	HMWVotlage_Get HMWTemperataure_Get HMWFanSpeed_Get		Hardware Monitor.dll

Cash Drawer Function

CashDrawerOpen

bool CashDrawerOpen (short num_drawer);

Purpose: Open the cash drawer API.
 Value: num_drawer = 1 (Open the Cash Drawer1)
 num_drawer = 2 (Open the Cash Drawer2)
 Return: True (1) on success, False (0) on failure

Example: CashDrawerOpen(0x01); // Open the Cash Drawer1

GetCashDrawerStatus

bool GetCashDrawerStatus (short num_drawer);

Purpose: Get the cash drawer status.
 Value: num_drawer = 1 (Get the Cash Drawer1 status)
 num_drawer = 2 (Get the Cash Drawer2 status)
 Return: True (1) on success, False (0) on failure

Example: Short data;
 data= GetCashDrawerStatus(0x01); // Get the Cash Drawer1 status
 if (data)
 MsgBox(“open1”); // Cash Drawer1 status
 “Open”
 Else
 MsgBox(“close1”); // Cash Drawer1 status
 “Close”
 Endif

Watch Dog Function

Watchdog_Set

bool Watchdog_Set (int value);

Purpose: Set the timeout for the watchdog timer.
Value value = 0 ~ 255
Return: True (1) on success, False (0) on failure

Watchdog_SetMinSec

bool Watchdog_SetMinSec (int kind);

Purpose: Set the unit of time as second/minute
Value kind = 1 (Measured in unit of second)
 2 (Measured in unit of minute)
Return: True (1) on success, False (0) on failure

Watchdog_Stop

bool Watchdog_Stop (void);

Purpose: Stop the watchdog timer
Value None
Return: True (1) on success, False (0) on failure

Watchdog_Recount

bool Watchdog_Recount (void);

Purpose: Restart the watchdog timer
Value None
Return: True (1) on success, False (0) on failure

AMI

BIOS SETUP

CHAPTER

4

This chapter shows how to set up the AMI BIOS.

The following sections are included:

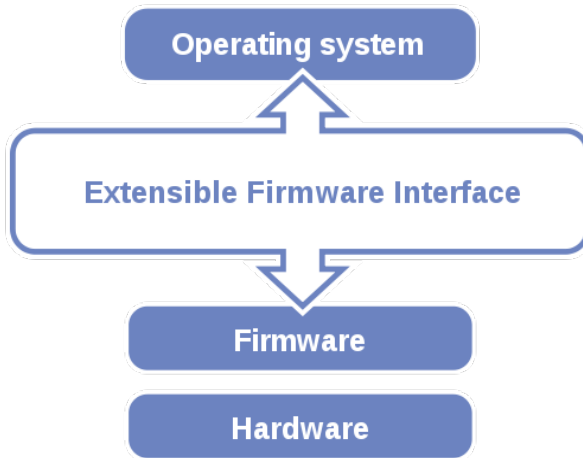
- Introduction
- Entering Setup Utility
- Main
- Advanced
- Chipset
- Security
- Boot
- Save & Exit

4-1. INTRODUCTION

The system PA-2019 uses an AMI Aptio BIOS that is stored in the Serial Peripheral Interface Flash Memory (SPI Flash) and can be updated. The SPI Flash contains the BIOS Setup program, Power-on Self-Test (POST), the PCI auto-configuration utility, LAN EEPROM information, and Plug and Play support.

Aptio is AMI’s BIOS firmware based on the UEFI (Unified Extensible Firmware Interface) specifications and the Intel Platform Innovation Framework for EFI. The UEFI specification defines an interface between the operating system and platform firmware. The interface consists of data tables that contain platform-related information, boot service calls, and runtime service calls that are available to the operating system and its loader. These elements have combined to provide a standard environment for booting the operating system and running pre-boot applications.

The diagram below shows the Extensible Firmware Interface’s location in the software stack.



EFI BIOS provides an user interface that allows you to modify hardware configuration, e.g. change the system date and time, enable/disable a system component, determine bootable device priority, set up personal password, etc., which is convenient for engineers to perform modifications and customize the computer system and allows technicians to troubleshoot the occurred errors when the hardware is faulty.

The BIOS setup menu allows users to view and modify the BIOS settings for the computer. After the system is powered on, users can access the BIOS setup menu by pressing or <Esc> immediately while the POST message is running before the operating system is loading.

4-2. ENTERING SETUP UTILITY

After the system is powered on, BIOS will enter the Power-On Self-Test (POST) routines and the POST message will be displayed:



POST Screen

Press the or <Esc> key to access the Setup Utility program and the **Main** menu of the Aptio Setup Utility will appear on the screen as below:



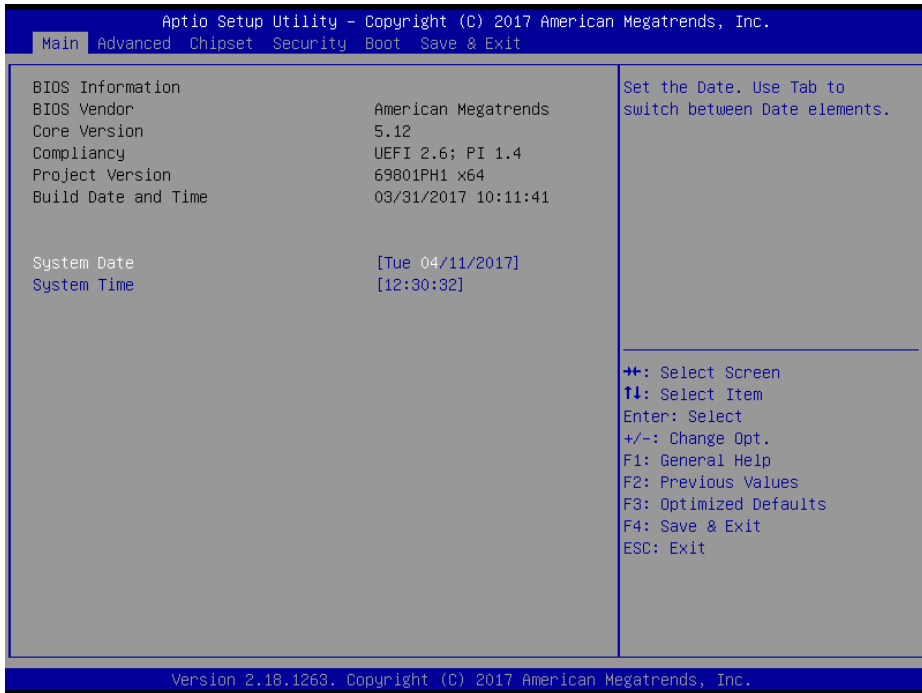
BIOS Setup Menu Initialization Screen

You may move the cursor by up/down keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear at the bottom of the screen.

4-3. MAIN

Menu Path *Main*

Use <↑> or <↓> arrow keys to highlight the item and key in the value you want in each item. This menu provides basic system configurations, such as system date and time.



Main Screen

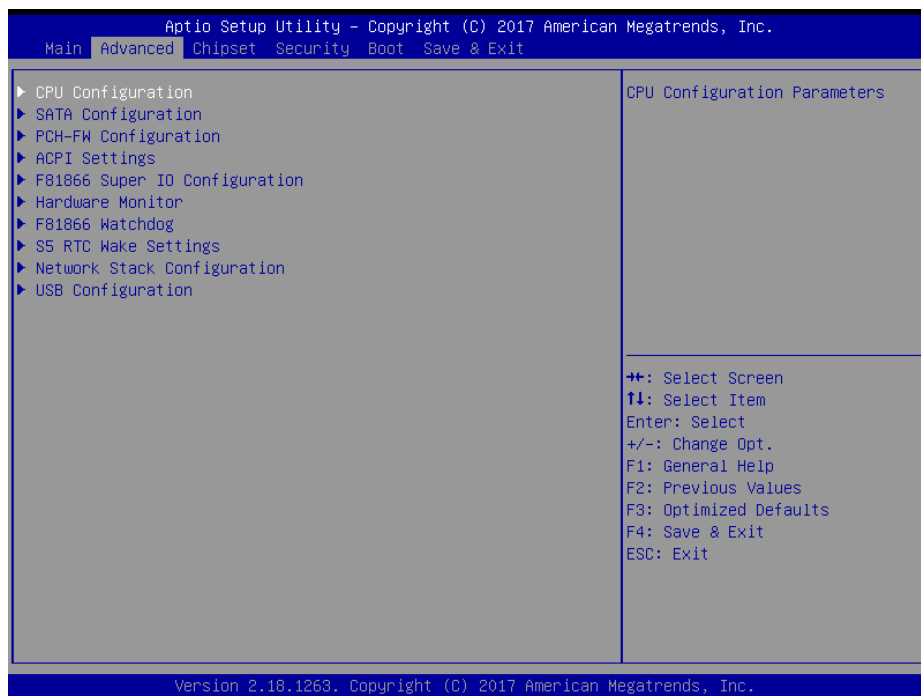
BIOS Setting	Options	Description/Purpose
BIOS Vendor	No changeable options	Displays the name of the BIOS vendor.
Core Version	No changeable options	Displays the current BIOS core version.
Compliancy	No changeable options	Displays the current UEFI version.
Project Version	No changeable options	Displays the version of the BIOS currently installed on the platform.
Build Date and Time	No changeable options	Displays the date that the current BIOS version is built.

BIOS Setting	Options	Description/Purpose
System Date	Month, day, year	Sets the system date. The format is [Day Month/ Date/ Year]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it. The "Day" is automatically changed.
System Time	Hour, minute, second	Sets the system time. The format is [Hour: Minute: Second]. Users can directly enter values or use <+> or <-> arrow keys to increase/decrease it.

4-4. ADVANCED

Menu Path *Advanced*

This menu provides advanced configurations such as sub-menus of CPU Configuration, SATA Configuration, PCH-FW Configuration, ACPI Settings, F81866 Super I/O Configuration, Hardware Health Configuration, F81866 Watchdog, S5 RTC Wake Settings, Network Stack Configuration and USB Configuration.



Advanced Menu Screen

BIOS Setting	Options	Description/Purpose
CPU Configuration	Sub-Menu	CPU Configuration Parameters.
SATA Configuration	Sub-Menu	SATA Device Options Settings.
PCH-FW Configuration	Sub-Menu	Management Engine Technology Parameters.

BIOS Setting	Options	Description/Purpose
ACPI Settings	Sub-Menu	System ACPI Parameters.
F81866 Super IO Configuration	Sub-Menu	System Super I/O Chip Parameters.
Hardware Monitor	Sub-Menu	Monitor hardware status.
F81866 Watchdog	Sub-Menu	F81866 Watchdog Parameters.
S5 RTC Wake Settings	Sub-Menu	S5 RTC Wake Settings.
Network Stack Configuration	Sub-Menu	Network Stack Settings.
USB Configuration	Sub-Menu	USB Configuration Parameters.

4-4-1. Advanced – CPU Configuration

Menu Path *Advanced > CPU Configuration*

The **CPU Configuration** provides advanced CPU settings and some information about CPU.



CPU Configuration Screen

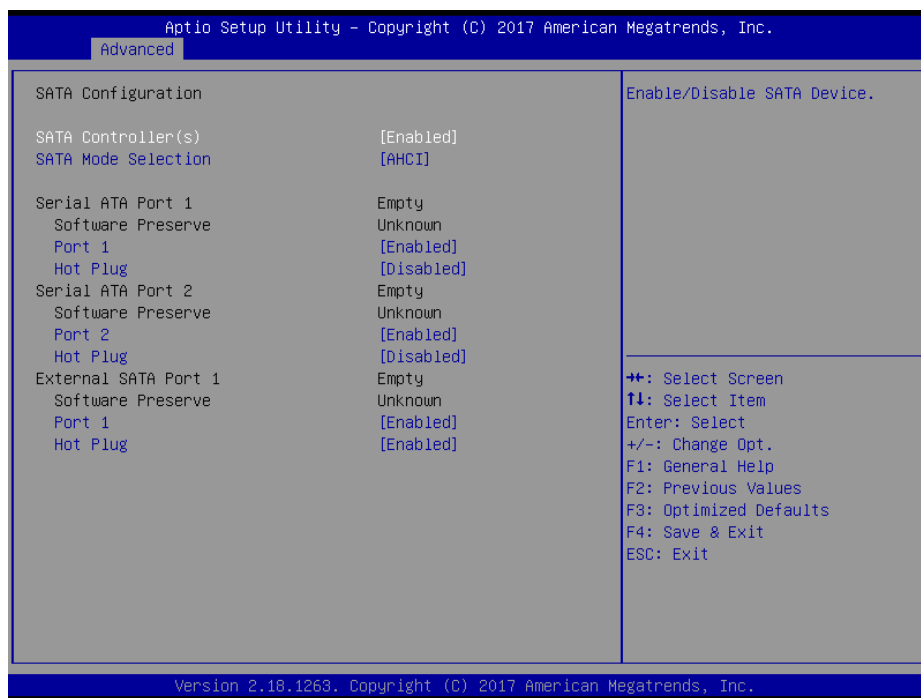
BIOS Setting	Options	Description/Purpose
Type	No changeable options	Displays CPU Model.
CPU Signature	No changeable options	Displays CPU Signature.
Microcode Patch	No changeable options	CPU Microcode Patch Revision.
CPU Speed	No changeable options	Displays the CPU Speed.
Processor Cores	No changeable options	Displays the number of cores of the processor.
VMX	No changeable options	Reports if Intel VT-x Technology is supported by the processor.

BIOS Setting	Options	Description/Purpose
		Previously codenamed "Vanderpool", VT-x represents Intel's technology for virtualization on the x86 platform. Utilizing Vanderpool Technology (VT), a VMM (Virtual Machine Monitor) can utilize the additional hardware capabilities.
SMX/TXT	No changeable options	Reports if Intel Secure Mode Extensions Technology is supported by the processor.
L1 Data Cache	No changeable options	Displays L1 Data Cache size.
L1 Instruction Cache	No changeable options	Displays L1 Instruction Cache size.
L2 Cache	No changeable options	Displays L2 Cache size.
L3 Cache	No changeable options	Displays L3 Cache size.
L4 Cache	No changeable options	Displays L4 Cache size
Hyper-threading	- Disabled - Enabled	When disabled, only one thread per enabled core is enabled.
Active Processor Cores	- All - 1 to n (depend on CPU)	Number of cores to enable in each processor package.
Intel (VMX) Virtualization Technology	- Disabled - Enabled	When enabled, a VMM (Virtual Machine Monitor) can utilize the additional hardware capabilities provided by Vanderpool Technology (VT).

4-4-2. Advanced – SATA Configuration (AHCI Mode)

Menu Path *Advanced > SATA Configuration*

The **SATA Configuration** allows users to enable / disable the SATA controller as well as the operational mode after the SATA controller is enabled. The following screen indicates the functions available when the SATA controller is enabled and the AHCI mode is specified.



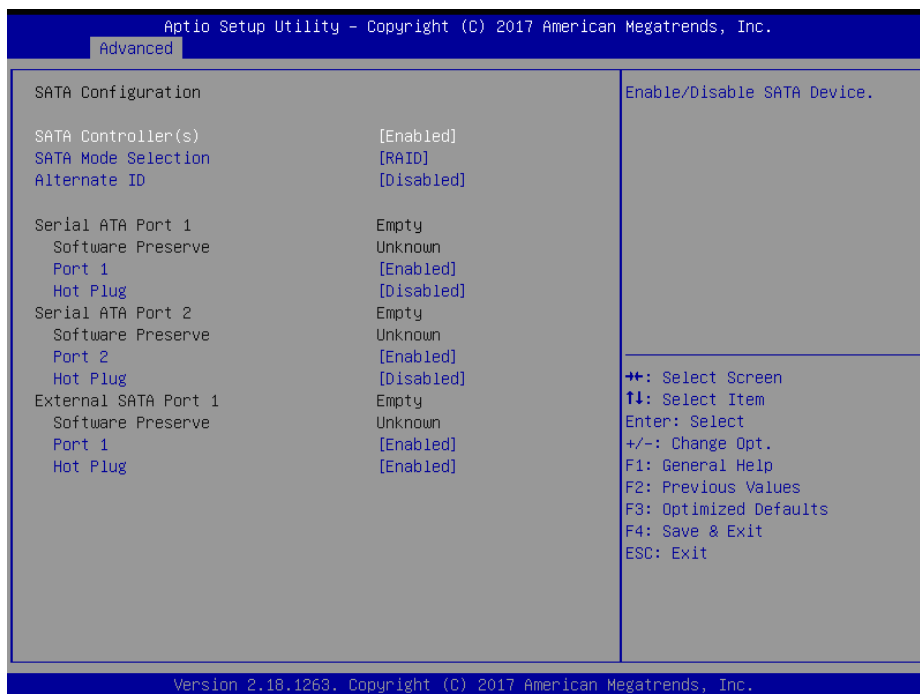
SATA Configuration Screen

BIOS Setting	Options	Description/Purpose
SATA Controller(s)	- Disabled - Enabled	Enables or Disables SATA Device.
SATA Mode Selection	- AHCI - RAID	Determines how SATA controller(s) operate.
Serial ATA Port 1 – 2,	No changeable options	Displays the SATA device's name.

BIOS Setting	Options	Description/Purpose
External SATA Port 1		
Software Preserve	No changeable options	Indicates whether the connected SATA device supports Software Setting Preservation (SSP).
Port 1 - 2	- Disabled - Enabled	Enables or Disables SATA Port 1 or 2 Device.
Hot Plug	- Disabled - Enabled	Enables or Disables Hot Plug function to designate a SATA port device as hot-pluggable.

Menu Path *Advanced > SATA Configuration (RAID for Q170 only)*

The following screen indicates the functions available when the SATA controller is enabled and the RAID mode is specified.



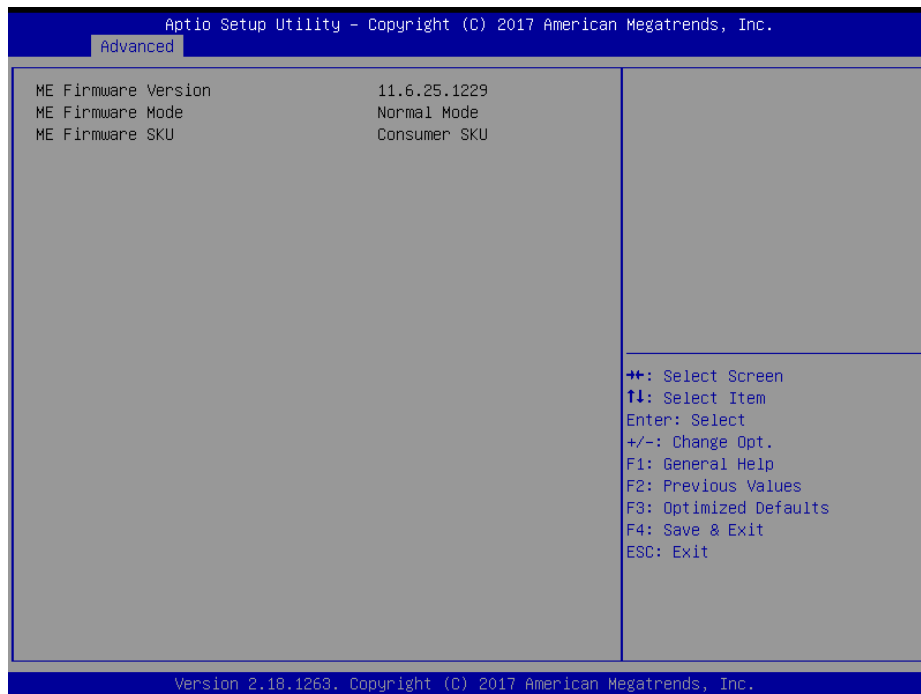
SATA Configuration Screen (RAID, for Q170 only)

BIOS Setting	Options	Description/Purpose
SATA Controller(s)	- Disabled - Enabled	Enables or Disables SATA device.
SATA Mode Selection	- AHCI - RAID	Determines how SATA controller(s) operate.
Alternate ID	- Disabled - Enabled	Reports the alternate device ID of SATA Controller.
Serial ATA Port 1 – 2, External SATA Port 1	No changeable options	Displays the SATA device's name.
Software Preserve	No changeable options	Indicates whether or not a connected device supports Software Setting

BIOS Setting	Options	Description/Purpose
		Preservation (SSP).
Port 1 - 2	- Disabled - Enabled	Enables or Disables SATA Port 1 or 2 device.
Hot Plug	- Disabled - Enabled	Enables or Disables Hot Plug function to designate a SATA port device as hot-pluggable.

4-4-3. Advanced – PCH-FW Configuration

The **PCH-FW** allows users to view the information about ME (Management Engine) firmware information, such ME firmware version, firmware mode and firmware SKU.



PCH-FW Configuration Screen

BIOS Setting	Options	Description/Purpose
ME FW Version	No changeable options	Display the ME Firmware Version.
ME Firmware Mode	No changeable options	Display the ME Firmware Mode.
ME Firmware SKU	No changeable options	Display the ME Firmware SKU.

4-4-4. Advanced – ACPI Settings

Menu Path *Advanced > ACPI Settings*

The **ACPI Settings** allows users to configure relevant ACPI (Advanced Configuration and Power Management Interface) settings, such as ACPI Sleep State, Hibernation, lock legacy resources, etc.

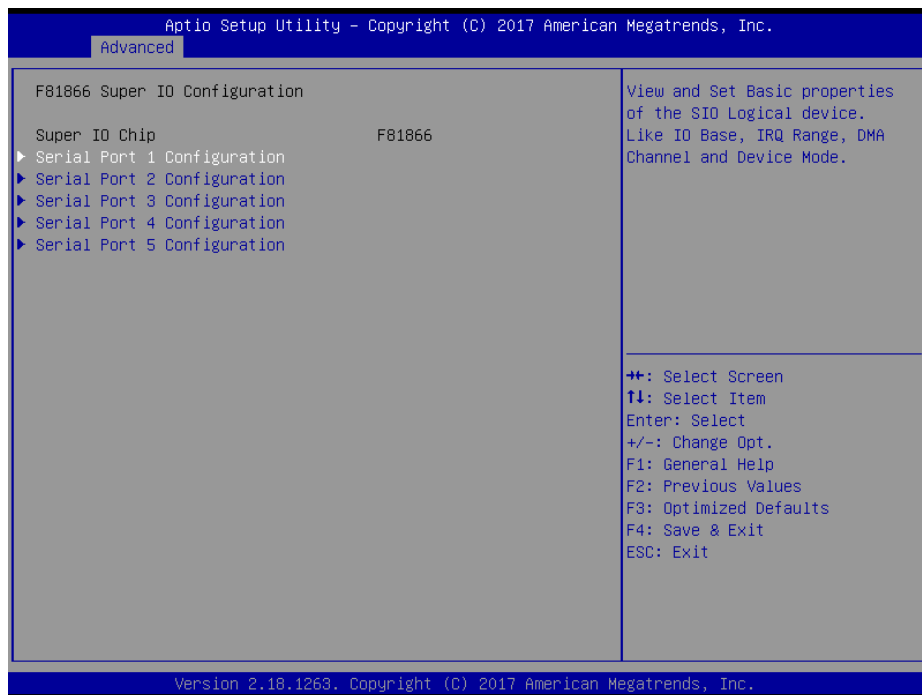


ACPI Settings Screen

BIOS Setting	Options	Description/Purpose
Enable Hibernation	- Disabled - Enabled	Enables or Disables System ability to Hibernate (OS/S4 Sleep State). This option may be not effective with some OS.
ACPI Sleep State	- Suspend Disabled - S3 (Suspend to RAM)	Selects the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.
Lock Legacy Resources	- Disabled - Enabled	Enables or Disables Lock of Legacy Resources.
S3 Video Repost	- Disabled - Enabled	Enables or Disables S3 Video Repost.

4-4-5. Advanced – F81866 Super IO Configuration

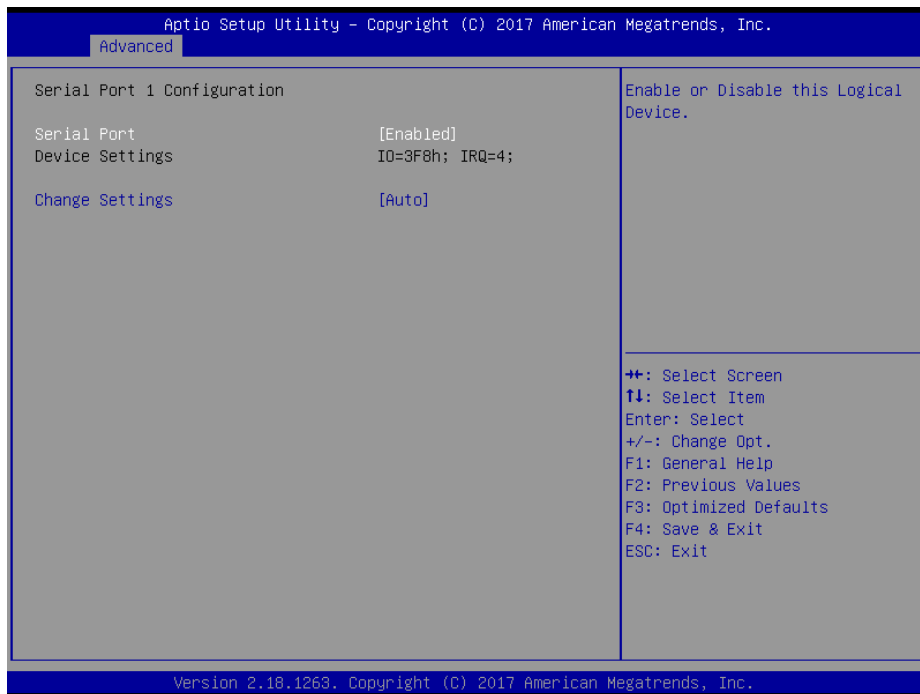
Menu Path *Advanced > F81866 Super IO Configuration*



F81866 Super IO Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port 1 Configuration	Sub-menu	Sets parameters of Serial Port 1 (COMA).
Serial Port 2 Configuration	Sub-menu	Sets parameters of Serial Port 2 (COMB).
Serial Port 3 Configuration	Sub-menu	Sets parameters of Serial Port 3 (COMC).
Serial Port 4 Configuration	Sub-menu	Sets parameters of Serial Port 4 (COMD).
Serial Port 5 Configuration	Sub-menu	Sets parameters of Serial Port 5 (COME).

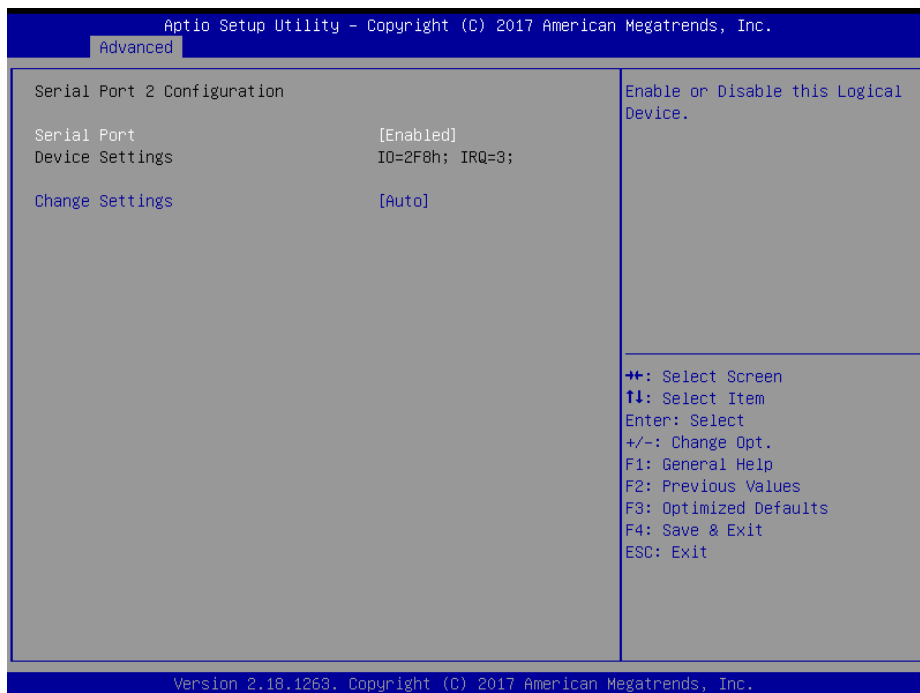
Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 1 Configuration*



Serial Port 1 Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port	- Disabled - Enabled	Enables or Disables Serial Port 1.
Device settings	No changeable options	Displays the current settings of Serial Port 1.
Change Settings	- Auto - IO=3F8h; IRQ=4; - IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	Selects IRQ and I/O resource settings for Serial Port 1.

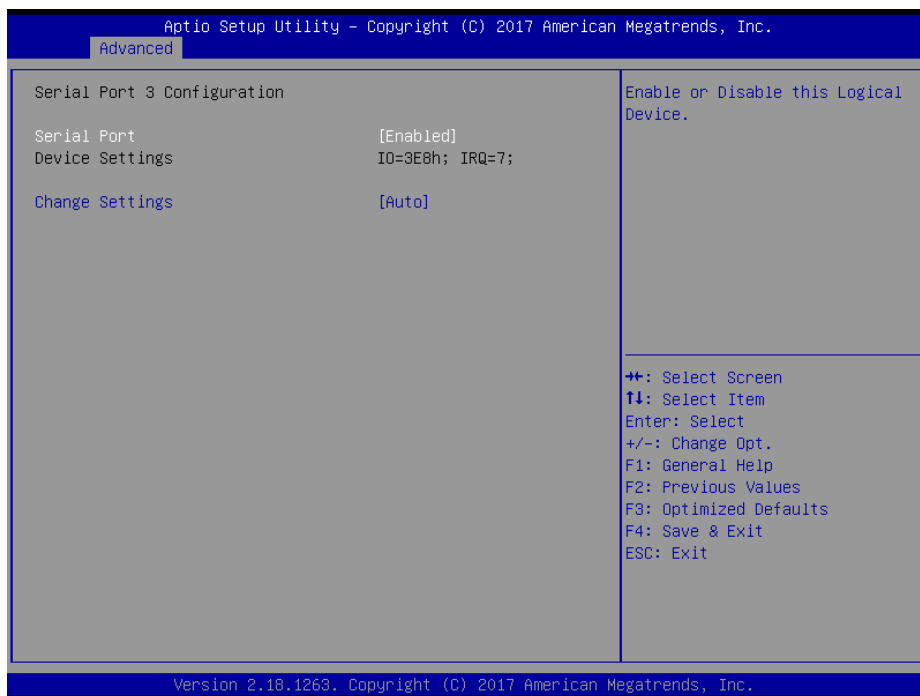
Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 2 Configuration*



Serial Port 2 Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port	- Disabled - Enabled	Enables or Disables Serial Port 2.
Device Settings	No changeable options	Displays the current settings of Serial Port 2.
Change Settings	- Auto - IO=2F8h; IRQ=3; - IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;	Selects IRQ and I/O resource settings for Serial Port 2.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 3 Configuration*



Serial Port 3 Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port	- Disabled - Enabled	Enables or Disables Serial Port 3.
Device Settings	No changeable options	Displays the current settings of Serial Port 3.
Change Settings	- Auto - IO=3E8h; IRQ=7; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F0h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E0h; IRQ=3,4,5,6,7,9,10,11,12;	Selects IRQ and I/O resource settings for Serial Port 3.

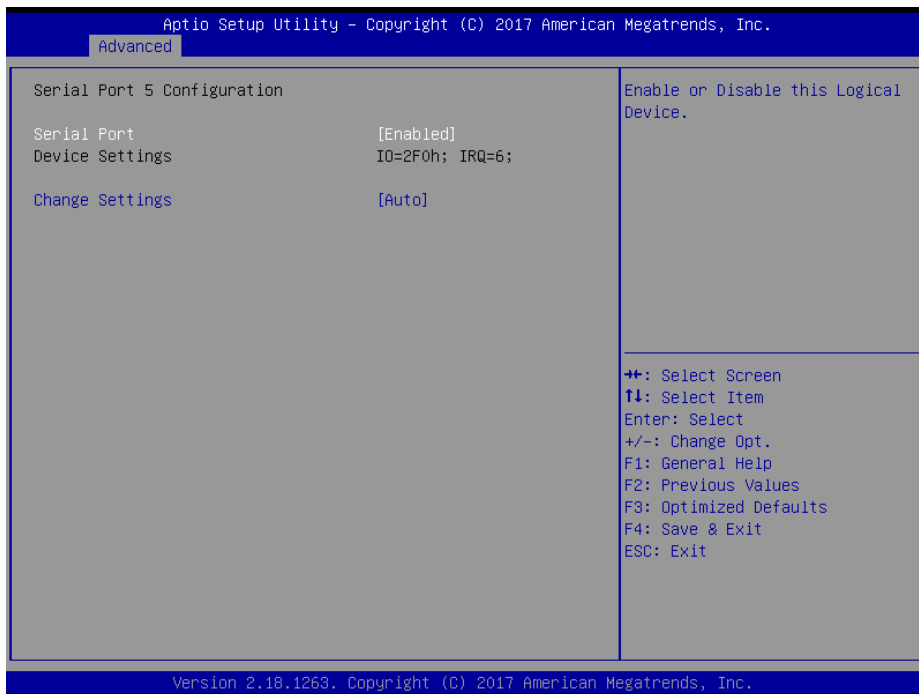
Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 4 Configuration*



Serial Port 4 Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port	- Disabled - Enabled	Enables or Disables Serial Port 4.
Device Settings	No changeable options	Displays the current settings of Serial Port 4.
Change Settings	- Auto - IO=2E8h; IRQ=10; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F0h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E0h; IRQ=3,4,5,6,7,9,10,11,12;	Selects IRQ and I/O resource settings for Serial Port 4.

Menu Path *Advanced > F81866 Super IO Configuration > Serial Port 5 Configuration*



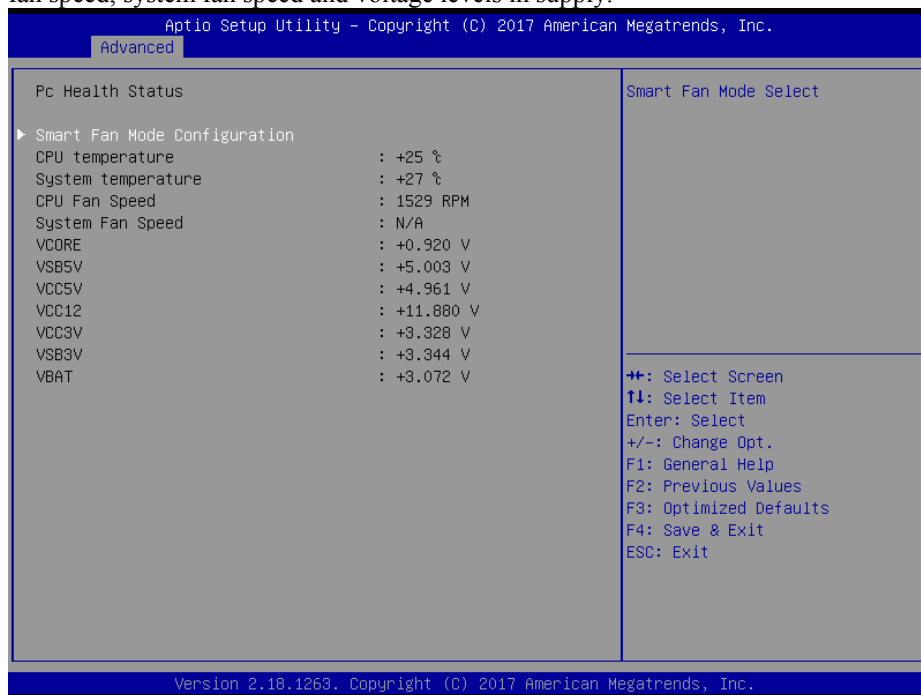
Serial Port 5 Configuration Screen

BIOS Setting	Options	Description/Purpose
Serial Port	- Disabled - Enabled	Enables or Disables Serial Port 5.
Device Settings	No changeable options	Displays the current settings of Serial Port 5.
Change Settings	- Auto - IO=2F0h; IRQ=6; - IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2F0h; IRQ=3,4,5,6,7,9,10,11,12; - IO=2E0h; IRQ=3,4,5,6,7,9,10,11,12;	Selects IRQ and I/O resource settings for Serial Port 5.

4-4-6. Advanced – Hardware Monitor

Menu Path *Advanced > Hardware Monitor*

The **Hardware Monitor** allows users to monitor the health and status of the system such as Smart Fan Mode Configuration, CPU temperature, system temperature, CPU fan speed, system fan speed and voltage levels in supply.



Hardware Monitor Screen

BIOS Setting	Options	Description/Purpose
Smart Fan Mode Configuration	Sub-Menu	Smart Fan Mode Selection.
CPU Temperature	No changeable options	Displays the processor's temperature.
System Temperature	No changeable options	Displays the system's temperature.
CPU Fan Speed	No changeable options	Display CPU Fan speed.
System Fan Speed	No changeable options	Display System Fan speed
VCORE	No changeable options	Detects and displays the VCORE CPU

BIOS Setting	Options	Description/Purpose
VSB5V	No changeable options	voltage. Detects and displays VSB5V voltage.
VCC5V	No changeable options	Detects and displays 5V voltage.
VCC12	No changeable options	Detects and displays 12V voltage.
VCC3V	No changeable options	Detects and displays 3V voltage.
VSB3V	No changeable options	Detects and displays the voltage level of VSB3V in supply.
VBAT	No changeable options	Detects and displays the battery voltage.

4-4-6-1. Advanced – Smart Fan Mode Configuration

Menu Path *Advanced > Hardware Monitor > Smart Fan Mode Configuration*



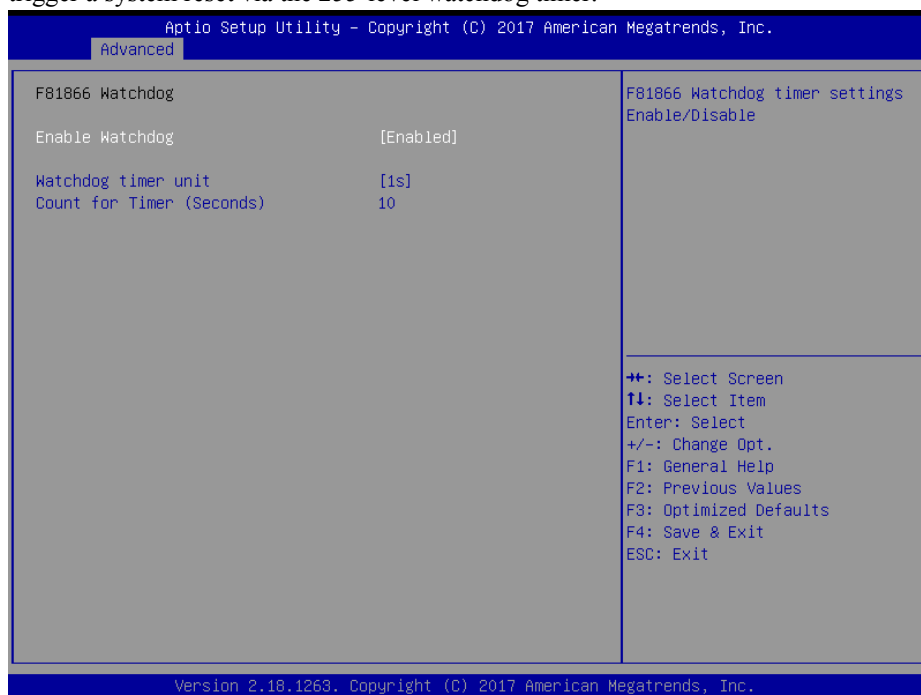
Smart Fan Mode Configuration Screen

BIOS Setting	Options	Description/Purpose
CPU Fan Smart Fan Control	- Manual Duty Mode - Auto Duty-Cycle Mode	Selects Smart Fan Mode for CPU Fan.
Manual Duty Mode	Numeric (from 1 to 100)	Manual mode fan control. Users can write expected duty cycle (PWM fan type) from 1 to 100.
System Fan Smart Fan Control	- Manual Duty Mode - Auto Duty-Cycle Mode	Selects Smart Fan Mode for System Fan.
Manual Duty Mode	Numeric (from 1 to 100)	Manual mode fan control. Users can write expected duty cycle (PWM fan type) from 1 to 100.

4-4-7. Advanced – F81866 Watchdog Configuration

Menu Path *Advanced > F81866 Watchdog*

If the system hangs or fails to respond, enable the F81866 watchdog function to trigger a system reset via the 255-level watchdog timer.



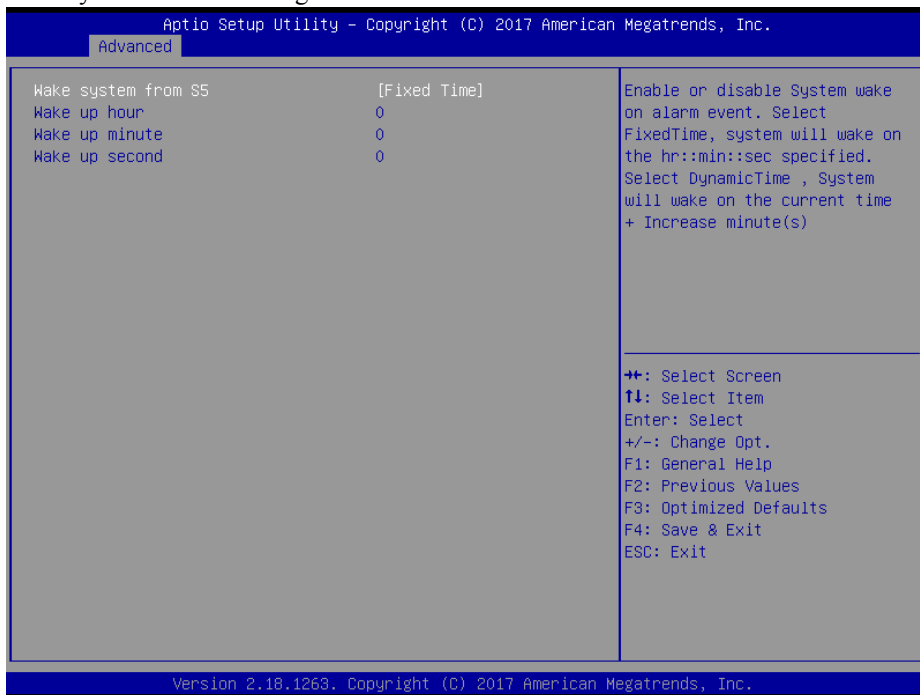
F81866 Watchdog Configuration Screen

BIOS Setting	Options	Description/Purpose
Enable Watchdog	- Enabled - Disabled	Enables/Disables F81866 Watchdog timer settings.
Watchdog timer unit	- 1s - 60s	Selects 1s (second) or 60s (minute) as the time unit of Watchdog timer.
Count for Timer (Seconds)	Numeric (from 1 to 255)	Sets the timeout for Watchdog timer. (Max. value: 255 seconds or minutes)

4-4-8. Advanced – S5 RTC Wake Settings

Menu Path *Advanced > S5 RTC Wake Settings*

The **S5 RTC Wake Settings** enables/disables the system to wake up at a preset time of a day from S5 State using RTC alarm.



S5 RTC Wake Settings Screen

BIOS Setting	Options	Description/Purpose
Wake system from S5	- Disabled - Fixed Time - Dynamic Time	Enables or disables System wake on alarm event. <ul style="list-style-type: none"> • Fixed Time: The system will wake on the time (hr::min::sec) specified. • Dynamic Time: The system will wake on the current time + Increase minute(s).
Wake up hour	Numeric (from 0 to 23)	Enters 0-23 to set the wake-up hour, e.g.: enters 3 for 3 am. and 15 for 3 pm.

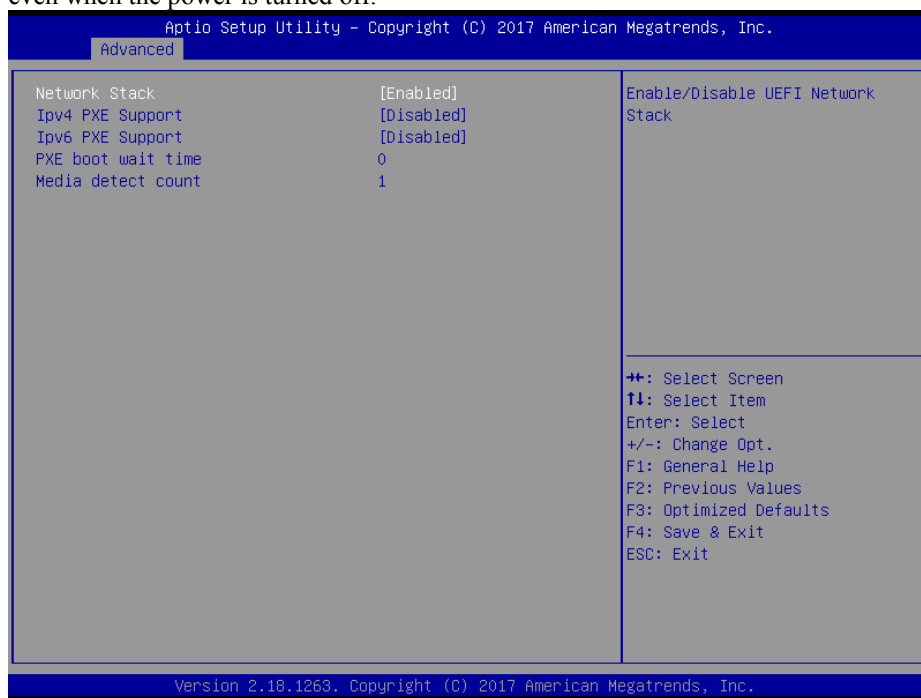
BIOS Setting	Options	Description/Purpose
Wake up minute	Numeric (from 0 to 59)	Enters 0-59 to set the wake-up minute.
Wake up second	Numeric (from 0 to 59)	Enters 0-59 to set the wake-up second.
Wake up minute increase	Numeric (from 1 to 5)	Enters 1-5 to set the increased minute(s) for dynamic wake-up time.

4-4-9. Advanced – Network Stack Configuration

Menu Path *Advanced > Network Stack Configuration*

The **Network Stack Configuration** allows users to enable/disable UEFI Network Stack, IPv4/IPv6 PXE (Pre-Boot Execution) support and configure PXE boot wait time and detects the media presence.

PXE allows a workstation to boot from a server on a network prior to booting the operating system on the local hard drive. A PXE-enabled workstation connects its NIC to the LAN via a jumper, which keeps the workstation connected to the network even when the power is turned off.



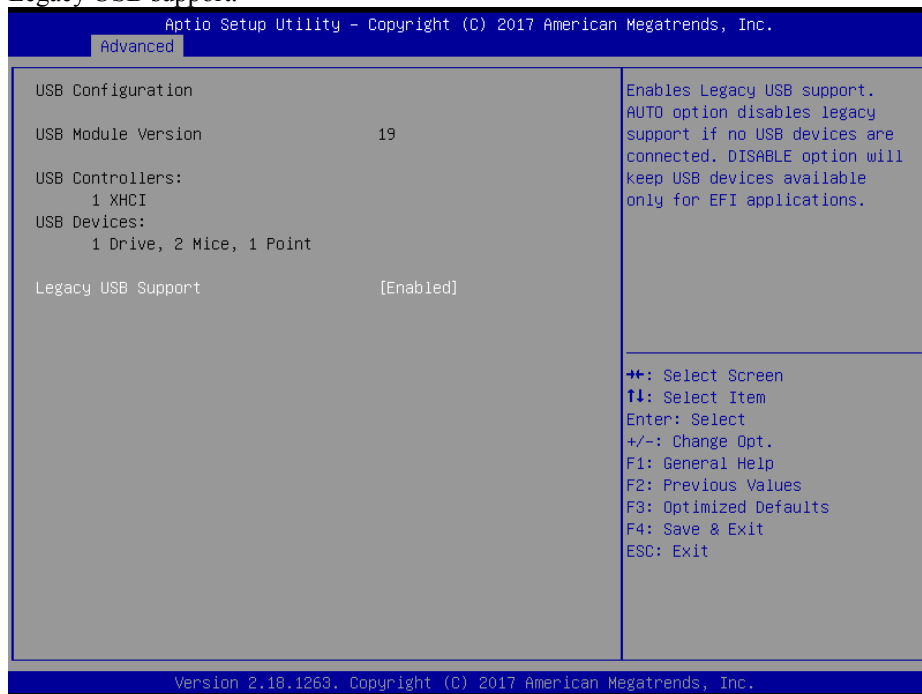
Network Stack Configuration Screen

BIOS Setting	Options	Description/Purpose
Network Stack	- Disabled - Enabled	Enables or Disables UEFI Network Stack.
Ipv4 PXE Support	- Disabled - Enabled	Enables IPv4 PXE Boot Support. If disabled, IPv4 PXE boot option will not be created.
Ipv6 PXE Support	- Disabled - Enabled	Enables IPv6 PXE Boot Support. If disabled, IPv6 PXE boot option will not be created.
PXE boot wait time	Numeric (from 0 to 5)	Number of seconds to wait for PXE boot to abort after the Esc key is pressed.
Media detect count	Numeric (from 1 to 50)	Number of times that the media presence will be checked.

4-4-10. Advanced – USB Configuration

Menu Path *Advanced > USB Configuration*

The **USB Configuration** allows users to configure advanced USB settings such as Legacy USB support.



USB Configuration Screen

BIOS Setting	Options	Description/Purpose
Legacy USB Support	- Disabled - Enabled - Auto	Sets to "Enabled" if you want to use USB device in the legacy operating system.

4-5. CHIPSET

Menu Path *Chipset*

This menu allows users to configure advanced Chipset settings such as System Agent (SA) and PCH-IO configuration parameters.



Chipset Screen

BIOS Setting	Options	Description/Purpose
System Agent (SA) Configuration	Sub-menu	System Agent (SA) parameters.
PCH-IO Configuration	Sub-menu	PCH parameters.

4-5-1. Chipset – System Agent (SA) Configuration

Menu Path *Chipset > System Agent (SA) Configuration*

The **System Agent Configuration** allows users to configure graphics settings and displays the DRAM information on the platform.



System Agent (SA) Configuration Screen

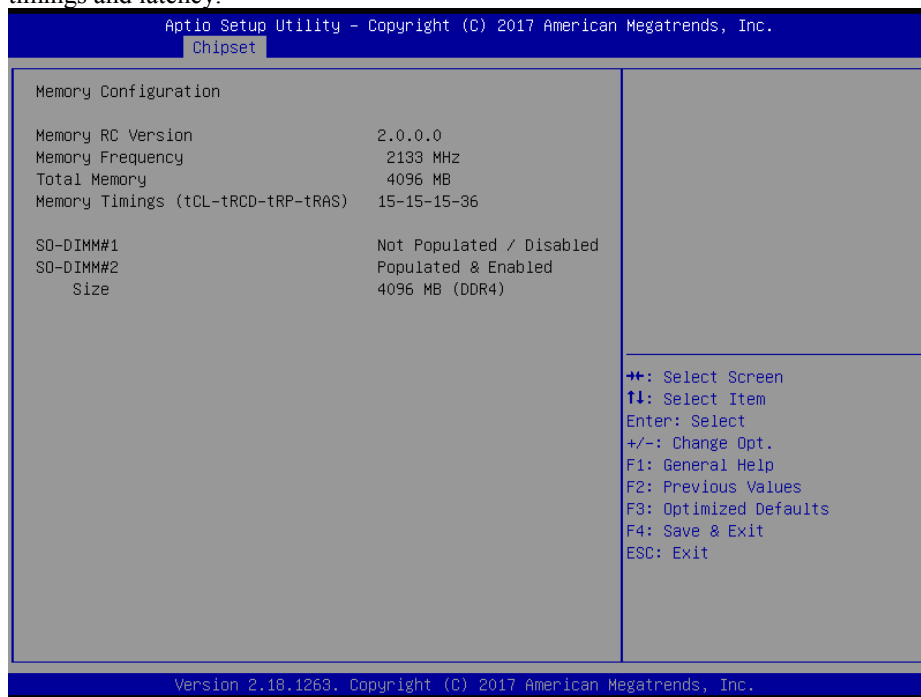
BIOS Setting	Options	Description/Purpose
SA PCIe Code Version	No changeable options	Displays the SA PCIe Code Version.
VT-d	No changeable options	Indicates whether Intel's VT-d (Virtualization Technology for Directed I/O) capability is supported. VT-d extends Intel's Virtualization Technology (VT) roadmap by providing hardware assists for virtualization solution, and helps end users improve security and reliability of the systems and also improves performance of

BIOS Setting	Options	Description/Purpose
		I/O devices in virtualized environment.
VT-d	- Disabled - Enabled	Enables or Disables VT-d function.
Memory Configuration	Sub-menu	Displays the DRAM information on the platform.
Graphics Configuration	Sub-menu	Configures Graphics Settings.

4-5-1-2. Chipset – Memory Configuration

Menu Path *Chipset > Memory Configuration*

The **Memory Configuration** allows users to check for the information about the memory frequency, total DRAM size, SO-DIMM#1, 2 size, and memory (RAM) timings and latency.



Memory Configuration Screen

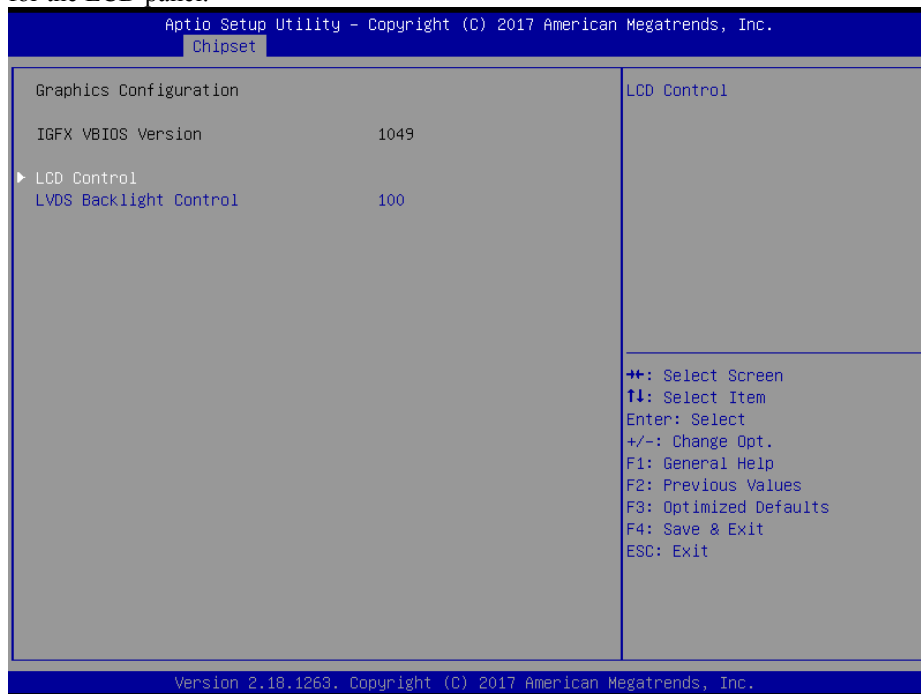
BIOS Setting	Options	Description/Purpose
Memory RC Version	No changeable options	Displays the Memory RC Version.
Memory Frequency	No changeable options	Displays the Frequency of Memory.
Total Memory	No changeable options	Displays the total system memory.
Memory Timings (tCL-tRCD-tRP-tRAS)	No changeable options	Displays the Memory (RAM) timings and latency. • CAS Latency (tCL) - This is the most

BIOS Setting	Options	Description/Purpose
		<p>important memory timing. CAS stands for Column Address Strobe. If a row has already been selected, it tells us how many clock cycles we'll have to wait for a result (after sending a column address to the RAM controller).</p> <ul style="list-style-type: none"> • Row Address (RAS) to Column Address (CAS) Delay (tRCD) - Once we send the memory controller a row address, we'll have to wait this many cycles before accessing one of the row's columns. So, if a row hasn't been selected, this means we'll have to wait tRCD + tCL cycles to get our result from the RAM. • Row Precharge Time (tRP) - If we already have a row selected, we'll have to wait this number of cycles before selecting a different row. This means it will take tRP + tRCD + tCL cycles to access the data in a different row. • Row Active Time (tRAS) - This is the minimum number of cycles that a row has to be active for to ensure we'll have enough time to access the information that's in it. This usually needs to be greater than or equal to the sum of the previous three latencies (tRAS = tCL + tRCD + tRP).
SO-DIMM#1	No changeable options	Displays the status of SO-DIMM#1.
Size	No changeable options	Displays the size of SO-DIMM#1.
SO-DIMM#2	No changeable options	Displays the status of SO-DIMM#2.
Size	No changeable options	Displays the size of SO-DIMM#2.

4-5-1-3. Chipset – Graphics Configuration

Menu Path *Chipset > Graphics Configuration*

The **Graphics Configuration** allows users to adjust the LVDS backlight brightness for the LCD panel.



Graphics Configuration Screen

BIOS Setting	Options	Description/Purpose
IGFX VBIOS Version	No changeable options	Displays the IGFX VBIOS Version.
LCD Control	Sub-menu	LCD Control sub-menu.
LVDS Backlight Control	Numeric (from 10 to 100)	Allows users to adjust the backlight of the LCD panel brightness ranging from 10 to 100 in scale.

Menu Path *Chipset > Graphics Configuration > LCD Control*

The **LCD Control** allows users to select the primary and secondary display device.



LCD Control Screen

BIOS Setting	Options	Description/Purpose
Primary IGFX Boot Display	- VBIOS default - VGA - LVDS	Selects Primary Display device.
Secondary IGFX Boot Display	- Disabled - VGA - LVDS	Selects Secondary Display device.

4-5-2. Chipset – PCH-IO Configuration

Menu Path *Chipset > PCH-IO Configuration*

The **PCH-IO Configuration** allows users to set PCI Express configuration parameters, enable/disable PCH LAN Controller and Wake-On-LAN function and determine the power on/off state that the system will go to following a power failure (G3 state).



PCH-IO Configuration Screen

BIOS Setting	Options	Description/Purpose
Intel PCH RC Version	No changeable options	Displays the Intel PCH RC Version.
Intel PCH SKU Name	No changeable options	Displays the Intel PCH SKU Name.
Intel PCH Rev ID	No changeable options	Displays the Intel PCH Revision ID.
PCI Express Configuration	Sub-menu	PCI Express Configuration settings.

BIOS Setting	Options	Description/Purpose
PCH LAN Controller	- Disabled - Enabled	Enables or Disables onboard NIC.
Wake On LAN	- Disabled - Enabled	Enables or Disables integrated LAN to wake up the system.
State After G3	- Power On - Power Off	Specifies the Power On/Off state that the system will go to when the power is re-applied following a power failure (G3 state).

4-5-2-1. Chipset – PCI Express Configuration

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration*

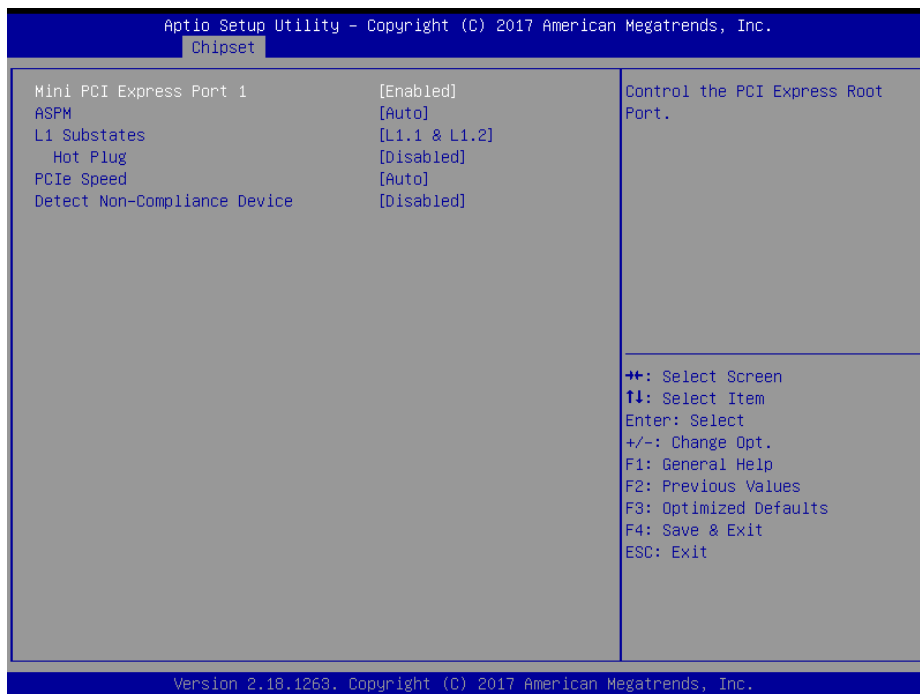
The **PCI Express Configuration** allows users to configure PCI Express slots, enable/disable the Mini PCI Express Ports 1-2, and set their bus speeds.



PCI Express Configuration Screen

BIOS Setting	Options	Description/Purpose
PCIe-USB Glitch W/A	- Disabled - Enabled	Provides the workaround to increase compatibility on bad USB device(s) connected behind PCIE/PEG Port.
PCIE Port assigned to LAN	No changeable options	Displays which PCIE Port assigned to LAN.
Mini PCI Express Port 1	Sub-menu	Mini PCI Express Port 1 settings.
Mini PCI Express Port 2	Sub-menu	Mini PCI Express Port 2 settings.

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration > Mini PCI Express Port 1*

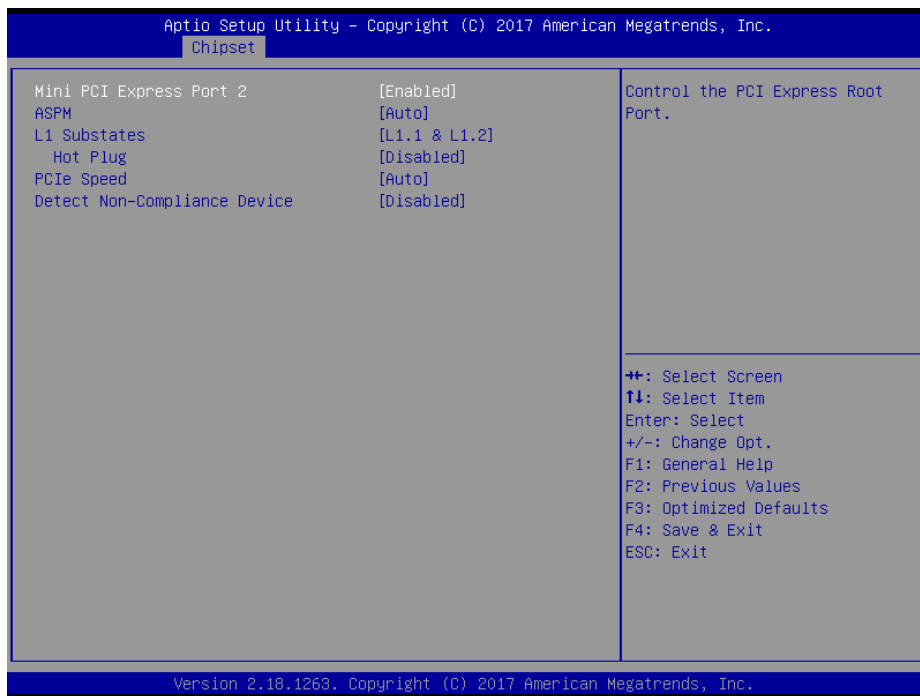


Mini PCI Express Port 1 Configuration Screen

BIOS Setting	Options	Description/Purpose
Mini PCI Express Port 1	- Disabled - Enabled	Controls the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1 - Auto	Sets the ASPM (Active-State Power Management) Level. The option allows users to set lower power mode that activates when the bus is not being used.
L1 Substates	- Disabled - L1.1 - L1.2 - L1.1 & L1.2	Selects PCI Express L1 Substates settings.
Hot Plug	- Disabled - Enabled	Enables or Disables Hot Plug function to designate PCI Express port 1 device as hot-pluggable.

BIOS Setting	Options	Description/Purpose
PCIe Speed	- Auto - Gen1 - Gen2 - Gen3	Selects the speed of PCI Express Port 1.
Detect Non-Compliance Device	- Disabled - Enabled	Detects a Non-Compliance PCI Express device that is connected to the PCI Express port. If enabled, it will take more time during POST.

Menu Path *Chipset > PCH-IO Configuration > PCI Express Configuration > Mini PCI Express Port 2*



Mini PCI Express Port 2 Configuration Screen

BIOS Setting	Options	Description/Purpose
Mini PCI Express Port 2	- Disabled - Enabled	Controls the PCI Express Root Port.
ASPM	- Disabled - L0s - L1 - L0sL1 - Auto	Controls PCIe Active State Power Management settings. The option allows users to set lower power mode that activates when the bus is not being used.
L1 Substates	- Disabled - L1.1 - L1.2 - L1.1 & L1.2	PCI Express L1 Substates settings.
Hot Plug	- Disabled - Enabled	Enables or Disables Hot Plug function to designate PCI Express port 2 device as hot-pluggable.
PCIe Speed	- Auto - Gen1 - Gen2 - Gen3	Selects the speed for PCI Express Port 2.
Detect Non-Compliance Device	- Disabled - Enabled	Detects a Non-Compliance PCI Express device that is connected to the PCI Express port. If enabled, it will take more time during POST.

4-6. SECURITY

Menu Path	Security
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From the **Security** menu, you are allowed to configure or change the administrator password. You will be asked to enter the configured administrator password before you can access the Setup Utility.

By setting an administrator password, you will prevent other users from changing your BIOS settings. You can configure an Administrator password and then configure a user password. Heed that a user password does not provide access to most of the features in the Setup utility.



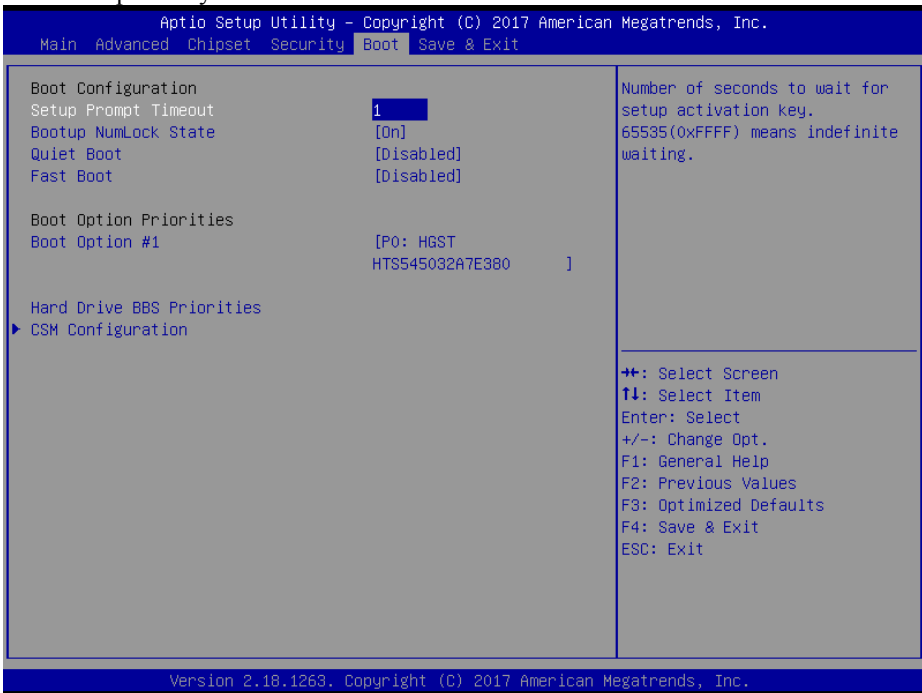
Security Screen

BIOS Setting	Options	Description/Purpose
Administrator Password	Password can be 3-20 alphanumeric characters.	Specifies the administrator password.
User Password	Password can be 3-20 alphanumeric characters.	Specifies the user password.

4-7. BOOT

Menu Path *Boot*

This menu provides control items for system boot configuration such as setting setup prompt timeout, enabling/disabling quiet boot and fast boot, selecting the boot sequence from the available device(s) and BBS option priorities, and setting CSM (Compatibility Support Module) configuration parameters to support legacy BIOS operation systems, various VGA, bootable devices and add-on devices for achieving better compatibility.



Boot Screen

BIOS Setting	Options	Description/Purpose
Setup Prompt Timeout	Numeric (from 1 to 65535)	Number of seconds to wait for setup activation key.
Bootup NumLock State	- On - Off	Specifies the NumLock state after the system is powered on. <ul style="list-style-type: none"> • On: Enables the NumLock function automatically after the system is powered on. • Off: Disables the NumLock function after the system is powered on.
Quiet Boot	- Disabled - Enabled	Enables or Disables Quiet Boot options. When this option is set to "Disabled", BIOS will display normal POST messages.
Fast Boot	- Disabled - Enabled	Enables or Disables Fast Boot option. It allows users to reduce the system startup time and start up the system in a fast manner.
Boot Option #1~#n	- [Drive(s)] - Disabled	Allows users to choose the boot sequence from the available device(s). Note that in the menu displayed, you will only see the device with the highest priority for a specific boot device type.
Hard Drive BBS Priorities	Sub-Menu	Defines the boot order for all the hard drives connected to the system, e.g. SATA, USB drive.
CSM Configuration	Sub-Menu	CSM configuration: Enable/Disable, Option ROM execution settings, etc.

4-7-1. Boot – Hard Drive BBS Priorities

Menu Path *Boot > Hard Drive BBS Priorities*

Select **Hard Drive BBS Priorities** from the **Boot** menu to configure the boot sequence and priority of the available drives.



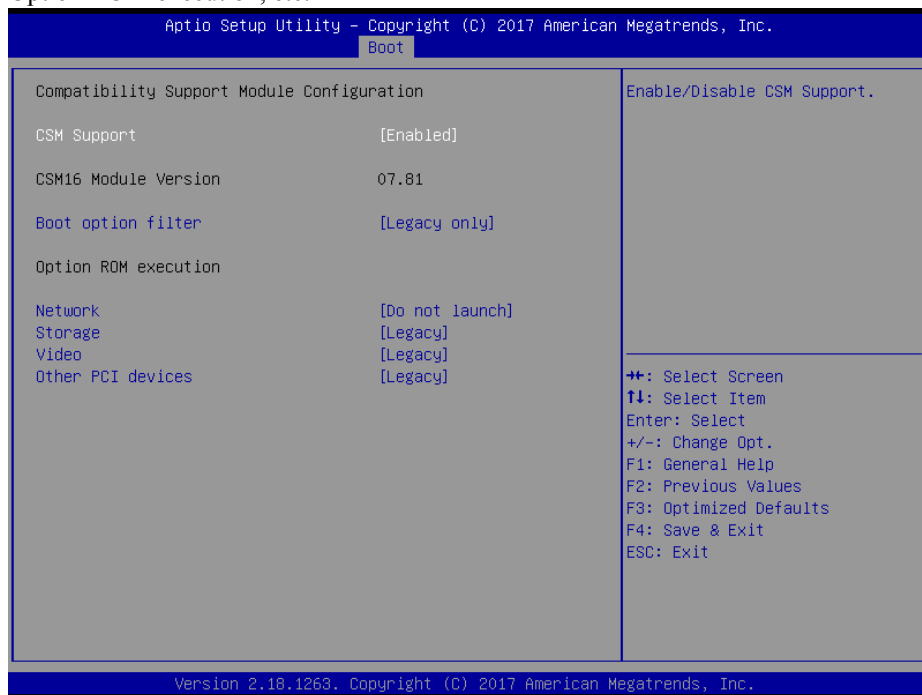
Hard Drive BBS Priorities Screen

BIOS Setting	Options	Description/Purpose
Boot Option #1~#n	- [Drive(s)] - Enabled	Allows users to set the priority of all the drives connected to the system or another bootable USB storage. Press Enter to enter the sub-menu and press <↑> or <↓> arrow keys to select the device. Another way is to press <+> or <-> to move the selected device up/down in the priority list.

4-7-2. Boot – CSM Configuration

Menu Path *Boot > CSM Configuration*

The **CSM Configuration** provides advanced CSM (Compatibility Support Module) configurations such as Enable/Disable CSM Support, Boot option filter, configure Option ROM execution, etc.



CSM Configuration Screen

BIOS Setting	Options	Description/Purpose
CSM Support	- Disabled - Enabled	Enables or Disables CSM Support.
CSM16 Module	No changeable options	Displays the CSM 16 Module version.

BIOS Setting	Options	Description/Purpose
Boot option filter	- UEFI and Legacy - Legacy only - UEFI only	This option controls Legacy/UEFI ROMs priority.
Network	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy PXE Option ROM.
Storage	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Storage Option ROM.
Video	- Do not launch - UEFI - Legacy	Controls the execution of UEFI and Legacy Video Option ROM.
Other PCI devices	- Do not launch - UEFI - Legacy	Determines Option ROM execution policy for devices other than Network, Storage or Video.

4-8. SAVE & EXIT

Menu Path	<i>Save & Exit</i>
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Save Changed BIOS Settings

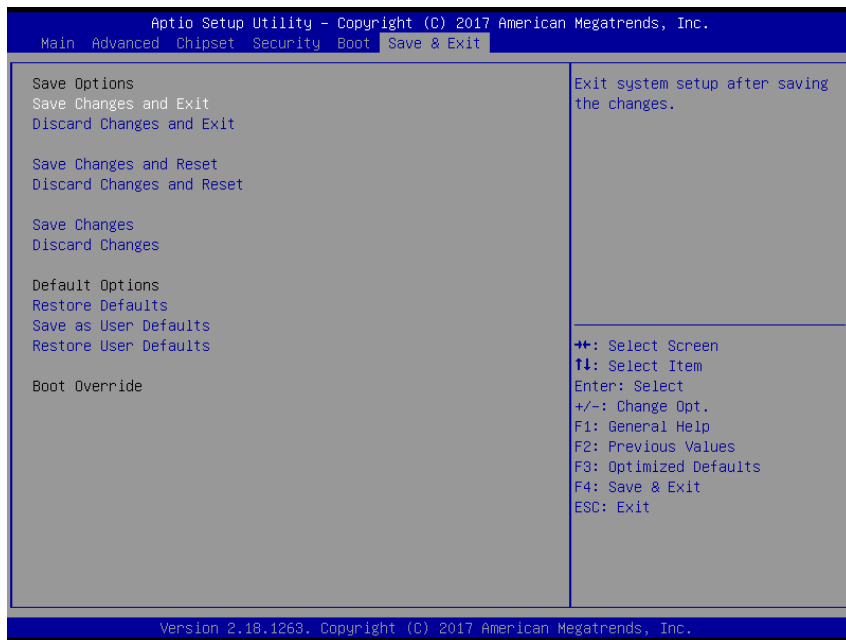
To save and validate the changed BIOS settings, select **Save Changes** from the **Save & Exit** menu, or you can select **Save Changes and Exit** to validate the changes and then exit the system. You can also simply press **F4** at any time to save the BIOS changes.

Discard Changed BIOS Settings

To cancel the BIOS settings you have previously configured, select **Discard Changes and Exit** from this menu, or simply press **Esc** to exit the BIOS setup. You can also select **Discard Changes and Reset** to cancel the changed settings and restore the factory BIOS defaults.

Load User Defaults

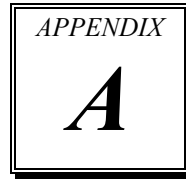
You may simply press **F3** at any time to load the **Optimized Values** which resets all BIOS settings to the factory defaults.



Save & Exit Screen

BIOS Setting	Options	Description/Purpose
Save Changes and Exit	No changeable options	Exits the system and saves the changes in NVRAM.
Discard Changes and Exit	No changeable options	Exits the system without saving any changes configured in BIOS settings.
Save Changes and Reset	No changeable options	Saves the changes in NVRAM and resets the system.
Discard Changes and Reset	No changeable options	Resets the system without saving any changes configured in BIOS settings.
Save Changes	No changeable options	Saves the changes done so far to any of the setup options.
Discard Changes	No changeable options	Discards the changes done so far to any of the BIOS settings.
Restore Defaults	No changeable options	Loads the optimized defaults for BIOS settings.
Save as User Defaults	No changeable options	Saves the changes done so far as User Defaults.
Restore User Defaults	No changeable options	Restores the User Defaults to all the BIOS settings.
Boot Override	- [Drive(s)]	Forces to boot the system from selected [drive(s)].

SYSTEM ASSEMBLY



This appendix contains exploded diagrams and part numbers of the 5-wire analog resistive touch panel and projected capacitive touch panel for PA-2019 system.

The following sections are included:

- Easy Maintenance
 - Hard Drive
 - Back Cover
 - CPU and Memory

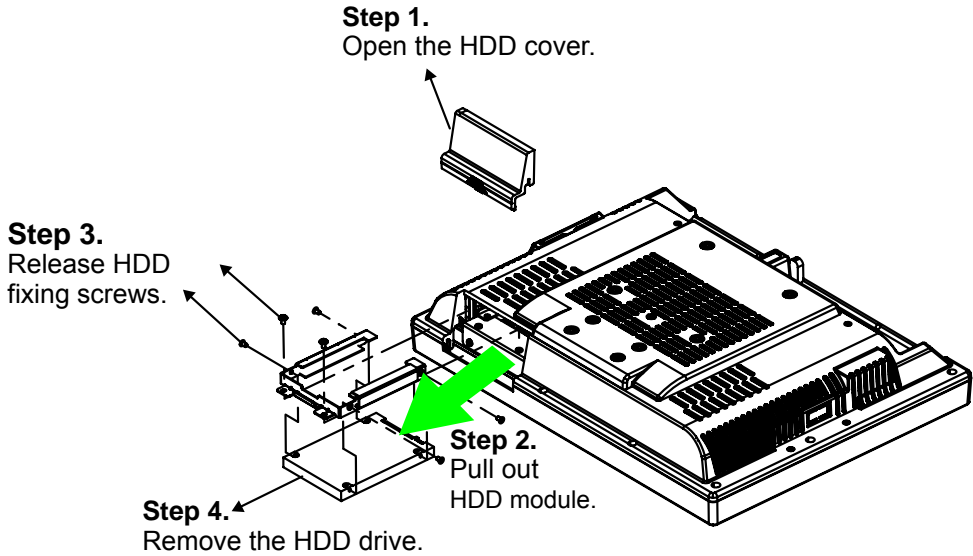
- Exploded Diagrams for Panel PC
 - **Exploded Diagrams for 5-wire Analog Resistive Touch Panel:**
 - Exploded Diagram for PA-2019 POS Open & Close
 - Exploded Diagram for PA-2019 120W Power Adapter
 - Exploded Diagram for PA-2019 PPC Open & Close
 - Exploded Diagram for PA-2019 Mainboard
 - Exploded Diagram for PA-2019 LCD Holder
 - Exploded Diagram for PA-2019 LCD Assembly
 - Exploded Diagram for PA-2019 Touch Panel Assembly
 - Exploded Diagram for PA-2019 HDD Module
 - Exploded Diagram for PA-2019 Metal Back Cover
 - Exploded Diagram for PA-2019 2.5" SATA HDD Module

Exploded Diagrams for Projected Capacitive Touch Panel:

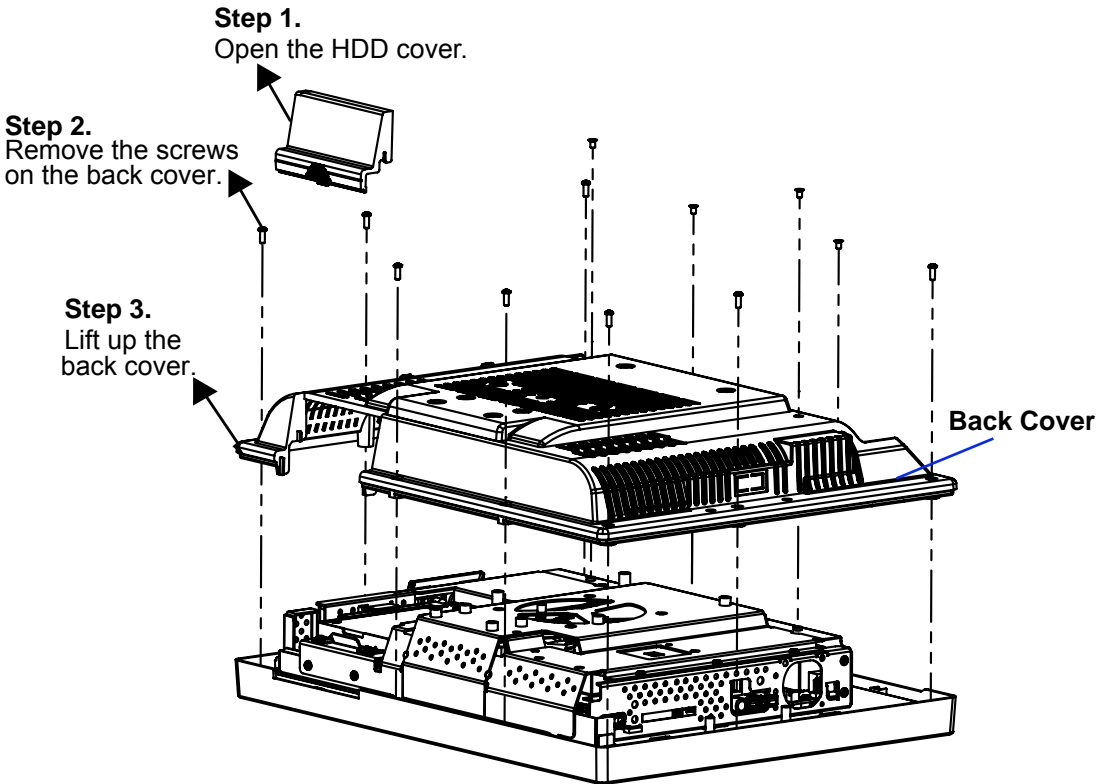
- Exploded Diagram for PA-2019 POS Open & Close
 - Exploded Diagram for PA-2019 POS Stand
 - Exploded Diagram for PA-2019 120W Power Adapter
 - Exploded Diagram for PA-2019 PPC Open & Close
 - Exploded Diagram for PA-2019 Mainboard
 - Exploded Diagram for PA-2019 LCD Holder
 - Exploded Diagram for PA-2019 LCD Assembly
 - Exploded Diagram for PA-2019 Touch Panel Assembly
 - Exploded Diagram for PA-2019 HDD Module
 - Exploded Diagram for PA-2019 Metal Back Cover
 - Exploded Diagram for PA-2019 2.5" SATA HDD Module
-
- Exploded Diagram for PA-2019 MSR Module
 - Exploded Diagram for PA-2019 2nd Display
 - Exploded Diagram for PA-2019 VFD Module

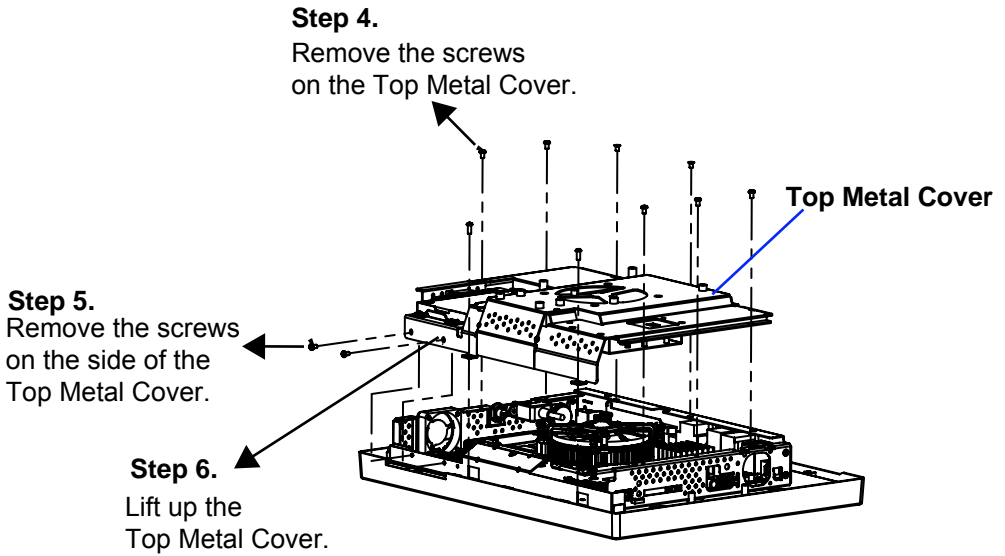
Easy Maintenance

HDD

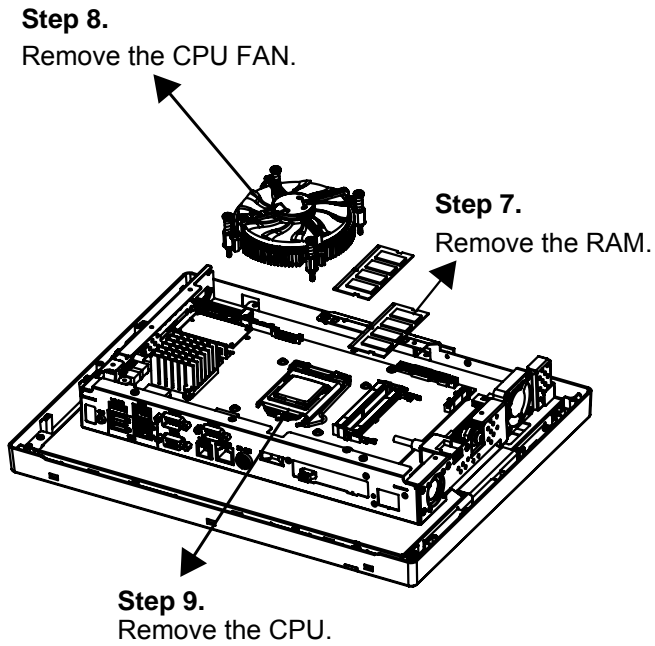


Back Cover



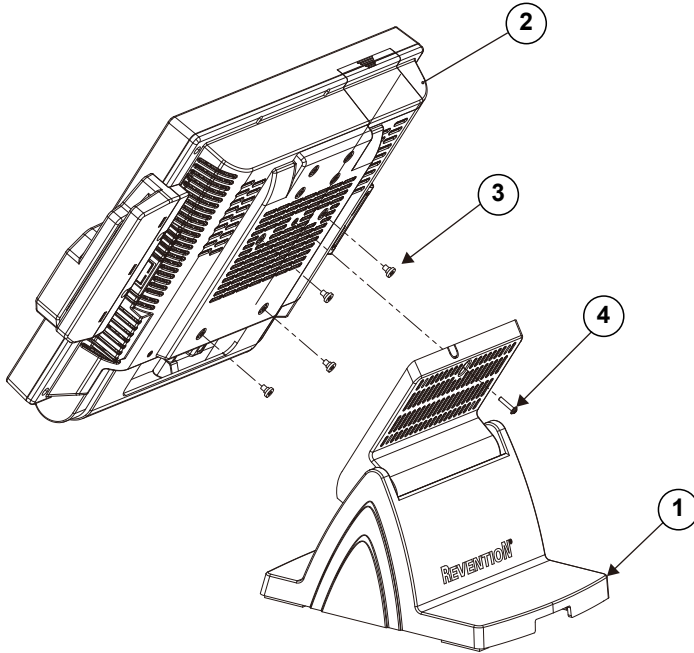


CPU and Memory



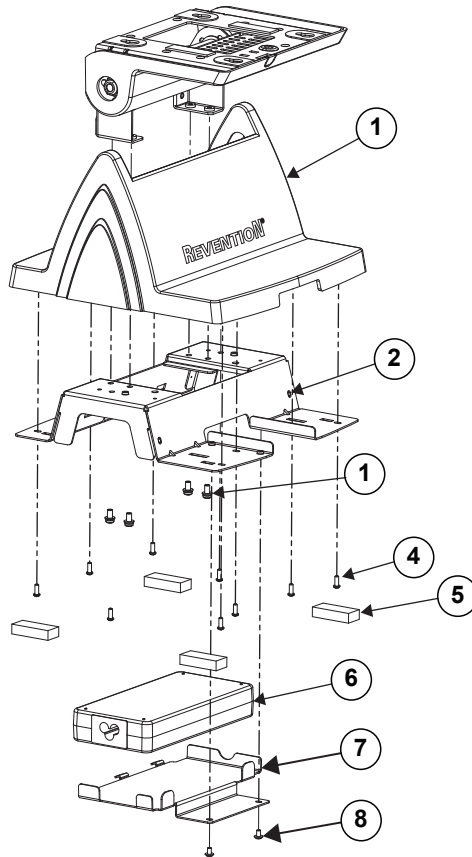
Projected Resistive Touch Screen

EXPLODED DIAGRAM FOR PA-2019 POS OPEN & CLOSE



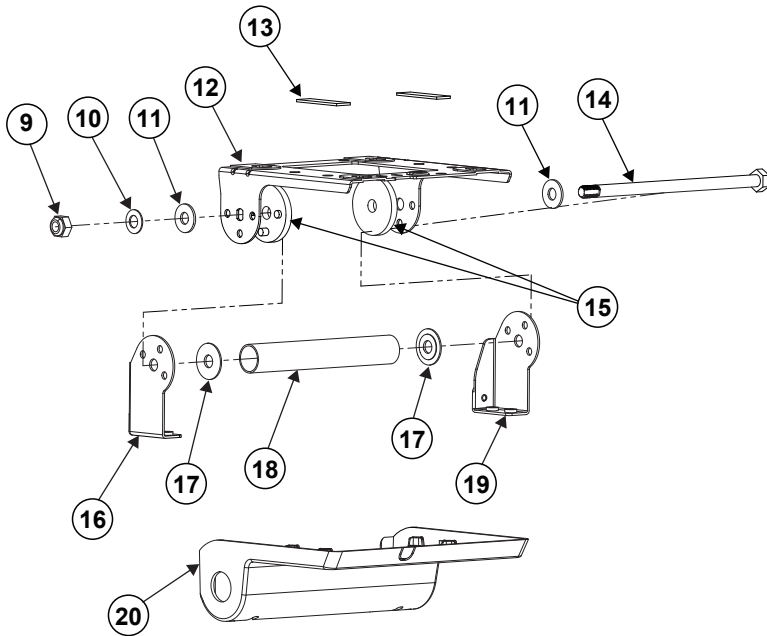
No.	Component Name	P/N No.	Q'ty
1	STAND UNIT	N/A	1
2	PA-2019 PPC	N/A	1
3	M4_H2.2_L4_I_Ni	22-272-40004911	4
4	M3_L5_W_B	22-235-30015011	1

EXPLODED DIAGRAM FOR PA-2019 POS STAND



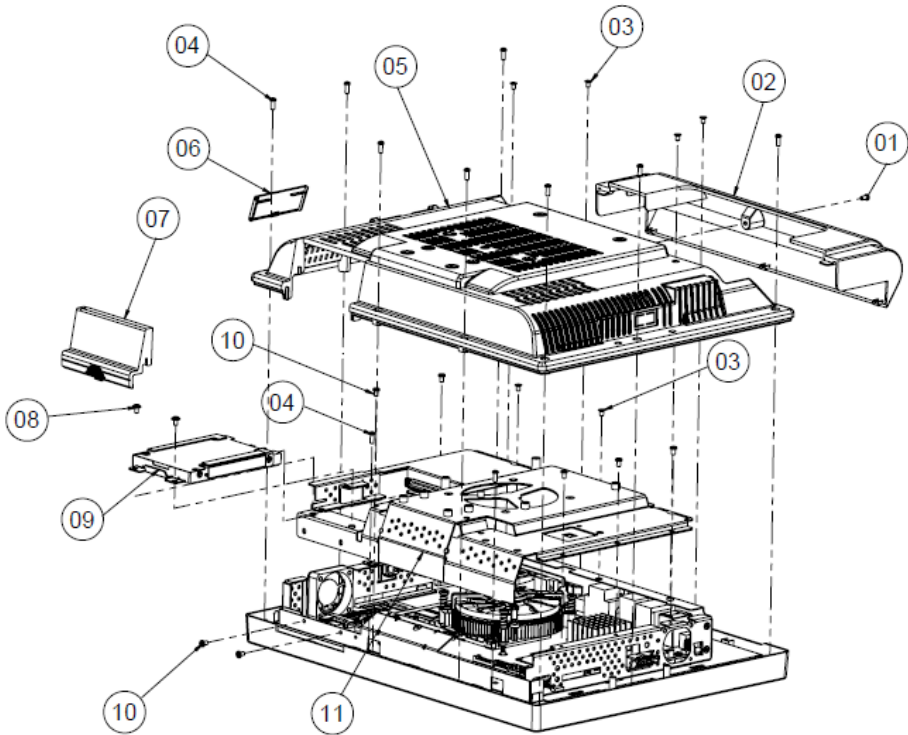
No.	Component Name	P/N No.	Q'ty
1	POS6920-STAND-COVER	30-002-28710226	1
2	PA-6920 STAND BASE 210W	20-032-03001226	1
3	M4_L8_R+S+W_Ni	22-232-40008211	4
4	TS_L8_R_B	22-122-30080011	9
5	Rubber 35x15x8t	30-004-01600000	4
6	120W Power Adapter	52-002-02900101	1
7	6980_120W_LEVEL6 HOLDER	20-029-03001226	1
8	M3_L5_W_Ni	22-242-30005311	2

EXPLODED DIAGRAM FOR PA-2019 120W Power Adapter



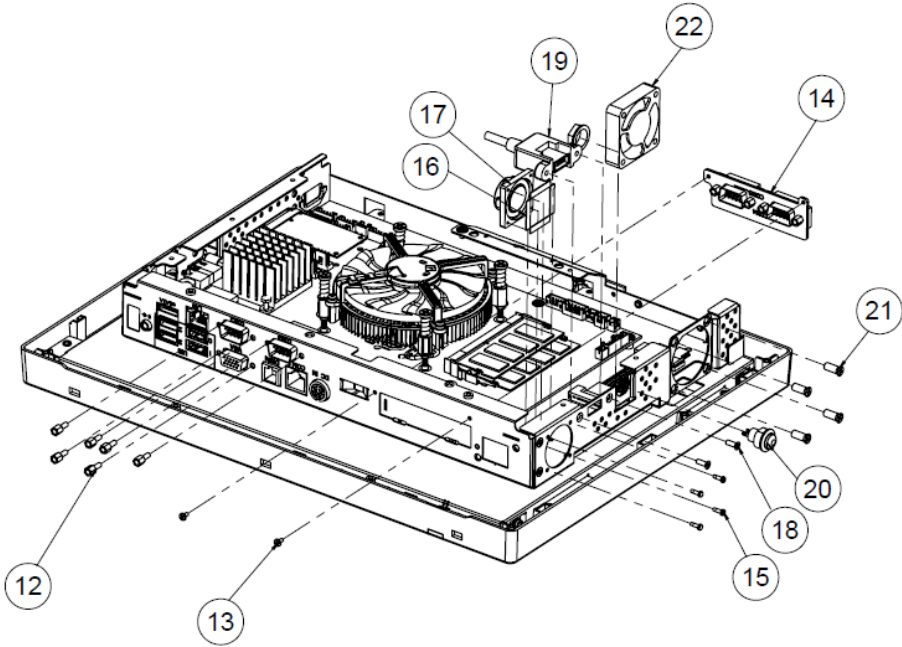
No.	Component Name	P/N No.	Q'ty
9	M8_NUT	23-142-80081291	1
10	Washer_OD15.9_ID8.2_T1.25	23-205-08090168	1
11	Washer_D8_D19_T1.5_Ni	23-202-08150191	2
12	6920-ROTATE_SUPPORT_TYPE2	80-002-03004226	1
13	Rubber_40_12_1.5	90-013-06100226	2
14	M8_H141_L13_Hex_Ni	22-252-80154005	1
15	PS5000-HINGE-SPACER	30-041-04100139	2
16	R_SUPPORT	80-002-03001226	1
17	Washer_id_8_5_od_24_2	23-202-09150247	2
18	POS6920-PIPE	80-056-02001226	1
19	L_SUPPORT	80-002-03002226	1
20	6920-ROTATE_COVER_TYPE2	30-002-28111326	1

EXPLODED DIAGRAM FOR PA-2019 PPC Open and Close



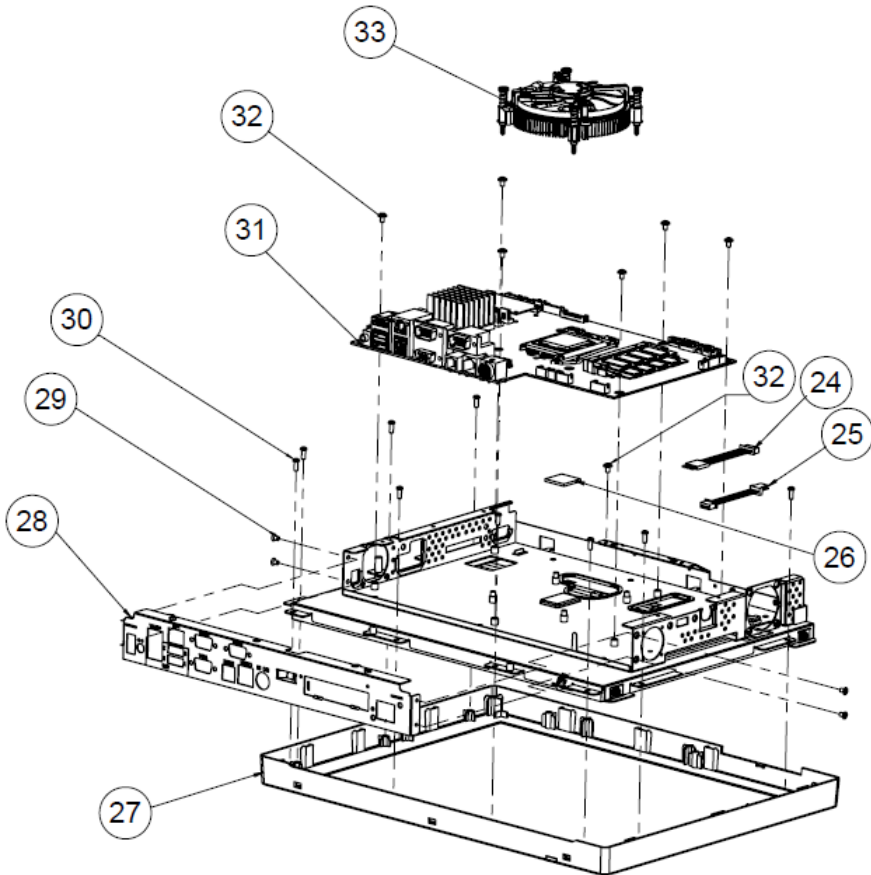
NO.	Name	PART NO.	Q'ty
1	M3_L6_I_B	82-275-30006018	1
2	POS-6920_Cable_cover	30-002-28310226	1
3	M3_L5_F_B	22-215-30005011	4
4	T3_L8_R_B	22-122-30080011	10
5	PA-6970_Back_cover	30-002-28110253	1
6	PA-6970_switch_cover	30-002-28510226	1
7	PA-6970_HDD_cover	30-002-28210253	1
8	M3_L5_W_Ni	22-242-30005311	2
9	PA-6980_HDD_UNIT		1
10	M5_L5_R_Ni	22-230-30005811	7
11	PA-6980_Metal_Back_UNIT		1

EXPLODED DIAGRAM FOR PA-2019 MAIN BOARD



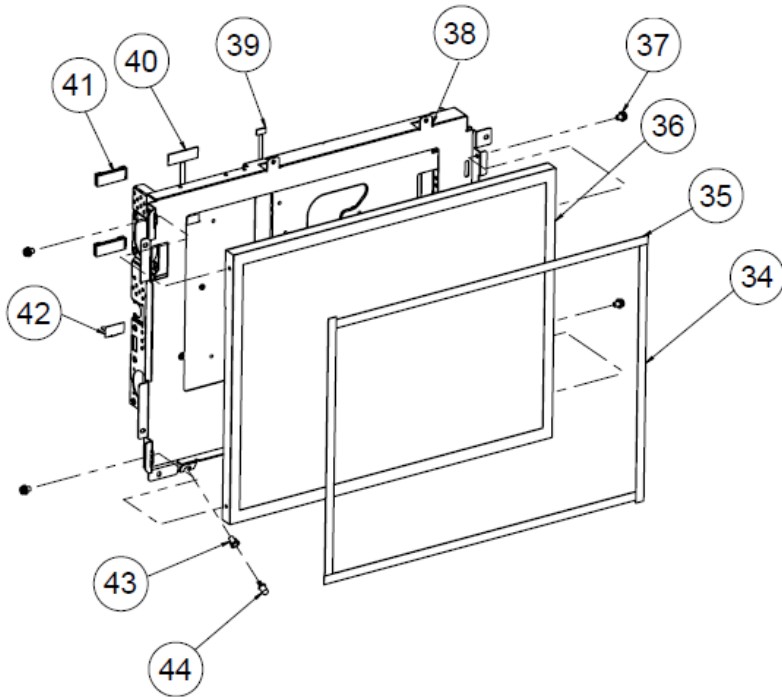
NO.	Name	PART NO.	Q'ty
12	No.4_UNC_L5_H6.8_BOSS	22-692-40048051	6
13	M2_L4_I_Ni	22-272-20004011	2
14	Option I/O	See Order	1
15	T2_L6_R_Ni	22-412-20060011	4
16	PORON_19.5x4x0.5	90-013-15200226	4
17	PA-6970_speaker	13-500-08280418	1
18	No.4_L8_F_B	22-315-40008019	2
19	PA-6980-USB_cable	27-006-37702111	1
20	PA-6980_switch_cable	27-019-24203071	1
21	T4.4_L11_F_Bt_Ni	22-212-46011011	4
22	40x40x10.5_fan	21-004-04040371	1

EXPLODED DIAGRAM FOR PA-2019 LCD HOLDER



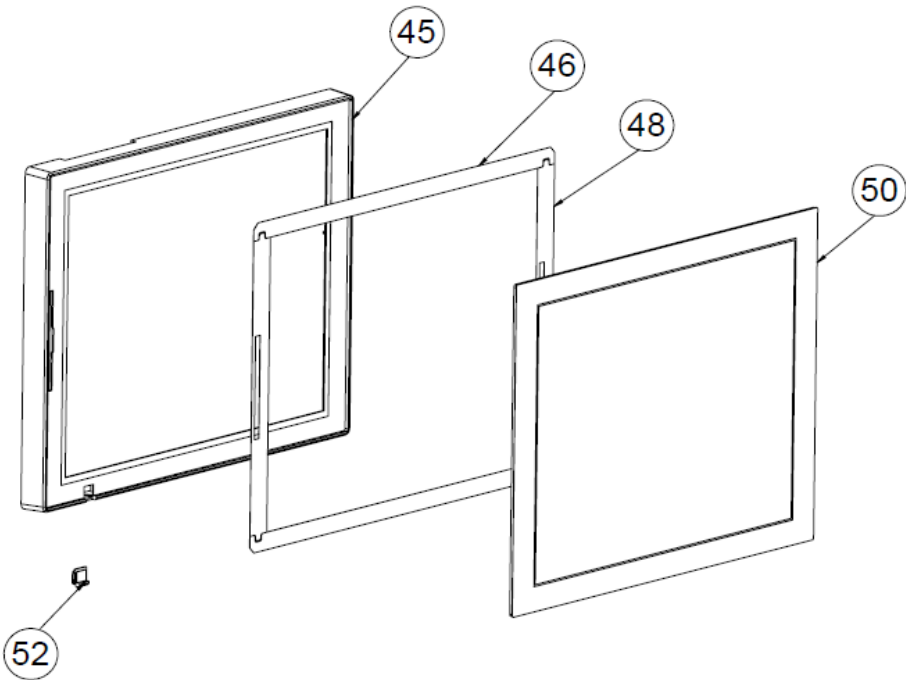
NO.	Name	PART NO.	Q'ty
24	TOUCH_EXTENDED CABLE	27-043-37703071	1
25	FAN_EXTENDED CABLE	27-043-37701071	1
26	thermal_pad	21-006-82020004	1
27	PA-6980_Front_UNIT		1
28	PA-6980_I/O_Plate	20-005-03001377	1
29	M3_L5_F_B	22-215-30005011	4
30	T3_L8_R_B	22-122-30080011	9
31	PB-6980RA		1
32	M3_L5_W_Ni	22-242-30005311	7
33	CPU cooler	21-003-19696001	1

EXPLODED DIAGRAM FOR PA-2019 LCD ASSEMBLY



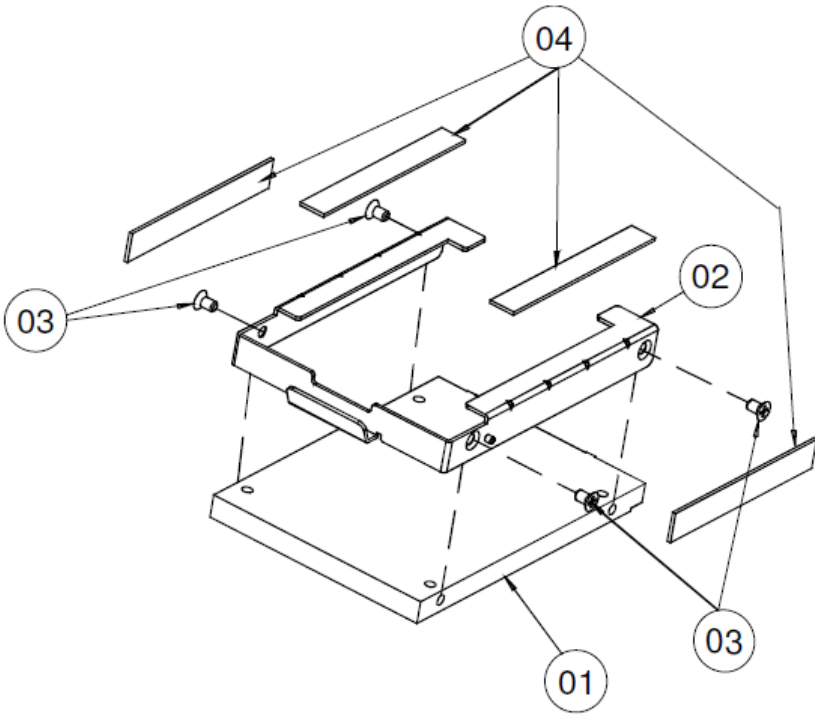
No.	Component Name	P/N No.	Q'ty
34	PORON 314.9x8x0.5(cut→242)	30-013-24100000	2
35	PORON 314.9x8x0.5(cut→316)	30-013-24100000	2
36	15" LCD panel	52-351-03015021	1
37	M3 L6 R+S W Ni	22-232-30060211	4
38	POS6980-LCD holder	20-029-03001377	1
39	panel led cable	27-055-37703071	1
40	PA-6980 lvds cable	27-020-37704111	1
41	Cable clip(fcm-25snqw)	90-042-04200000	2
42	Led holder(CLED-1)	30-014-04100009	1
43	PA-6980 led cable	27-018-24707071	1

EXPLODED DIAGRAM FOR PA-2019 TOUCH PANEL ASSEMBLY



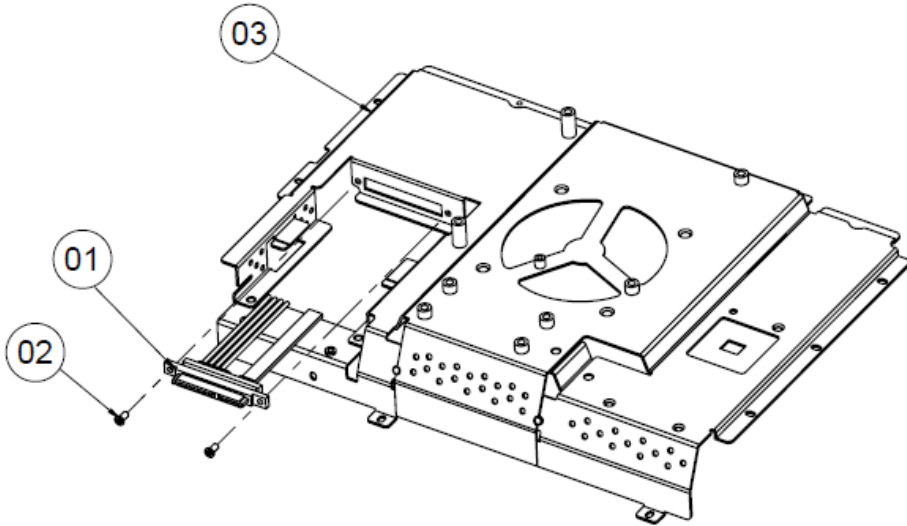
No.	Component Name	P/N No.	Q'ty	Remark
44	PA-6980 lcd front cover	30-002-28110377	1	
45	DOUBLE_COATED_TAPE_A (For ELO)	94-026-04901220	2	For ELO
	DOUBLE_COATED_TAPE_B (For Abon)	94-026-05001220		For Abon
47	DOUBLE_COATED_TAPE_B (ForELO)	94-026-04902220	2	For ELO
	DOUBLE_COATED_TAPE_B (For Abon)	94-026-05002220		For Abon
49	15" Flat resistive touch (ELO)	52-380-00114701	1	ELO Touch
	15" Flat resistive touch (Abon)	52-380-00200114		Abon Touch
51	PA-6920 led lens	30-021-02130269	1	

EXPLODED DIAGRAM FOR PA-2019 HDD MODULE



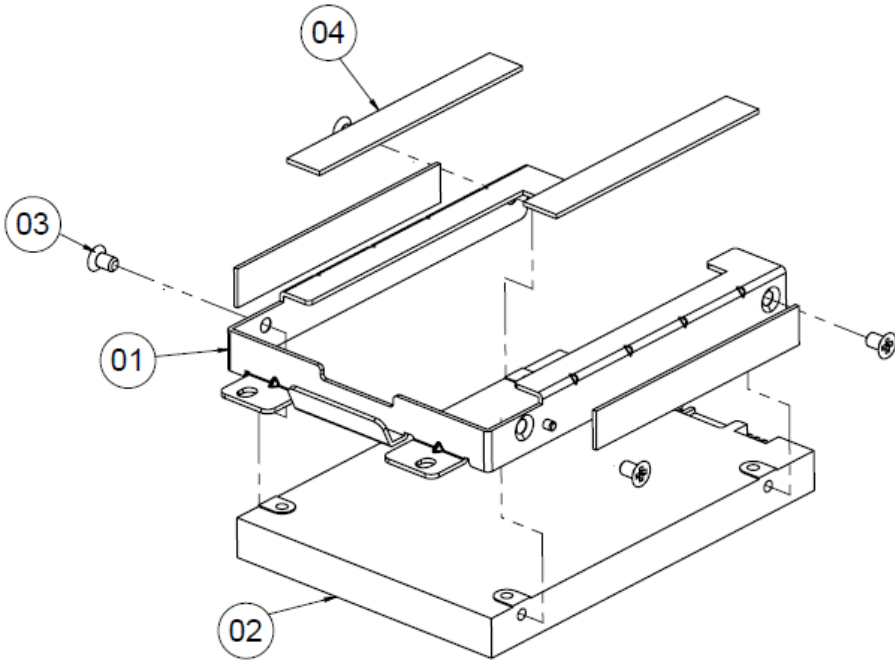
No.	Part Name	Part No.	Qty
1	320GB 2.5" SATA II HDD(option)	52-451-20110105	1
2	PA-6151 HDD_TRAY	80-054-03001226	1
3	F_SCREW,M3.0X0.5PX5mm	22-215-30005011	4
4	HDD CHASSIC EVA (63x9x1mm)	90-013-15100226	4

EXPLODED DIAGRAM FOR PA-2019 METAL BACK COVER



NO.	COMPONENT NAME	PART NO.	Q'ty
1	pa-6980_sata_cable	27-008-32609081	1
2	M3_L6_I_B	82-275-30006018	2
3	PA-6980_Metal_back_cover	20-004-03001377	1

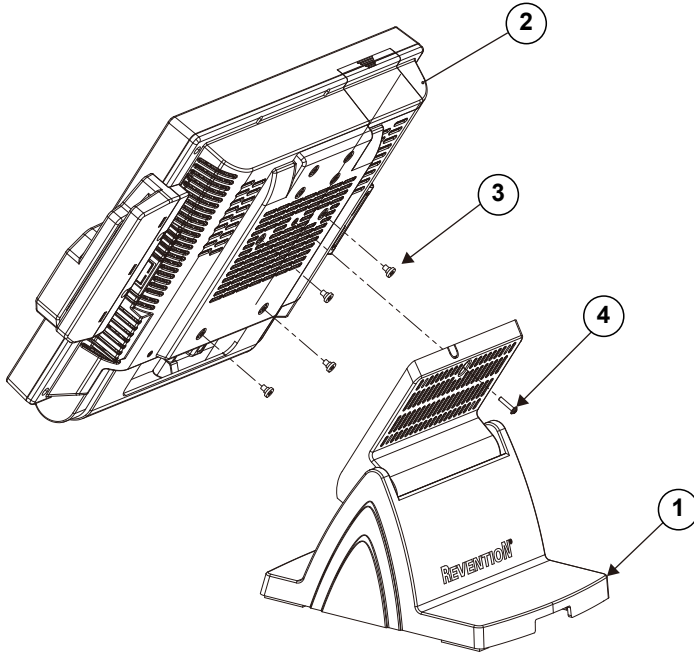
EXPLODED DIAGRAM FOR PA-2019 2.5" SATA HDD



NO.	COMPONENT NAME	PART NO.	Q'ty
1	HDD_track	80-054-03001226	1
2	2.5" Sata HDD	See Order	1
3	M3_L5_F_B	22-215-30005011	4
4	PORON_63X9X1t	90-013-15100226	4

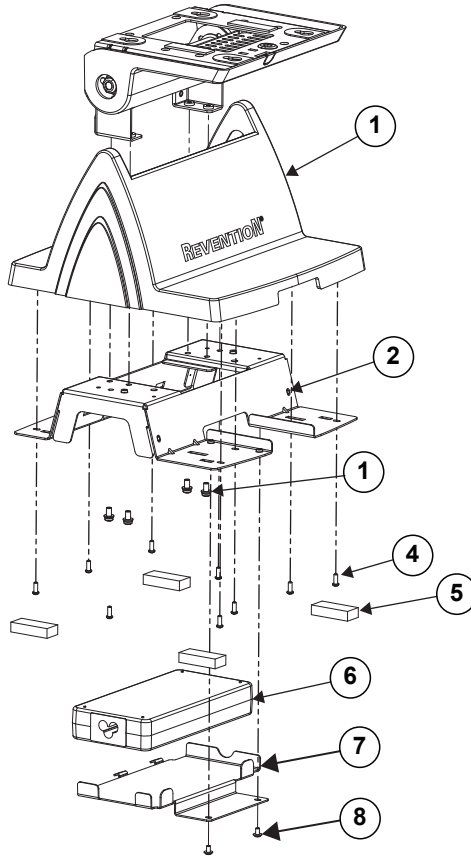
Projected Capacitive Touch Screen

EXPLODED DIAGRAM FOR PA-2019 POS OPEN & CLOSE



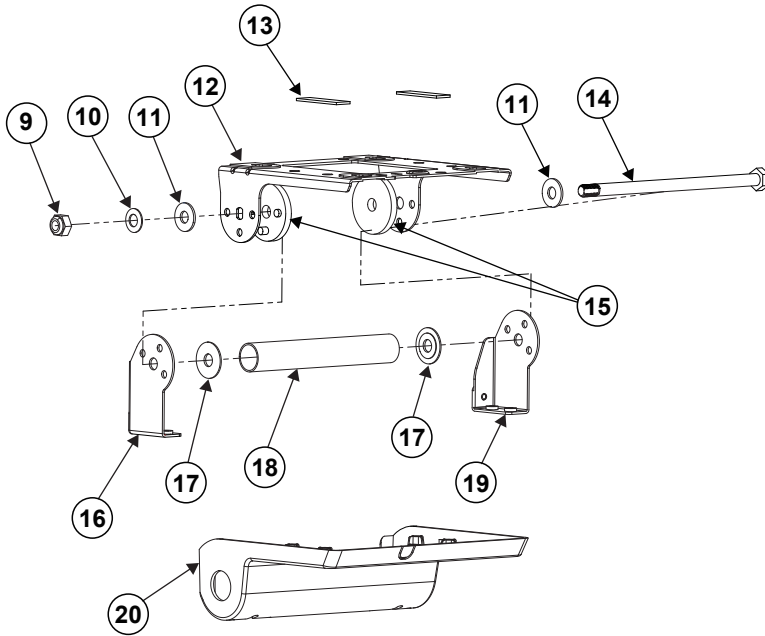
No.	Component Name	P/N No.	Q'ty
1	STAND UNIT	N/A	1
2	PA-2019 PPC	N/A	1
3	M4_H2.2_L4_I_Ni	22-272-40004911	4
4	M3_L5_W_B	22-235-30015011	1

EXPLODED DIAGRAM FOR PA-2019 POS STAND



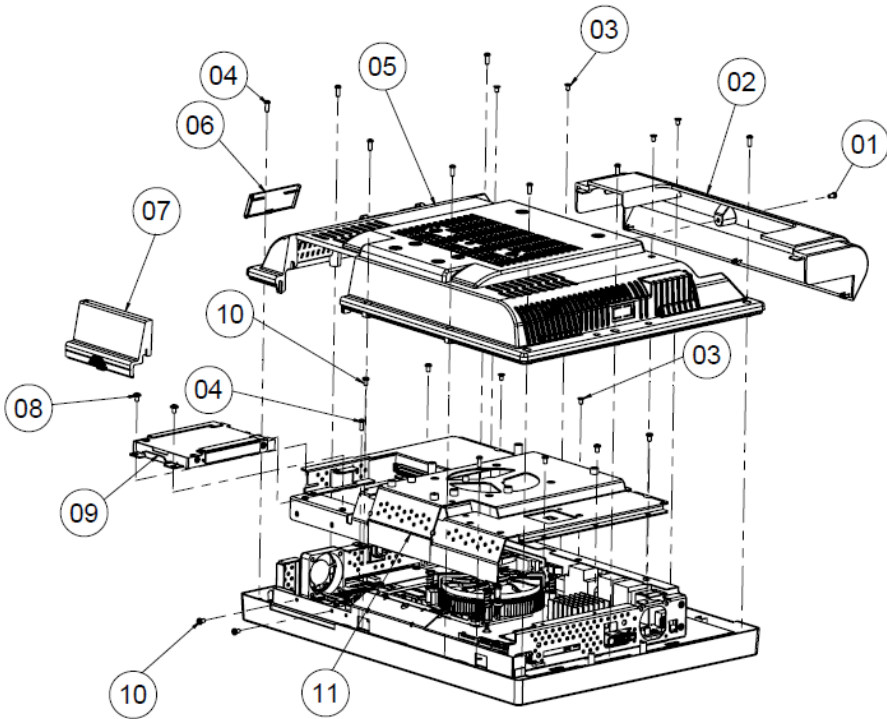
No.	Component Name	P/N No.	Q'ty
1	POS6920-STAND-COVER	30-002-28710226	1
2	PA-6920 STAND BASE 210W	20-032-03001226	1
3	M4_L8_R+S+W_Ni	22-232-40008211	4
4	TS_L8_R_B	22-122-30080011	9
5	Rubber 35x15x8t	30-004-01600000	4
6	120W Power Adapter	52-002-02900101	1
7	6980_120W_LEVEL6 HOLDER	20-029-03001226	1
8	M3_L5_W_Ni	22-242-30005311	2

EXPLODED DIAGRAM FOR PA-2019 120W POWER ADAPTER



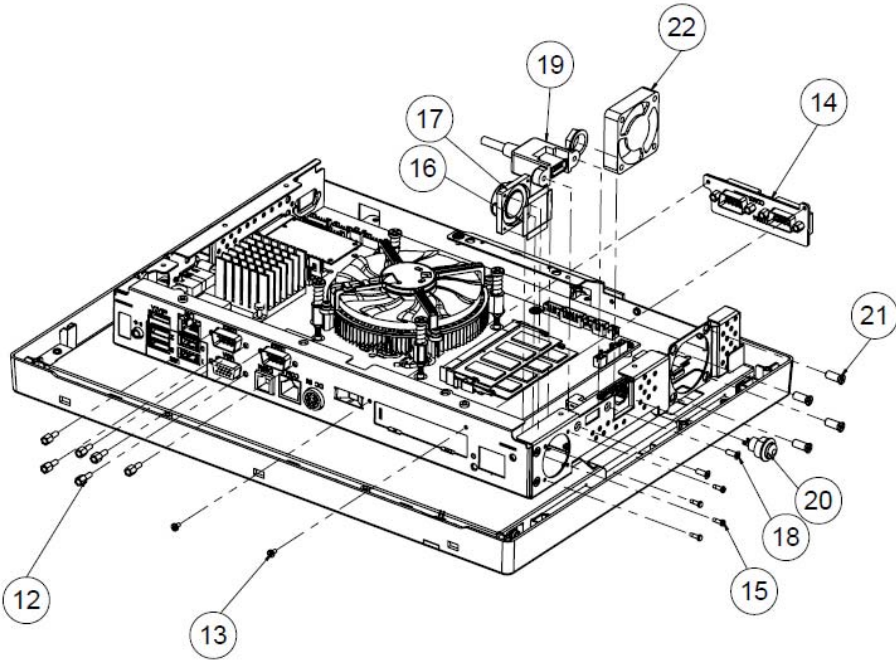
No.	Component Name	P/N No.	Q'ty
9	M8_NUT	23-142-80081291	1
10	Washer_OD15.9_ID8.2_T1.25	23-205-08090168	1
11	Washer_D8_D19_T1.5_Ni	23-202-08150191	2
12	6920-ROTATE_SUPPORT_TYPE2	80-002-03004226	1
13	Rubber_40_12_1.5	90-013-06100226	2
14	M8_H141_L13_Hex_Ni	22-252-80154005	1
15	PS5000-HINGE-SPACER	30-041-04100139	2
16	R_SUPPORT	80-002-03001226	1
17	Washer_id_8_5_od_24_2	23-202-09150247	2
18	POS6920-PIPE	80-056-02001226	1
19	L_SUPPORT	80-002-03002226	1
20	6920-ROTATE_COVER_TYPE2	30-002-28111326	1

EXPLODED DIAGRAM FOR PA-2019 PPC OPEN & CLOSE



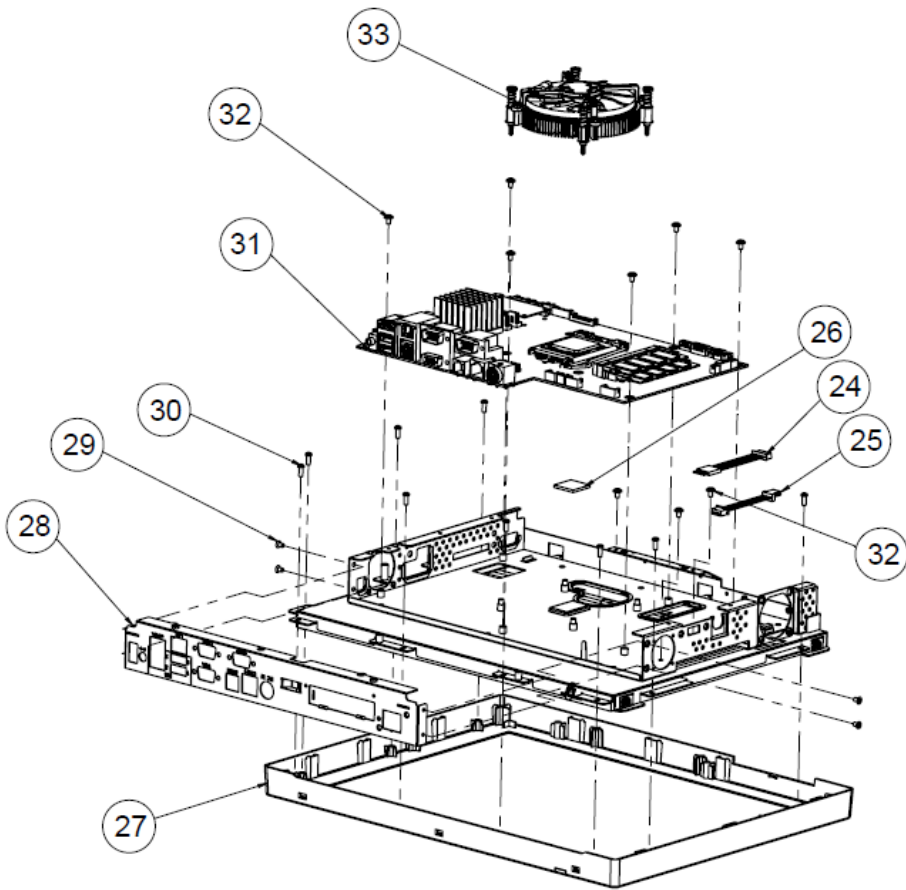
NO.	Name	PART NO.	Q'ty
1	M3_L6_I_B	82-275-30006018	1
2	POS-6920_Cable_cover	30-002-28310226	1
3	M3_L5_F_B	22-215-30005011	4
4	T3_L8_R_B	22-122-30080011	10
5	PA-6970_Back_cover	30-002-28110253	1
6	PA-6970_switch_cover	30-002-28510226	1
7	PA-6970_HDD_cover	30-002-28210253	1
8	M3_L5_W_Ni	22-242-30005311	2
9	PA-6980_HDD_UNIT		1
10	M5_L5_R_Ni	22-230-30005811	7
11	PA-6980_Metal_Back_UNIT		1

EXPLODED DIAGRAM FOR PA-2019 MAINBOARD



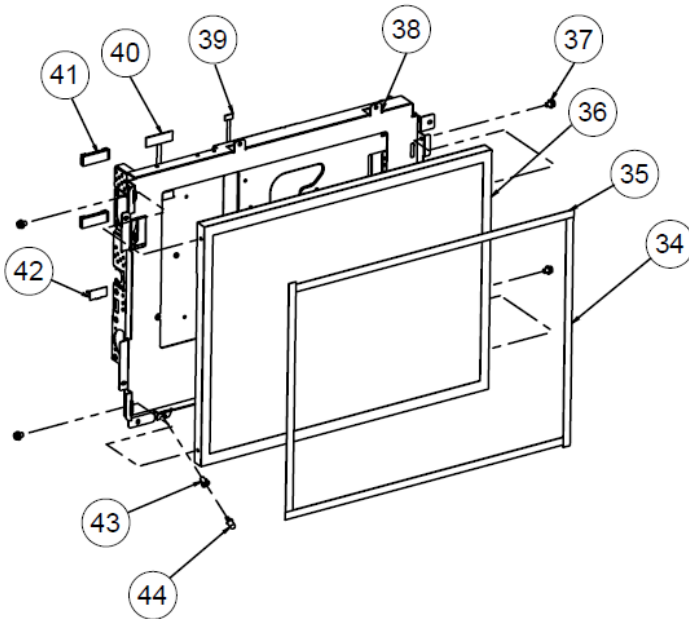
NO.	Name	PART NO.	Q'ty
12	No.4_UNC_L5_H6.8_BOSS	22-692-40048051	6
13	M2_L4_I_Ni	22-272-20004011	2
14	Option I/O	See Order	1
15	T2_L6_R_Ni	22-412-20060011	4
16	PORON_19.5x4x0.5	90-013-15200226	4
17	PA-6970_speaker	13-500-08280418	1
18	No.4_L8_F_B	22-315-40008019	2
19	PA-6980-USB_cable	27-006-37702111	1
20	PA-6980_switch_cable	27-019-24203071	1
21	T4.4_L11_F_Bt_Ni	22-212-46011011	4
22	40x40x10.5_fan	21-004-04040371	1

EXPLODED DIAGRAM FOR PA-2019 LCD HOLDER



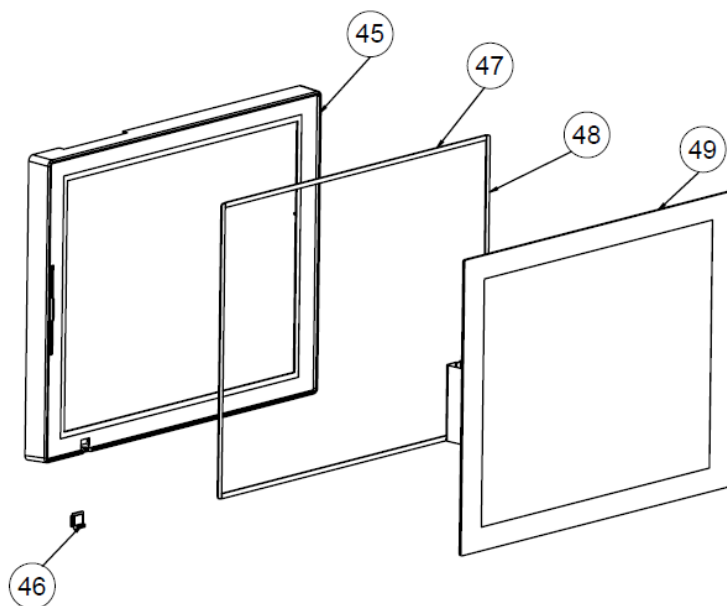
NO.	Name	PART NO.	Q'ty
24	TOUCH_EXTENDED CABLE	27-043-37703071	1
25	FAN_EXTENDED CABLE	27-043-37701071	1
26	thermal_pad	21-006-82020004	1
27	PA-6980_Front_UNIT		1
28	PA-6980_I/O_Plate	20-005-03001377	1
29	M3_L5_F_B	22-215-30005011	4
30	T3_L8_R_B	22-122-30080011	9
31	PB-6980RA		1
32	M3_L5_W_NI	22-242-30005311	9
33	CPU cooler	21-003-19696001	1

EXPLODED DIAGRAM FOR PA-2019 LCD ASSEMBLY



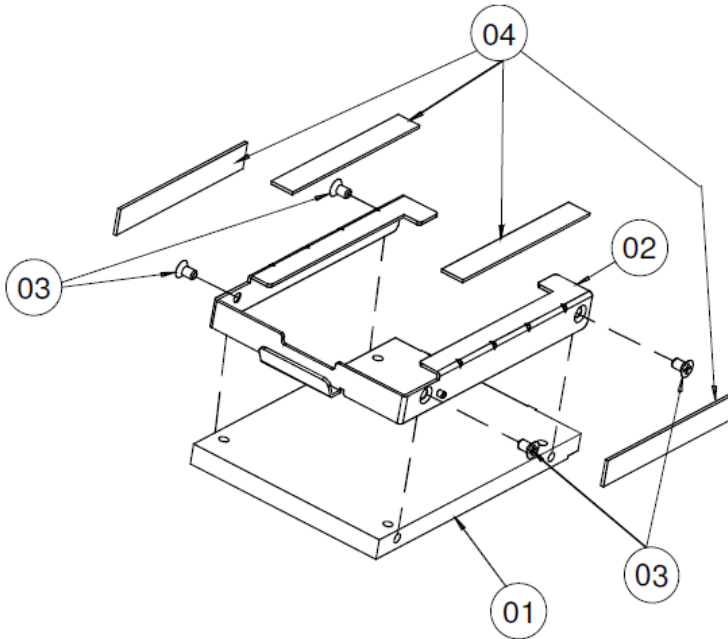
NO.	Name	PART NO.	Q'ty
34	PORON_314.9x8x0.5(cut->242)	30-013-24100000	2
35	PORON_314.9x8x0.5(cut->316)	30-013-24100000	2
36	15" LCD panel	52-351-03015021	1
37	M3_L6_R+S+W_Ni	22-232-30060211	4
38	POS6980-LCD_holder	20-029-03001377	1
39	panel_led_cable	27-055-37703071	1
40	PA-6980_lvds_cable	27-020-37704111	1
41	Cable clip(fcm-25snqw)	90-042-04200000	2
42	Led_holder(CLED-1)	30-014-04100009	1
43	PA-6980_led_cable	27-018-24707071	1

EXPLODED DIAGRAM FOR PA-2019 TOUCH PANEL ASSEMBLY



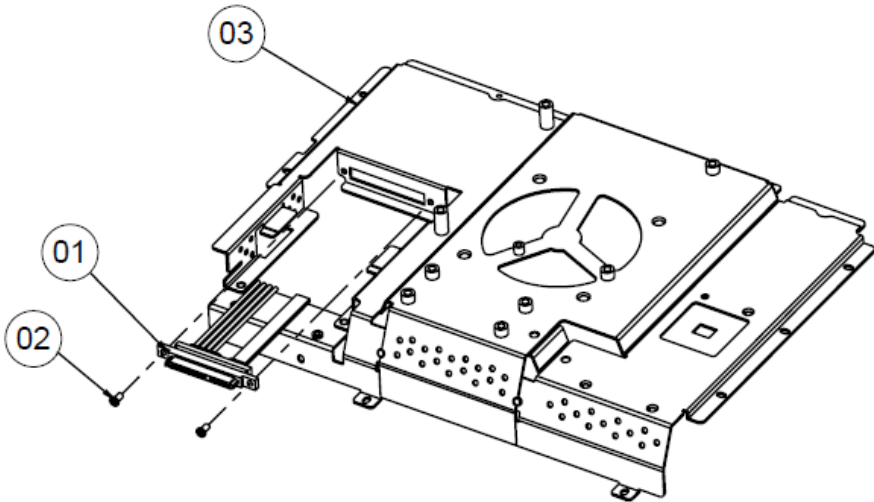
No.	Component Name	P/N No.	Q'ty
45	PA-6980_lcd_front over	30-002-2	1
46	PA-6920_led_len	30-021-02130	1
47	DOUBLE APE_349.3x4.5x1.1t	94-02	2
48	DOUBLE APE_272.8x4.5x1.1t	94-026	2
49	15" lat_Capacitive_Touch	52-380-00150	1

EXPLODED DIAGRAM FOR PA-2019 HDD MODULE



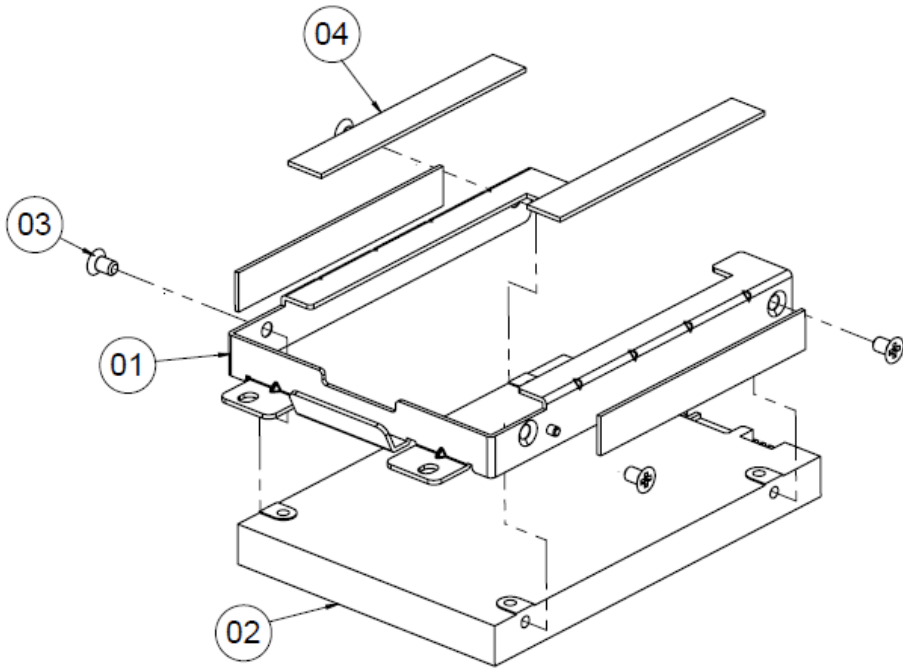
No.	Part Name	Part No.	Qty
1	320GB 2.5" SATA II HDD (option)	52-451-20110105	1
2	PA-6151 HDD_TRAY	80-054-03001226	1
3	F_SCREW,M3.0X0.5PX5mm	22-215-30005011	4
4	HDD CHASSIC EVA (63x9x1mm)	90-013-15100226	4

EXPLODED DIAGRAM FOR PA-2019 METAL BACK COVER



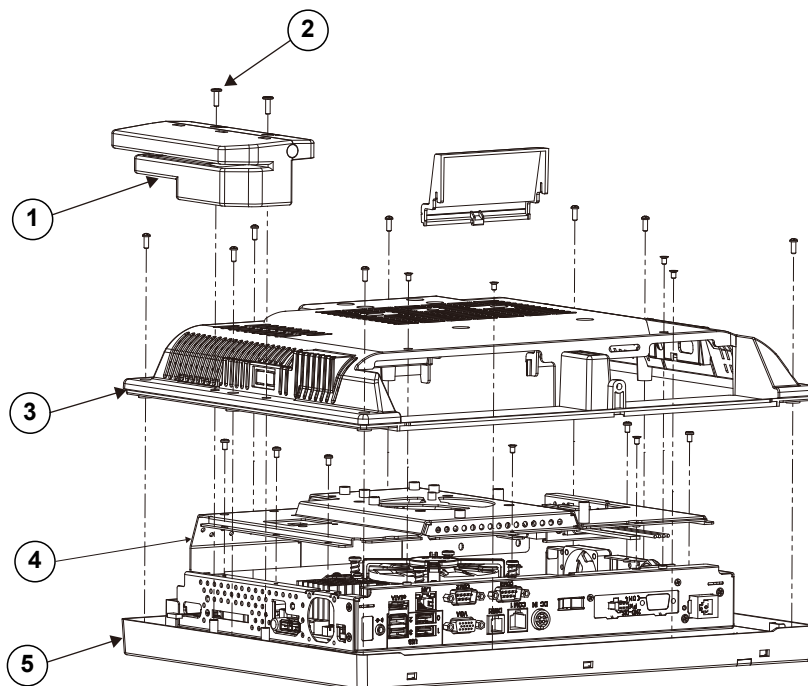
NO.	COMPONENT NAME	PART NO.	Q'ty
1	pa-6980_sata_cable	27-008-32609081	1
2	M3_L6_I_B	82-275-30006018	2
3	PA-6980_Metal_back_cover	20-004-03001377	1

EXPLODED DIAGRAM FOR PA-2019 2.5" SATA HDD



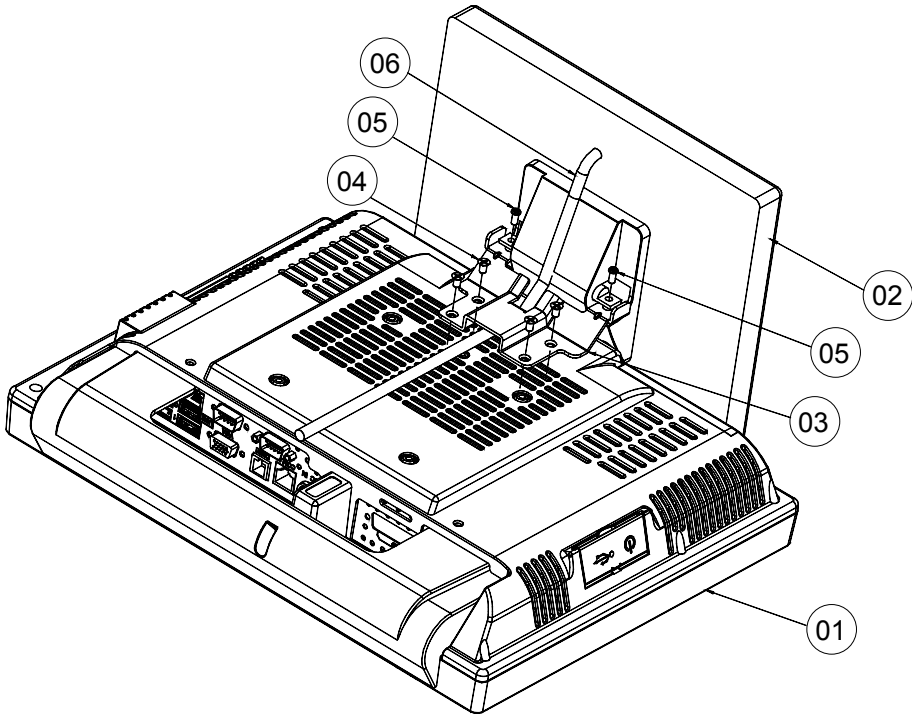
NO.	COMPONENT NAME	PART NO.	Q'ty
1	HDD_track	80-054-03001226	1
2	2.5" Sata HDD	See Order	1
3	M3_L5_F_B	22-215-30005011	4
4	PORON_63X9X1t	90-013-15100226	4

EXPLODED DIAGRAM FOR PA-2019 MSR MODULE



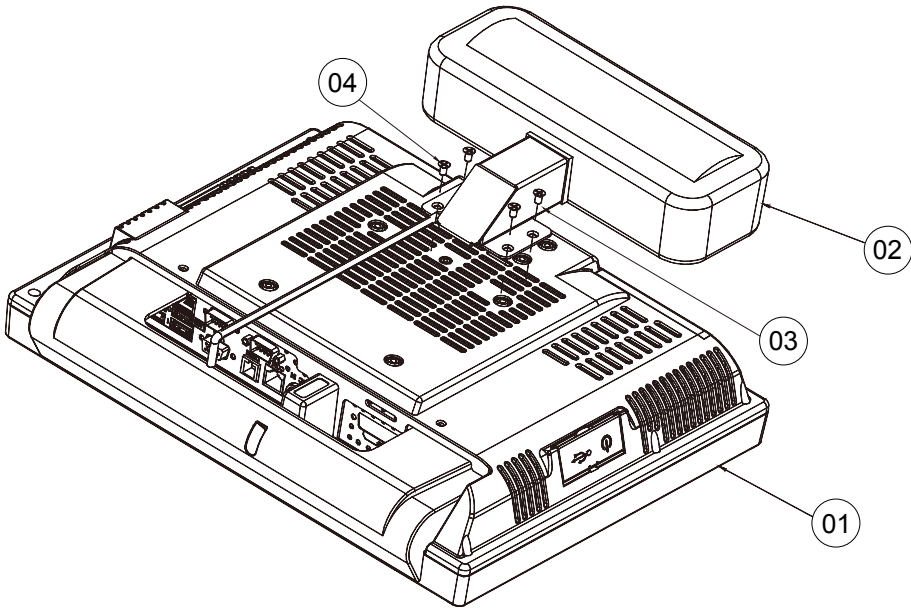
No.	Component Name	P/N No.	Q'ty
1	MSR Module	PA-6980RZ-48A/48B	1
2	M3 L12 I B	22-2753010120	2
3	PA-6970 Back Cover	30-002-28110253	1
4	Top Metal Module Assy	N/A	1
5	Main Module Assy	N/A	1

EXPLODED DIAGRAM FOR PA-2019 2ND DISPLAY



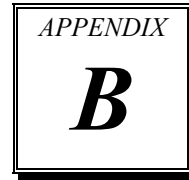
No.	Component Name	P/N No.	Q'ty
1	PA-2019_PPC	N/A	1
2	2ND_DISPLAY (8")	52-380-06080316	1
3	2ND_DIS_SUPPORT_BRACKET	80-006-03061226	1
4	F_SCREW M4 L8mm	22-215-40008711	4
5	R_SCREW M4 L8mm	22-245-40008011	2
6	POWER CABLE	27-012-24710111	1

EXPLODED DIAGRAM FOR PA-2019 VFD MODULE



No.	Component Name	P/N No.	Q'ty
1	PA-2019_PPC	N/A	1
2	VFD_DISPLAY	52-901-24001703	1
3	VFD_SUPPORT_BRACKET	80-006-03062226	1
4	F_SCREW_M4_L8mm	22-215-40008711	4

TECHNICAL SUMMARY

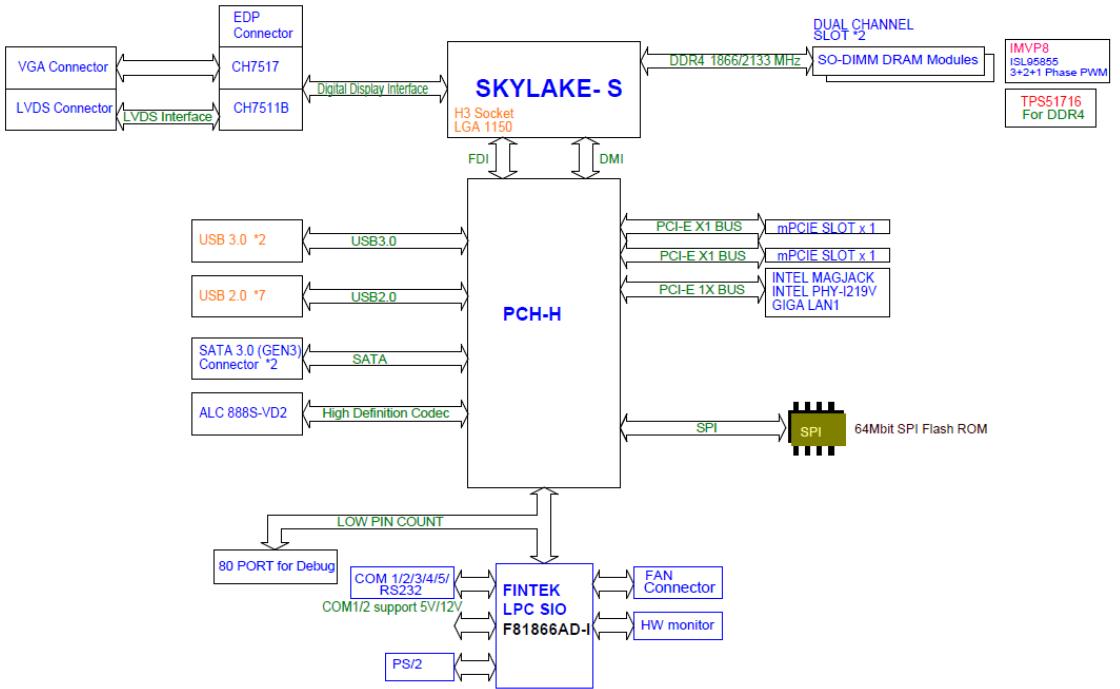


This appendix will give you a brief introduction of the allocation maps for the system resources.

The following sections are included:

- Block Diagram
- Interrupt Map
- I / O Map
- Memory Map
- Watchdog Timer Configuration
- Flash BIOS Update

BLOCK DIAGRAM



INTERRUPT MAP

IRQ	ASSIGNMENT
IRQ 0	System timer
IRQ 1	Standard PS/2 Keyboard
IRQ 3	Communications Port (COM2)
IRQ 4	Communications Port (COM1)
IRQ 6	Communications Port (COM5)
IRQ 7	Communications Port (COM3)
IRQ 8	System CMOS/real time clock
IRQ 10	Communications Port (COM4)
IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
IRQ 11	Intel(R) 100 Series/C230 Series Chipset Family Thermal subsystem - A131
IRQ 13	Numeric data processor
IRQ 14	Motherboard resources
IRQ 16	Standard AHCI 1.0 Serial ATA Controller
IRQ 16	High Definition Audio Controller
IRQ 81	Microsoft ACPI-Compliant System
IRQ 82	Microsoft ACPI-Compliant System
IRQ 83	Microsoft ACPI-Compliant System
IRQ 84	Microsoft ACPI-Compliant System
IRQ 85	Microsoft ACPI-Compliant System
IRQ 86	Microsoft ACPI-Compliant System
IRQ 87	Microsoft ACPI-Compliant System
IRQ 88	Microsoft ACPI-Compliant System
IRQ 89	Microsoft ACPI-Compliant System
IRQ 90	Microsoft ACPI-Compliant System
IRQ 91	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
IRQ 92	Microsoft ACPI-Compliant System
IRQ 93	Microsoft ACPI-Compliant System
IRQ 94	Microsoft ACPI-Compliant System
IRQ 95	Microsoft ACPI-Compliant System
IRQ 96	Microsoft ACPI-Compliant System
IRQ 97	Microsoft ACPI-Compliant System
IRQ 98	Microsoft ACPI-Compliant System
IRQ 99	Microsoft ACPI-Compliant System
IRQ 100	Microsoft ACPI-Compliant System
IRQ 101	Microsoft ACPI-Compliant System
IRQ 102	Microsoft ACPI-Compliant System
IRQ 103	Microsoft ACPI-Compliant System
IRQ 104	Microsoft ACPI-Compliant System
IRQ 105	Microsoft ACPI-Compliant System
IRQ 106	Microsoft ACPI-Compliant System
IRQ 107	Microsoft ACPI-Compliant System
IRQ 108	Microsoft ACPI-Compliant System
IRQ 109	Microsoft ACPI-Compliant System
IRQ 110	Microsoft ACPI-Compliant System
IRQ 111	Microsoft ACPI-Compliant System
IRQ 112	Microsoft ACPI-Compliant System
IRQ 113	Microsoft ACPI-Compliant System
IRQ 114	Microsoft ACPI-Compliant System
IRQ 115	Microsoft ACPI-Compliant System
IRQ 116	Microsoft ACPI-Compliant System
IRQ 117	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
IRQ 118	Microsoft ACPI-Compliant System
IRQ 119	Microsoft ACPI-Compliant System
IRQ 120	Microsoft ACPI-Compliant System
IRQ 121	Microsoft ACPI-Compliant System
IRQ 122	Microsoft ACPI-Compliant System
IRQ 123	Microsoft ACPI-Compliant System
IRQ 124	Microsoft ACPI-Compliant System
IRQ 125	Microsoft ACPI-Compliant System
IRQ 126	Microsoft ACPI-Compliant System
IRQ 127	Microsoft ACPI-Compliant System
IRQ 128	Microsoft ACPI-Compliant System
IRQ 129	Microsoft ACPI-Compliant System
IRQ 130	Microsoft ACPI-Compliant System
IRQ 131	Microsoft ACPI-Compliant System
IRQ 132	Microsoft ACPI-Compliant System
IRQ 133	Microsoft ACPI-Compliant System
IRQ 134	Microsoft ACPI-Compliant System
IRQ 135	Microsoft ACPI-Compliant System
IRQ 136	Microsoft ACPI-Compliant System
IRQ 137	Microsoft ACPI-Compliant System
IRQ 138	Microsoft ACPI-Compliant System
IRQ 139	Microsoft ACPI-Compliant System
IRQ 140	Microsoft ACPI-Compliant System
IRQ 141	Microsoft ACPI-Compliant System
IRQ 142	Microsoft ACPI-Compliant System
IRQ 143	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
IRQ 144	Microsoft ACPI-Compliant System
IRQ 145	Microsoft ACPI-Compliant System
IRQ 146	Microsoft ACPI-Compliant System
IRQ 147	Microsoft ACPI-Compliant System
IRQ 148	Microsoft ACPI-Compliant System
IRQ 149	Microsoft ACPI-Compliant System
IRQ 150	Microsoft ACPI-Compliant System
IRQ 151	Microsoft ACPI-Compliant System
IRQ 152	Microsoft ACPI-Compliant System
IRQ 153	Microsoft ACPI-Compliant System
IRQ 154	Microsoft ACPI-Compliant System
IRQ 155	Microsoft ACPI-Compliant System
IRQ 156	Microsoft ACPI-Compliant System
IRQ 157	Microsoft ACPI-Compliant System
IRQ 158	Microsoft ACPI-Compliant System
IRQ 159	Microsoft ACPI-Compliant System
IRQ 160	Microsoft ACPI-Compliant System
IRQ 161	Microsoft ACPI-Compliant System
IRQ 162	Microsoft ACPI-Compliant System
IRQ 163	Microsoft ACPI-Compliant System
IRQ 164	Microsoft ACPI-Compliant System
IRQ 165	Microsoft ACPI-Compliant System
IRQ 166	Microsoft ACPI-Compliant System
IRQ 167	Microsoft ACPI-Compliant System
IRQ 168	Microsoft ACPI-Compliant System
IRQ 169	Microsoft ACPI-Compliant System

IRQ	ASSIGNMENT
IRQ 170	Microsoft ACPI-Compliant System
IRQ 171	Microsoft ACPI-Compliant System
IRQ 172	Microsoft ACPI-Compliant System
IRQ 173	Microsoft ACPI-Compliant System
IRQ 174	Microsoft ACPI-Compliant System
IRQ 175	Microsoft ACPI-Compliant System
IRQ 176	Microsoft ACPI-Compliant System
IRQ 177	Microsoft ACPI-Compliant System
IRQ 178	Microsoft ACPI-Compliant System
IRQ 179	Microsoft ACPI-Compliant System
IRQ 180	Microsoft ACPI-Compliant System
IRQ 181	Microsoft ACPI-Compliant System
IRQ 182	Microsoft ACPI-Compliant System
IRQ 183	Microsoft ACPI-Compliant System
IRQ 184	Microsoft ACPI-Compliant System
IRQ 185	Microsoft ACPI-Compliant System
IRQ 186	Microsoft ACPI-Compliant System
IRQ 187	Microsoft ACPI-Compliant System
IRQ 188	Microsoft ACPI-Compliant System
IRQ 189	Microsoft ACPI-Compliant System
IRQ 190	Microsoft ACPI-Compliant System
IRQ 4294967294	Intel(R) Ethernet Connection (2) I219-V
IRQ 4294967292	Intel(R) USB 3.0 eXtensible Host Controller
IRQ 4294967293	Intel(R) HD Graphics 510
IRQ 4294967291	Intel(R) Management Engine Interface

Note: These resource information were gathered using Windows 7 (the IRQ could be assigned differently depending on OS)

I/O MAP

I/O MAP	ASSIGNMENT
0x000003F8-0x000003FF	Communications Port (COM1)
0x000002F8-0x000002FF	Communications Port (COM2)
0x000003E8-0x000003EF	Communications Port (COM3)
0x000002E8-0x000002EF	Communications Port (COM4)
0x0000F090-0x0000F097	Standard AHCI 1.0 Serial ATA Controller
0x0000F080-0x0000F083	Standard AHCI 1.0 Serial ATA Controller
0x0000F060-0x0000F07F	Standard AHCI 1.0 Serial ATA Controller
0x000002F0-0x000002F7	Communications Port (COM5)
0x00000000-0x00000CF7	PCI bus
0x00000D00-0x0000FFFF	PCI bus
0x00000070-0x00000077	System CMOS/real time clock
0x00000070-0x00000077	Motherboard resources
0x00000A00-0x00000A0F	Motherboard resources
0x00000A10-0x00000A1F	Motherboard resources
0x00000A20-0x00000A2F	Motherboard resources
0x0000F040-0x0000F05F	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123
0x0000002E-0x0000002F	Motherboard resources
0x0000004E-0x0000004F	Motherboard resources
0x00000061-0x00000061	Motherboard resources
0x00000063-0x00000063	Motherboard resources
0x00000065-0x00000065	Motherboard resources
0x00000067-0x00000067	Motherboard resources
0x00000080-0x00000080	Motherboard resources
0x00000092-0x00000092	Motherboard resources

I/O MAP	ASSIGNMENT
0x000000B2-0x000000B3	Motherboard resources
0x00000680-0x0000069F	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x0000FFFF-0x0000FFFF	Motherboard resources
0x00001800-0x000018FE	Motherboard resources
0x0000164E-0x0000164F	Motherboard resources
0x0000FF00-0x0000FFFE	Motherboard resources
0x00000800-0x0000087F	Motherboard resources
0x00001854-0x00001857	Motherboard resources
0x000000F0-0x000000F0	Numeric data processo
0x0000F000-0x0000F03F	Intel(R) HD Graphics 510
0x000003B0-0x000003BB	Intel(R) HD Graphics 510
0x000003C0-0x000003DF	Intel(R) HD Graphics 510
0x00000060-0x00000060	Standard PS/2 Keyboard
0x00000064-0x00000064	Standard PS/2 Keyboard
0x00000020-0x00000021	Programmable interrupt controller
0x00000024-0x00000025	Programmable interrupt controller
0x00000028-0x00000029	Programmable interrupt controller
0x0000002C-0x0000002D	Programmable interrupt controller
0x00000030-0x00000031	Programmable interrupt controller
0x00000034-0x00000035	Programmable interrupt controller
0x00000038-0x00000039	Programmable interrupt controller
0x0000003C-0x0000003D	Programmable interrupt controller
0x000000A0-0x000000A1	Programmable interrupt controller
0x000000A4-0x000000A5	Programmable interrupt controller

I/O MAP	ASSIGNMENT
0x000000A8-0x000000A9	Programmable interrupt controller
0x000000AC-0x000000AD	Programmable interrupt controller
0x000000B0-0x000000B1	Programmable interrupt controller
0x000000B4-0x000000B5	Programmable interrupt controller
0x000000B8-0x000000B9	Programmable interrupt controller
0x000000BC-0x000000BD	Programmable interrupt controller
0x000004D0-0x000004D1	Programmable interrupt controller
0x00000040-0x00000043	System timer
0x00000050-0x00000053	System timer

Memory MAP

MEMORY MAP	ASSIGNMENT
0xFED00000-0xFED003FF	High precision event timer
0xDF048000-0xDF049FFF	Standard AHCI 1.0 Serial ATA Controller
0xDF04C000-0xDF04C0FF	Standard AHCI 1.0 Serial ATA Controller
0xDF04B000-0xDF04B7FF	Standard AHCI 1.0 Serial ATA Controller
0xDF040000-0xDF043FFF	High Definition Audio Controller
0xDF020000-0xDF02FFFF	High Definition Audio Controller
0xA0000-0xBFFFF	PCI bus
0xA0000-0xBFFFF	Intel(R) HD Graphics 510
0x90000000-0xDFFFFFFF	PCI bus
0xFD000000-0xFE7FFFFF	PCI bus
0xFD000000-0xFE7FFFFF	Motherboard resources
0xDF044000-0xDF047FFF	Intel(R) 100 Series/C230 Series Chipset Family PMC - A121
0xFED10000-0xFED17FFF	Motherboard resources
0xFED18000-0xFED18FFF	Motherboard resources
0xFED19000-0xFED19FFF	Motherboard resources
0xE0000000-0xEFFFFFFF	Motherboard resources
0xFED20000-0xFED3FFFF	Motherboard resources
0xFED90000-0xFED93FFF	Motherboard resources
0xFED45000-0xFED8FFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Motherboard resources
0xFF000000-0xFFFFFFFF	Intel(R) 82802 Firmware Hub Device
0xFEE00000-0xFEEFFFFF	Motherboard resources
0xDFFE0000-0xDFFFFFFF	Motherboard resources
0xDF04A000-0xDF04A0FF	Intel(R) 100 Series/C230 Series Chipset Family SMBus - A123

MEMORY MAP	ASSIGNMENT
0xFDAF0000-0xFDAFFFFFF	Motherboard resources
0xFDAE0000-0xFDAEFFFFFF	Motherboard resources
0xFDAC0000-0xFDACFFFF	Motherboard resources
0xDF000000-0xDF01FFFF	Intel(R) Ethernet Connection I219-V
0xDF030000-0xDF03FFFF	Intel(R) USB 3.0 eXtensible Host Controller
0xFDAD0000-0xFDADFFFF	Motherboard resources
0xFDB00000-0xFDFFFFFFF	Motherboard resources
0xFE000000-0xFE01FFFF	Motherboard resources
0xFE036000-0xFE03BFFF	Motherboard resources
0xFE03D000-0xFE3FFFFFF	Motherboard resources
0xFE410000-0xFE7FFFFFF	Motherboard resources
0xDE000000-0xDEFFFFFF	Intel(R) HD Graphics 510
0xC0000000-0xCFFFFFFF	Intel(R) HD Graphics 510
0xDF04E000-0xDF04EFFF	Intel(R) 100 Series/C230 Series Chipset Family Thermal subsystem - A131
0xFE40F000-0xFE40FFFF	Intel(R) Management Engine Interface

WATCHDOG TIMER CONFIGURATION

The I/O port address of the watchdog timer is 2E (hex) and 2F (hex). 2E (hex) is the address port. 2F (hex) is the data port. User must first assign the address of register by writing address value into address port 2E (hex), then write/read data to/from the assigned register through data port 2F (hex).

Configuration Sequence

To program F81866 configuration registers, the following configuration sequence must be followed:

- (1) Enter the extended function mode.
- (2) Configure the configuration registers.
- (3) Exit the extended function mode.

(1) Enter the extended function mode

To place the chip into the Extended Function Mode, two successive writes of 0x87 must be applied to Extended Function Enable Registers (EFERs, i.e. 2Eh or 4Eh).

(2) Configure the configuration registers

The chip selects the Logical Device and activates the desired Logical Devices through Extended Function Index Register (EFIR) and Extended Function Data Register (EFDR). The EFIR is located at the same address as the EFER, and the EFDR is located at address (EFIR+1). First, write the Logical Device Number (i.e. 0x07) to the EFIR and then write the number of the desired Logical Device to the EFDR. If accessing the Chip (Global) Control Registers, this step is not required. Secondly, write the address of the desired configuration register within the Logical Device to the EFIR and then write (or read) the desired configuration register through the EFDR.

(3) Exit the extended function mode

To exit the Extended Function Mode, writing 0xAA to the EFER is required. Once the chip exits the Extended Function Mode, it is in the normal running mode and is ready to enter the configuration mode.

Code example for watchdog timer

Enable watchdog timer and set timeout interval to 30 seconds.

```
;----- Enter to extended function mode -----  
mov     dx,    2eh  
mov     al,    87h  
out     dx,    al  
out     dx,    al  
;----- Select Logical Device 7 of watchdog timer -----  
mov     al,    07h  
out     dx,    al  
inc     dx  
mov     al,    07h  
out     dx,    al  
;-----Enable Watch dog feature -----  
mov     al,    030h  
out     dx,    al  
inc     dx  
mov     al,    01h  
out     dx,    al  
;----- Enable Watch PME-----  
dec     dx  
mov     al,    0FAh  
out     dx,    al  
inc     dx  
in      al,    dx  
and     al,    51h  
out     dx,    al  
;----- Set second as counting unit -----  
dec     dx  
mov     al,    0F5h  
out     dx,    al  
inc     dx  
in      al,    dx  
and     al,    30h  
out     dx,    al  
;----- Set timeout interval as 30seconds and start counting -----  
dec     dx  
mov     al,    0F6h
```

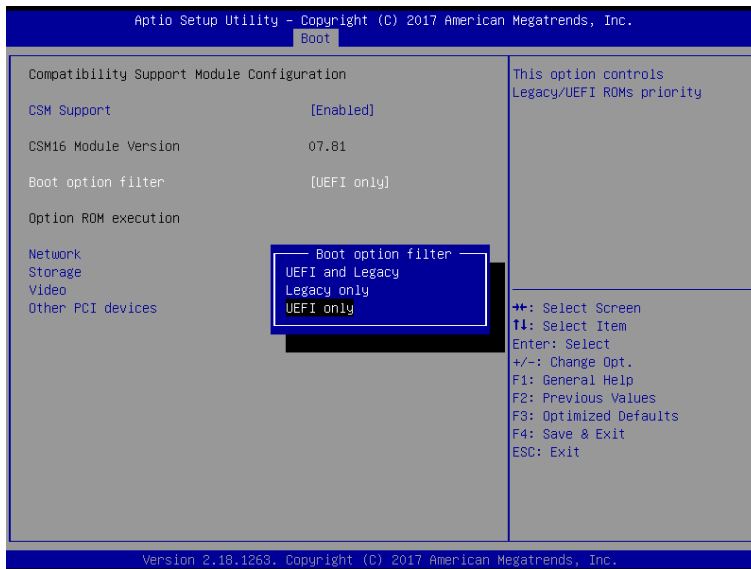


```
out    dx,  al
inc    dx
mov    al,  1Eh
out    dx,  al
;-----Exit the extended function mode -----
dec    dx
mov    al,  0AAh
out    dx,  al
```

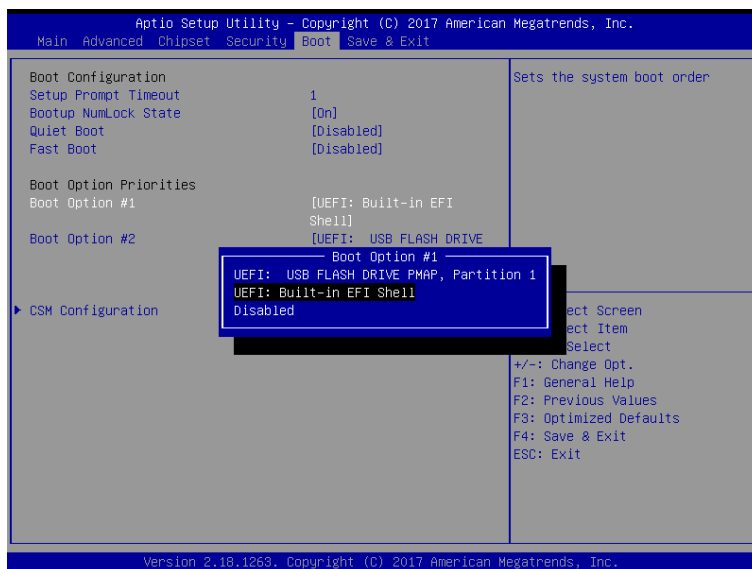
Flash BIOS Update

I. Before System BIOS Update

1. Prepare a USB media (e.g. USB storage device) to save the required files for BIOS update.
2. Download and save the BIOS file (e.g. 69801PH1.bin) to the storage device.
3. Copy AMI flash utility – AFUEFIx64.exe (v5.09.01) into the storage device. The utility and BIOS file should be saved to the same path.
4. Make sure the target system can first boot to the EFI shell environment.
 - (1) Connect the bootable USB device.
 - (2) Turn on the computer and press <ESC> or key during boot to enter BIOS Setup.
 - (3) System will go into the BIOS setup menu.
 - (4) Select **[Boot]** menu and enter into **[CSM Configuration]** menu.
 - (5) Select **[Boot option filter]** to **[UEFI Only]** and press <F4> key to save configuration and restart the system.



- (6) Press <ESC> or to enter into BIOS setup menu again.
- (7) Select [Boot] menu and set [UEFI: Built-in EFI Shell] to be the 1st boot device.
- (8) Press <F4> key to save configuration and restart the system to boot into EFI Shell environment.



II. AFUEFIx64 Command for System BIOS Update

AFUEFIx64.efi is the AMI firmware update utility; the command line is shown as below:

AFUEFIx64 <ROM File Name> [option1] [option2]....

You can type “**AFUEFIx64 /?**” to see all the definition of each control options. The recommended options for BIOS ROM update include following parameters:

/P: Program main BIOS image

/B: Program Boot Block

/N: Program NVRAM

/X: Do not check ROM ID

II. BIOS Update Procedure

1. Boot into EFI Shell, change to the path where you put BIOS image and AFUEFIx64.

```
Shell> fs0:  
fs0:\> cd afuefix64
```

2. Type "**AFUEFIx64 6980xxxx.bin /p /b /n /x**" and press enter to start the flash procedure.
(Note that **xxxx** means the BIOS revision part, e.g. 1PH1...)
3. During the update procedure, you will see the BIOS update process status and its percentage. Beware! Do not turn off system power or reset your computer if the whole procedure are not complete yet, or it may crash the BIOS ROM and make system unable to boot up next time.
4. After BIOS update procedures is completed, the following messages will be shown as follows:

```
fs0:\afuefix64> afuefix64 69801PH1.bin /p /b /n /x  
+-----+  
|                AMI Firmware Update Utility v5.09.01.1317                |  
|      Copyright (C) 2017 American Megatrends Inc. All Rights Reserved.      |  
+-----+  
Reading flash ..... done  
- ME Data Size Checking . ok  
- FFS checksums ..... ok  
- Check RomLayout ..... Ok.  
Erasing Boot Block ..... done  
Updating Boot Block ..... done  
Verifying Boot Block ..... done  
Erasing Main Block ..... done  
Updating Main Block ..... done  
Verifying Main Block ..... done  
Erasing NVRAM Block ..... done  
Updating NVRAM Block ..... done  
Verifying NVRAM Block ..... done  
fs0:\afuefix64> _
```

5. Restart the system and boot up the system with new BIOS now.
6. The BIOS Update procedure is completed after the system is restarted.
7. Reboot the system and verify if the BIOS version shown on the initialization screen has been updated.



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Version 2.18.1263. Copyright (C) 2017 American Megatrends, Inc.
BIOS Date: 03/31/2017 10:11:41 Ver: 69801PH1
Press or <ESC> to enter setup.